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Vishay Dale

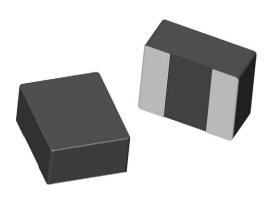
RoHS

HALOGEN FREE

GREEN

(5-2008)

Commercial Power Inductor, Low DCR



LINKS TO ADDITIONAL RESOURCES



FEATURES

- 2.5 mm x 2.0 mm x 1.2 mm SMD package
- Handles high transient current spikes without saturation
- Magnetically shielded composite construction
- Bottom plated terminals allow for a smaller pad layout for compact board spacing
- Packaging information: SMD packaging
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

- SSD modules
- DC/DC converter for CPU
- · Noise suppression and filtering
- · Data networking and storage systems

STANDARD ELECTRICAL SPECIFICATIONS								
	L ₀ INDUCTANCE ± 20 % AT 0 A	DCR TYP. 25 °C	DCR MAX. 25 °C	HEAT RATING CURRENT DC TYP.	SATURATION CURRENT DC TYP. (A)			
PART NUMBER	(μH)	$(m\Omega)$	$(m\Omega)$	(A) ⁽¹⁾	20 % DROP (2)	30 % DROP (3)		
IHLL1008ABEZR33M1Z	0.33	14.0	19.0	6.0	7.3	8.5		
IHLL1008ABEZR47M1Z	0.47	17.0	21.0	6.1	6.0	7.3		
IHLL1008ABEZR68M1Z	0.68	25.0	30.0	5.5	5.6	6.3		
IHLL1008ABEZ1R0M1Z	1.0	35.0	42.0	4.2	3.7	5.0		
IHLL1008ABEZ1R5M1Z	1.5	53.0	61.0	3.6	3.0	3.6		
IHLL1008ABEZ2R2M1Z	2.2	68.0	82.0	3.0	3.0	3.3		
IHLL1008ABEZ3R3M1Z	3.3	110.0	135.0	2.1	2.0	2.8		
IHLL1008ABEZ4R7M1Z	4.7	160.0	190.0	1.8	1.8	2.4		

Notes

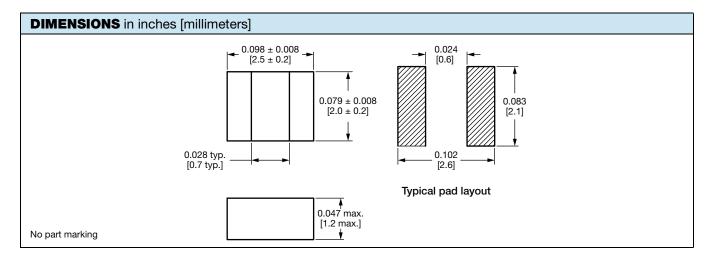
- All test data is referenced to 25 °C ambient
- Test condition: 1 MHz, 1 V
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component
 placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be
 verified in the end application
- $^{(1)}\,$ DC current (A) that will cause an approximate ΔT of 40 $^{\circ}C$
- (2) DC current (A) that will cause L₀ to drop approximately 20 %
- (3) DC current (A) that will cause L₀ to drop approximately 30 %

PATENT(S): www.vishay.com/patents

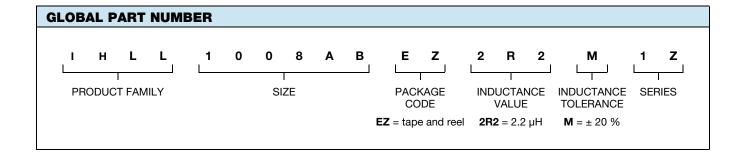
Revision: 16-Apr-2024

This Vishay product is protected by one or more United States and international patents.

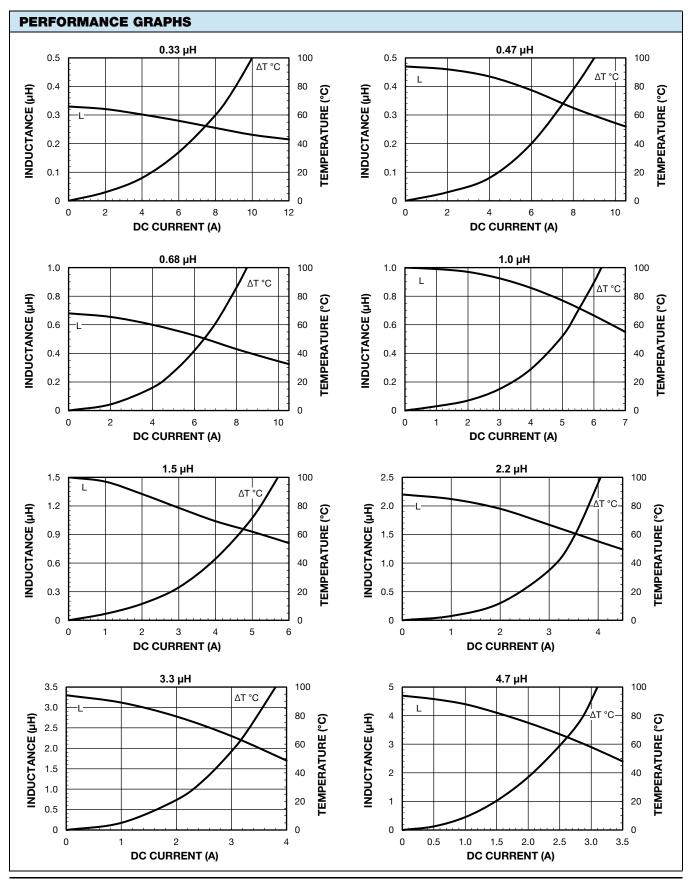
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DESCRIPTION				
IHLL-1008AB-1Z	2.2 μΗ	± 20 %	EZ	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD









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Vishay

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