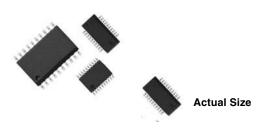


VTSRC20-AA, VSSRC20-AA, VSORC20-AA

Vishay Dale Thin Film

25 mil or 50 mil Pitch, T-Filter Thin Film Surface Mount Resistor/Capacitor Network

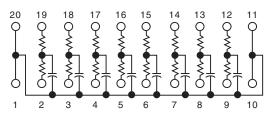


Small outline, surface mount, EMI/RFI reduction

Vishay Thin Film's T-filter network is an integrated thin film network on a single die. Noise suppression is at a maximum with the use of thin film technology. The T-filter network, schematic AA is designed to suppress EMI/RFI noise with such applications as I/O ports of personal computers and peripherals, workstations and local area networks. With a rugged molded case to protect the circuit from the environment and an integrated thin film network this product is your choice when reduced size, improved accuracy and surface mount capability are your goals.

Available packages SOIC, SSOP and TSSOP.

SCHEMATIC AA



FEATURES

- Resistors and capacitors on a single chip
- · Saves board space
- Reduces total assembly costs
- Uniform performance characteristics
- UL 94 V-0 flame resistant
- Rugged, molded case construction
- VTSRC JEDEC M0-153AC
 VSSRC JEDEC M0-137AD
 VSORC JEDEC MS-013AC
- Compliant to RoHS Directive 2002/95/EC

TYPICAL PERFORMANCE

	TCR	TOLERANCE		
RESISTOR	200	10 %		
	тсс	TOLERANCE		
CAPACITOR	200	20 %		

STANDARD VALUES					
	MODELS	P (0)			
VSORC	VSSRC	VTSRC	R (Ω)	C (pF)	
	Х		10	100	
	Х		25	200	
Х			100	390	

STANDARD ELECTRICAL SPECIFICATIONS					
TEST	SPECIFICATIONS	CONDITIONS			
Material	Tantalum nitride on silicon	-			
Pin/Lead Number	20	-			
Resistance Range	10 Ω to 750 Ω	-			
TCR: Absolute	± 200 ppm/°C	0 °C to + 70 °C			
TCR: Tracking	± 10 ppm/°C	-			
Tolerance: Absolute	\pm 10 % standard (R), \pm 20 % standard (C)	At 1 MHz and V _{RMS} over + 10 °C to + 70 °C			
Power Rating: Resistor	100 mW	-			
Power Rating: Package	(T)SSOP: 1 W, SOIC: 1.2 W	See derating curve			
Stability: Ratio	± 2 %	1000 h			
Operating Temperature Range	0 °C to + 70 °C	-			
Storage Temperature Range	- 55 °C to + 125 °C	-			
Capacitance Range	TSSOP: 10 pF to 150 pF, SOIC/SSOP: 10 pF to 250 pF	-			
ESD Protection	> 2 kV	MIL-STD-883, method 3015			
Breakdown Voltage	35 V to 50 V	-			

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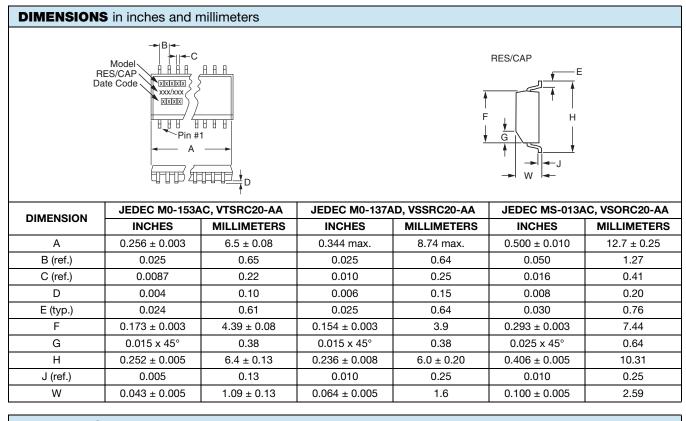
COMPLIANT

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VTSRC20-AA, VSSRC20-AA, VSORC20-AA

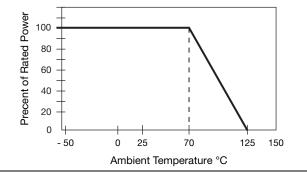
Vishay Dale Thin Film



IMPRINTING					
VSORC, VSSRC, VTSRC	20	AA	XXX	1	XXX
MODEL	PIN COUNT	SCHEMATIC	RESISTANCE Code: e.g. 100 = 10 W	/	CAPACITANCE Code: e.g. 101 = 100 pF
		XXXX			
		Date code	Opti	onal ma	rking

MECHANICAL SPECIFICATIONS					
Resistive Element	Tantalum nitride				
Substrate Material	Silicon				
Body	Molded epoxy				
Terminals	Copper alloy				
Plating	100 % matte Sn				
Lead Coplanarity	0.0005"				
Marking Resistance to Solvents	Permanency testing per MIL-STD-202, method 215				

DERATING CURVE



PACKING INFORMATION						
MODEL	LEADS	TAPE AND REEL	TUBES			
JEDEC M0-153AC, VTSRC (TSSOP)	20	2500	74			
JEDEC M0-137AD, VSSRC (SSOP)	20	2500	55			
JEDEC MS-013AC, VSORC (SOIC)	20	1000	38			

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Vishay Dale Thin Film

GLOBAL PART NUMBER INFORMATION							
New Global Part Numbering: VTSRC20AA330470TF							
V T S R C 2 0 A A 3 3 0 4 7 0 T						0 T F	
GLOBAL MOD	EL		IBER OF LEADS/ CHEMATICS	RESISTANCE AND TO CAPACITANCE AND TO		F	PACKAGING
VTSRC VSSRC			20AA	хххууу		UF = TUBED	
VSORC		First 2 digits are significant figures. Last digit specifies number of zeros to follow.			TAPE AND REEL TF = Full reels		
				K = 10 % resistance M = 20 % capacitor			
Historical Part Number example: VTSRC20AA330K470MT/R (for reference purposes only)							
VTSRC	20	20 AA		330K	470	м	T/R
MODEL	NUM OF LE		SCHEMATIC	RESISTANCE	TOLERA	ANCE	PACKAGING



Vishay

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