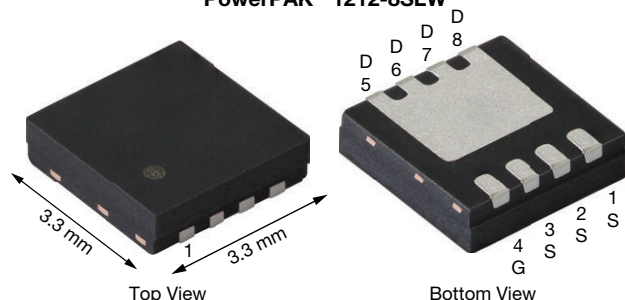


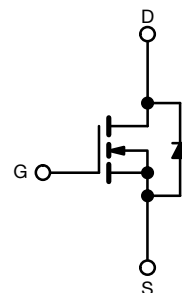
Automotive N-Channel 72 V (D-S) 175 °C MOSFET

PowerPAK® 1212-8SLW

Marking code: Q079

PRODUCT SUMMARY	
V_{DS} (V)	72
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.0095
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.0120
I_D (A) ^a	54
Configuration	Single

FEATURES

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Wettable flank terminals
- Low thermal resistance with 0.75 mm profile
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE


N-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK® 1212-8SLW
Lead (Pb)-free and halogen-free	SQS178ELNW (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V_{DS}	72	V	
Gate-source voltage	V_{GS}	± 20		
Continuous drain current ^a	$T_C = 25$ °C $T_C = 125$ °C	I_D	54	A
			31	
Continuous source current (diode conduction) ^a		I_S	59	
Pulsed drain current ^{a, b}		I_{DM}	115	
Single pulse avalanche current	$L = 0.1$ mH	I_{AS}	21	mJ
Single pulse avalanche energy		E_{AS}	22	
Maximum power dissipation ^{a, b}	$T_C = 25$ °C	P_D	65	W
	$T_C = 125$ °C		21	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C	
Soldering recommendations (peak temperature) ^c		260		

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient	R_{thJA}	54	°C/W	
Junction-to-case (drain) ^e	R_{thJC}	2.3		

Notes

- Values based on R_{thJC} and T_C of 25 °C. Actual values achievable will be dependent on the thermal characteristics of the complete system
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
- See solder profile (www.vishay.com/doc?73257). A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- When mounted on 1" square PCB (FR4 material)
- As per on JESD51-14

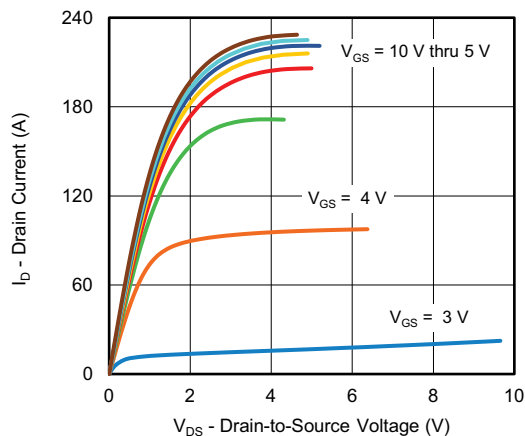
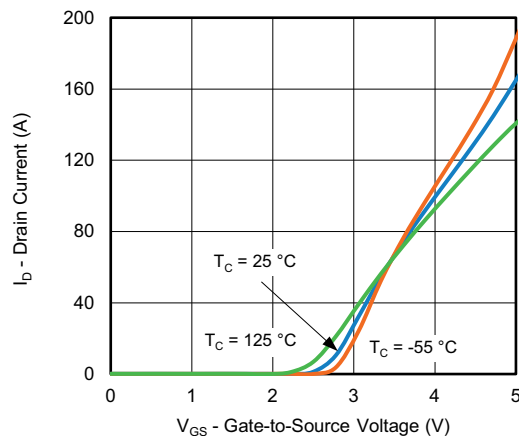
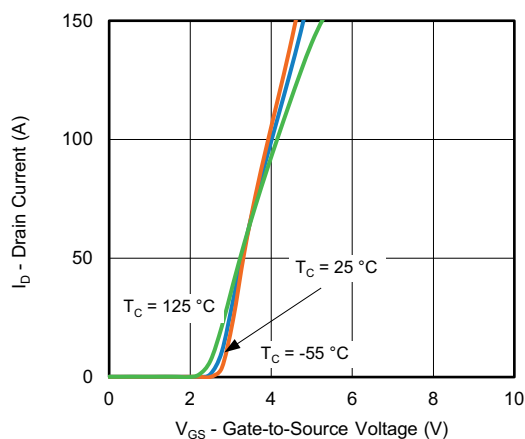
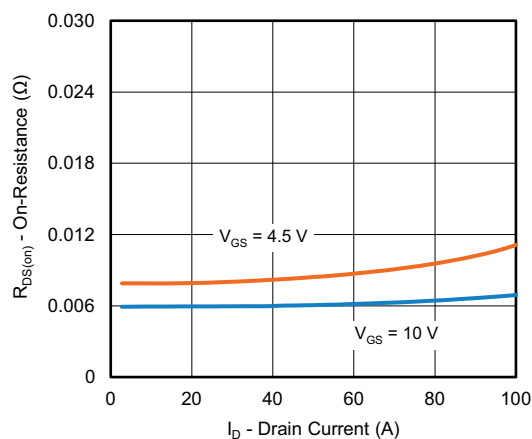
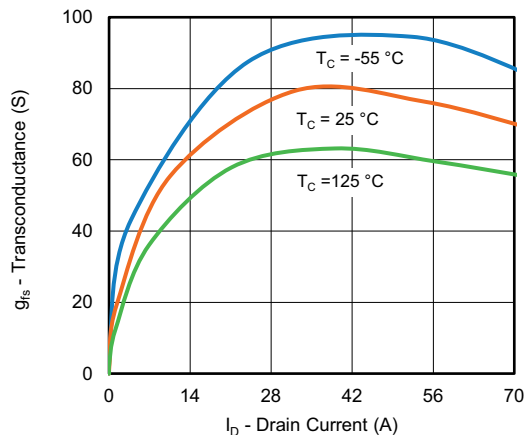
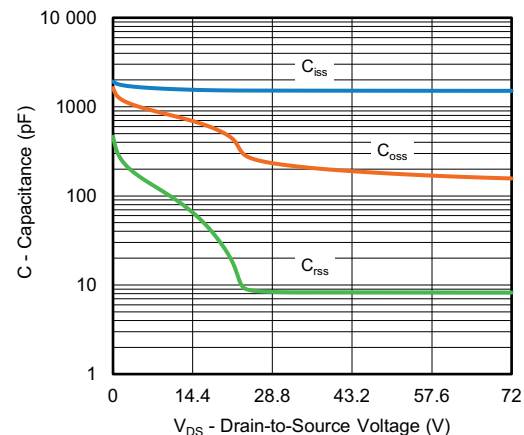


SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		72	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		1.2	1.7	2.5	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 72 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 72 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 72 V, T _J = 175 °C	-	-	150	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	20	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V	I _D = 10 A	-	0.0095	0.012	Ω
		V _{GS} = 10 V		-	0.007	0.0095	
		V _{GS} = 10 V	I _D = 10 A, T _J = 125 °C	-	-	0.0178	
		V _{GS} = 10 V	I _D = 10 A, T _J = 175 °C	-	-	0.0220	
Forward transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 55 A		-	76	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 25 V, f = 1 MHz	-	1522	2131	pF
Output capacitance	C _{oss}			-	264	370	
Reverse transfer capacitance	C _{rss}			-	9	15	
Total gate charge ^c	Q _g	V _{GS} = 10 V	V _{DS} = 36 V, I _D = 4 A	-	26	39	nC
Gate-source charge ^c	Q _{gs}			-	5	-	
Gate-drain charge ^c	Q _{gd}			-	5	-	
Gate resistance	R _g	f = 1 MHz		0.3	1.0	1.6	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = 36 V, R _L = 14.4 Ω, I _D ≅ 2.5 A, V _{GEN} = 10 V, R _g = 1 Ω		-	10	15	ns
Rise time ^c	t _r			-	3	6	
Turn-off delay time ^c	t _{d(off)}			-	23	35	
Fall time ^c	t _f			-	9	14	
Source-Drain Diode Ratings and Characteristic ^b							
Pulsed current ^a	I _{SM}			-	-	115	A
Forward voltage	V _{SD}	I _F = 10 A, V _{GS} = 0 V		-	0.82	1.1	V
Body diode reverse recovery time	t _{rr}	V _{DD} = 58 V, I _{FM} = 3.5 A, di/dt = 100 A/μs, R = 10 Ω, L = 0.3 mH, pulse width = 2 μs		-	30	60	ns
Body diode reverse recovery charge	Q _{rr}			-	23	46	nC
Reverse recovery fall time	t _a			-	15	-	ns
Reverse recovery rise time	t _b			-	15	-	
Body diode peak reverse recovery current	I _{RM(REC)}					-	-1.3

Notes

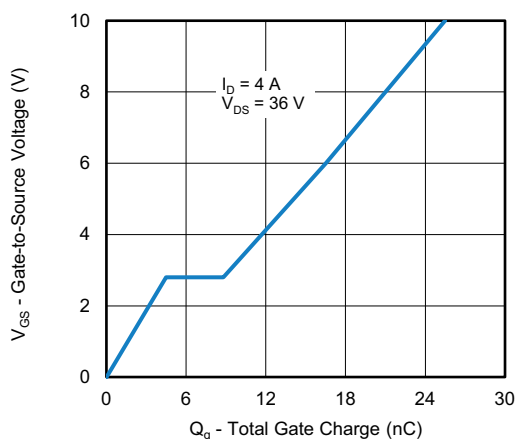
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

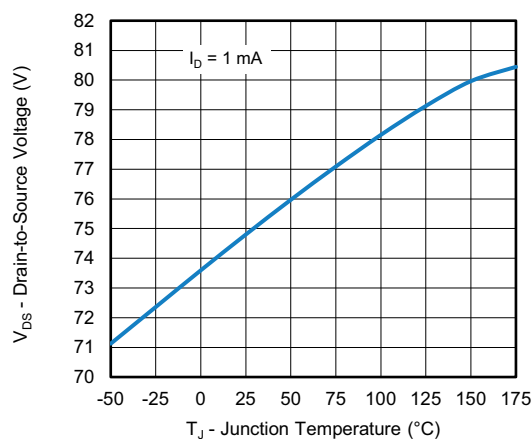
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Transconductance

Capacitance



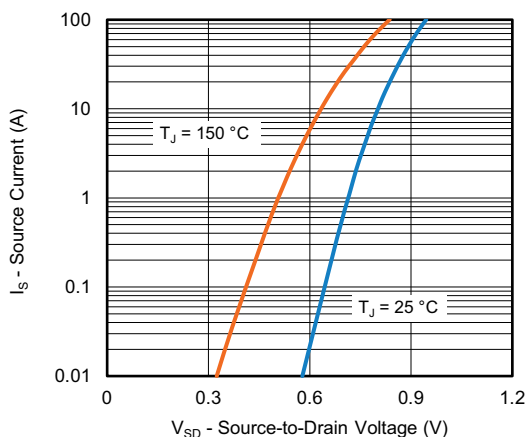
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



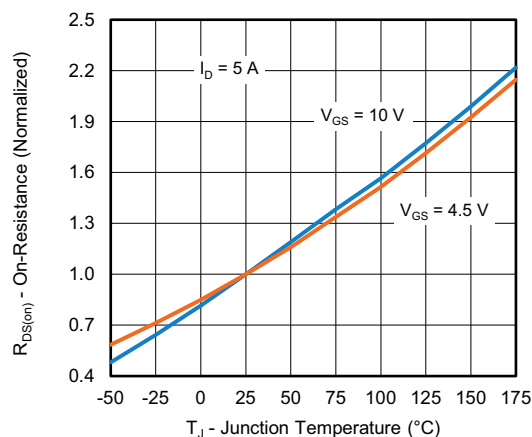
Gate Charge



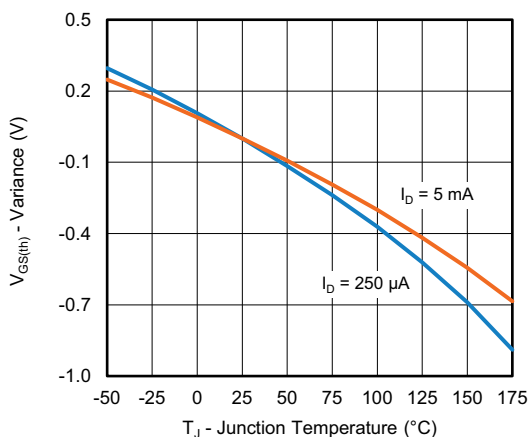
Drain Source Breakdown vs. Junction Temperature



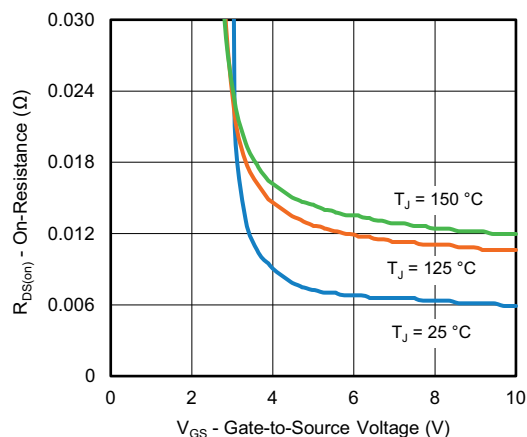
Source Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



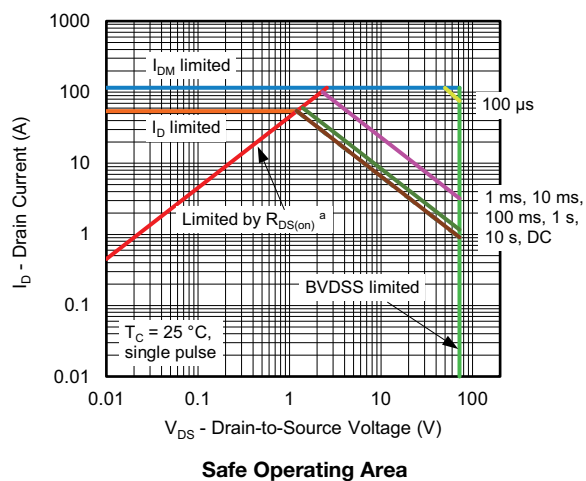
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

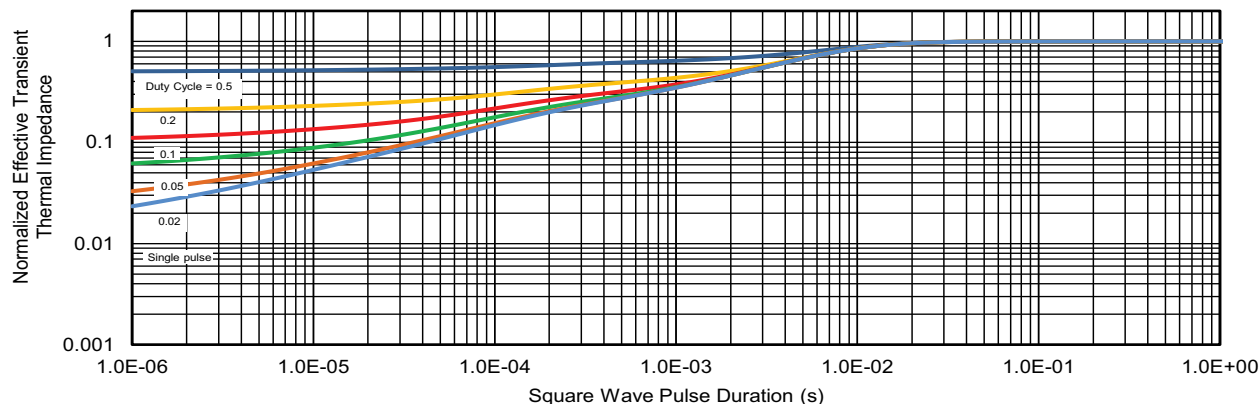


THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

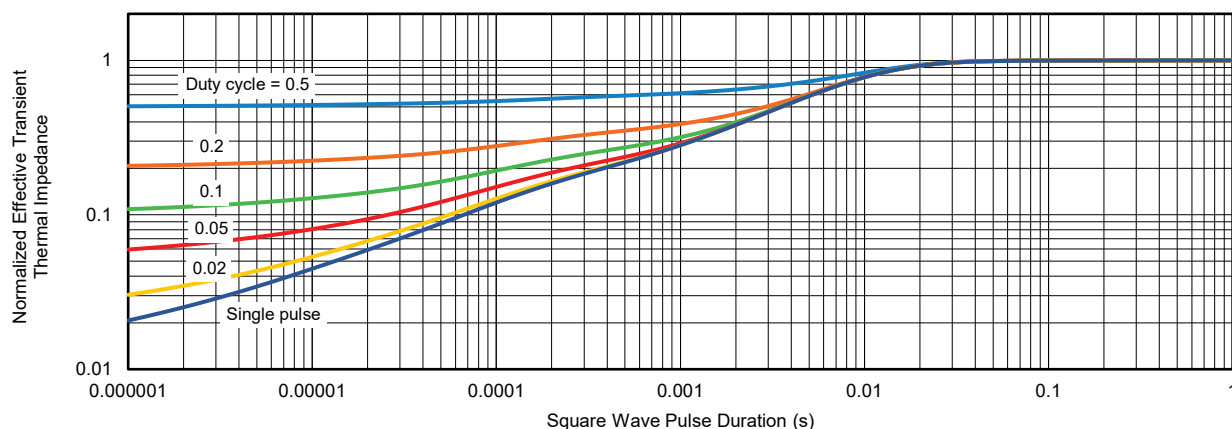


Note

a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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