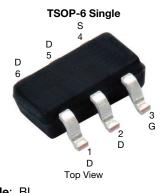


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Vishay Siliconix

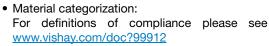
P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (Ω) MAX.	I _D (A) ^d	Q _g (TYP.)			
-60	0.089 at V _{GS} = -10 V	-5.1	10.1 nC			
-00	0.146 at V _{GS} = -4.5 V	-4	10.1110			



FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested

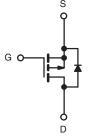




HALOGEN FREE

APPLICATIONS

- · Load switches
- DC/DC converter



P-Channel MOSFET

Marking Code: BL **Ordering Information:**

Si3127DV-T1-GE3 (lead (Pb)-free and halogen-free)

ABSOLUTE MAXIMUM RATINGS $(T_A = 2)$,			1
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	-60		
Gate-Source Voltage		V_{GS}	± 20	v
	T _C = 25 °C		-5.1	
Continuous Dusin Comment /T 150 °C)	T _C = 70 °C	1. [-4.1	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	-3.5 ^{a,b}	
	T _A = 70 °C		-2.8 ^{a,b}	
Pulsed Drain Current (t = 100 μs)		I _{DM}	-20	Α
Continuous Course Dunie Die de Course	T _C = 25 °C		-3.5	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	-1.7 ^{a,b}	
Avalanche Current	1 0411	I _{AS}	-15	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	11.25	mJ
	T _C = 25 °C		4.2	
Mariana Barra Biada atian	T _C = 70 °C	1 , [2.7	w
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^{a,b}	VV
	T _A = 70 °C	1 -	1.3 ^{a,b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient a,c	t ≤ 10 s	R _{thJA}	40	62.5	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	25	30	C/ VV	

Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 110 °C/W.
- d. Based on $T_C = 25$ °C.



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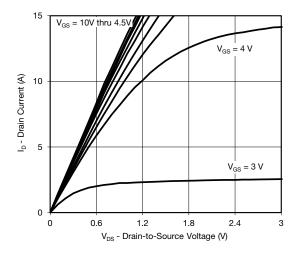
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	V		
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$ $I_D = -250 \mu A$		-	-6.7	=	mV/°C		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 - 200 μ. (-	4.3	-	11107 0		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	-1	ı	-3	V		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	ı	± 100	nA		
Zero Gate Voltage Drain Current	leas	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μΑ		
Zero date voltage Drain Gunerit	I _{DSS}	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$	-	-	-5			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	-30	-	-	Α		
Drain-Source On-State Resistance a	В	V _{GS} = -10 V, I _D = -3.5 A	-	0.074	0.089			
Drain-Source On-State nesistance ~	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$	-	0.095	0.146	Ω		
Forward Transconductance a	9 _{fs}	$V_{DS} = -30 \text{ V}, I_D = -3.5 \text{ A}$	-	11	-	S		
Dynamic ^b					'			
Input Capacitance	C _{iss}		-	832	-	pF		
Output Capacitance	C _{oss}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	88	-			
Reverse Transfer Capacitance	C _{rss}		-	63	-			
Talal Oals Obassa	l l	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -3.5 \text{ A}$	-	20	30	nC		
Total Gate Charge			-	10.1	15.2			
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.5 \text{ A}$	-	3.3	-			
Gate-Drain Charge	Q _{gd}		-	3.9	-			
Gate Resistance	R _g	f = 1 MHz	1.8	9	18	Ω		
Turn-On Delay Time	t _{d(on)}		-	8	16			
Rise Time	t _r	$V_{DD} = -30 \text{ V}, R_1 = 10.7 \Omega$	-	6	12	- -		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -2.8 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	35	53			
Fall Time	t _f		-	16	24			
Turn-On Delay Time	t _{d(on)}		-	40	60	ns		
Rise Time	t _r	$V_{DD} = -30 \text{ V}, R_1 = 10.7 \Omega$	-	28	42	-		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -2.8 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	31	47			
Fall Time	t _f		-	15	23			
Drain-Source Body Diode Characterist	ics				l			
Continous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	-3.5			
Pulse Diode Forward Current (t = 100 µs)	I _{SM}		-	-	-20	A		
Body Diode Voltage	V _{SD}	I _S = -2.8 A, V _{GS} = 0 V	-	-0.85	-1.2	V		
Body Diode Reverse Recovery Time	t _{rr}		-	32	48	ns		
Body Diode Reverse Recovery Charge	Q _{rr}			45	68	nC		
Reverse Recovery Fall Time	t _a	$T_{J} = 25 ^{\circ}\text{C}$	-	24	-	1		
Reverse Recovery Rise Time	t _b	- · · · · · · · · · · · · · · · · · · ·		8		ns		

Notes

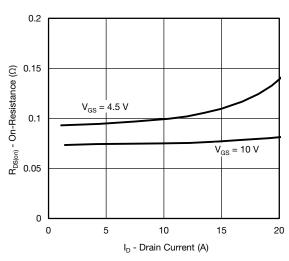
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

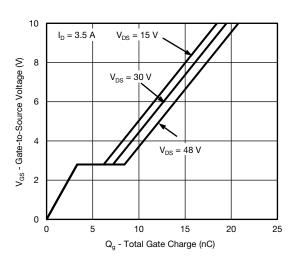




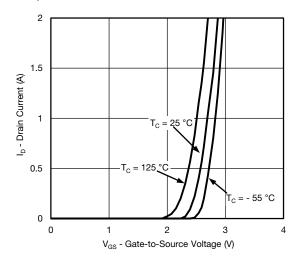
Output Characteristics



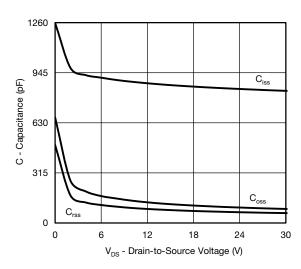
On-Resistance vs. Drain Current



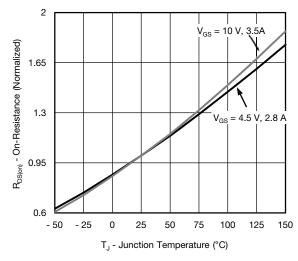
Gate Charge



Transfer Characteristics

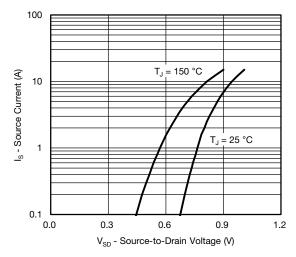


Capacitance

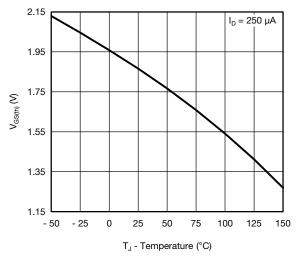


On-Resistance vs. Junction Temperature

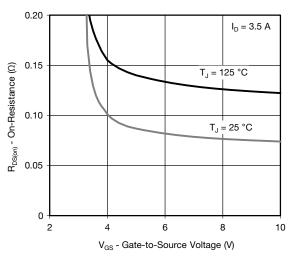




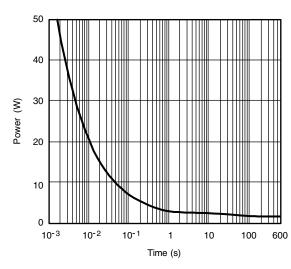
Source-Drain Diode Forward Voltage



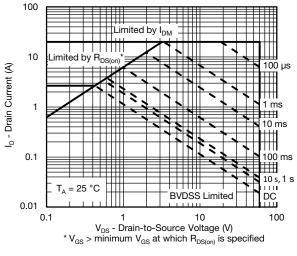
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

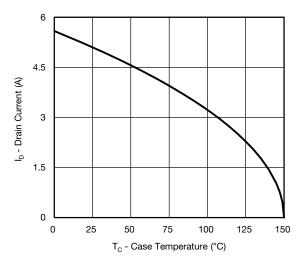


Single Pulse Power, Junction-to-Ambient

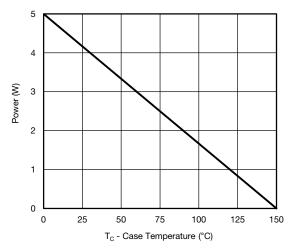


Safe Operating Area

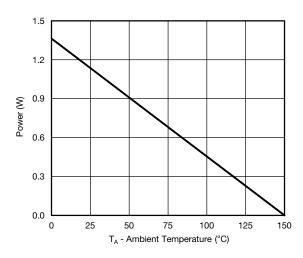




Current Derating*





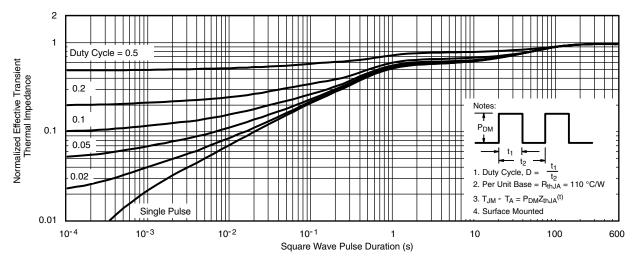


Power Derating, Junction-to-Ambient

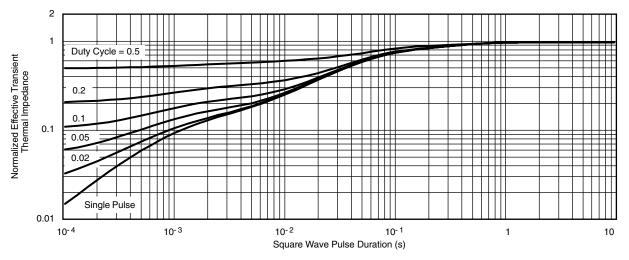
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^{*} The power dissipation P_D is based on $T_{J \text{ (max.)}} = 150 \,^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

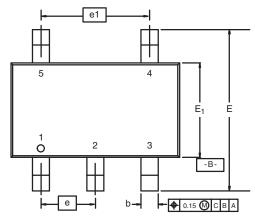
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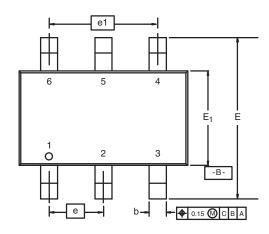




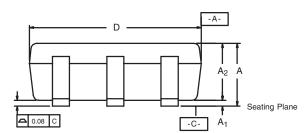
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

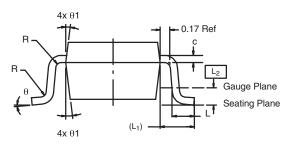




5-LEAD TSOP





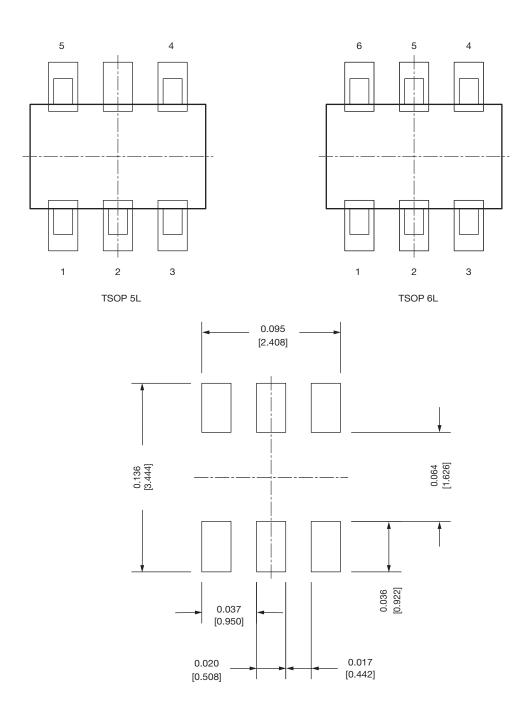


	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.008		
D	2.95	3.05	3.10	0.116 0.120		0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.079		
L	0.32	-	0.50	0.012	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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