



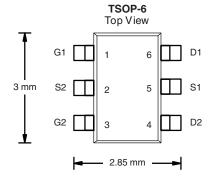
Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.105 at V _{GS} = 10 V	± 2.5		
	$0.175 \text{ at V}_{GS} = 4.5 \text{ V}$	± 2.0		

FEATURES

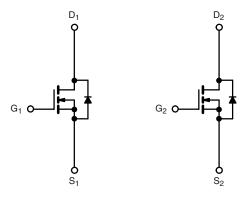
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3948DV-T1-E3 (Lead (Pb)-free)

Si3948DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T,I = 150 °C) ^{a, b}	T _A = 25 °C	- I _D	± 2.5	A	
Continuous Drain Current (1 _J = 150 °C)	T _A = 70 °C		± 2.0		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	± 8	A	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	1.05		
a b	T _A = 25 °C	- P _D	1.15	W	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C] ' ^D	0.73		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian de Ambienta	t ≤ 5 s	- R _{thJA} R _{thJL}	93	110	°C/W
Maximum Junction-to-Ambient ^a	Steady State		130	150	
Maximum Junction-to-Lead	Steady State		75	90	

Notes:

a. Surface Mounted on FR4 board.

 $b.\ t \leq 5\ s.$

Si3948DV

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SPECIFICATIONS $T_J = 25 ^{\circ}\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	1 .	V _{DS} = 24 V, V _{GS} = 0 V			1		
	I _{DSS}	V_{DS} = 24 V, V_{GS} = 0 V, T_J = 55 °C			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	5			Α	
	В	V _{GS} = 10 V, I _D = 2.5 A		0.085	0.105	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.0 \text{ A}$		0.140	0.175	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 2.5 \text{ A}$		4.3		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 1.05 A, V _{GS} = 0 V		0.81	1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			2.1	3.2		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 5.0 \text{ V}, I_{D} = 1.8 \text{ A}$		0.7		nC	
Gate-Drain Charge	Q_{gd}			0.7			
Turn-On Delay Time	t _{d(on)}			7	11		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		9	14		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_G = 6 Ω		13	20	ns	
Fall Time	t _f			5	8		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.05 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		35	60		

Notes:

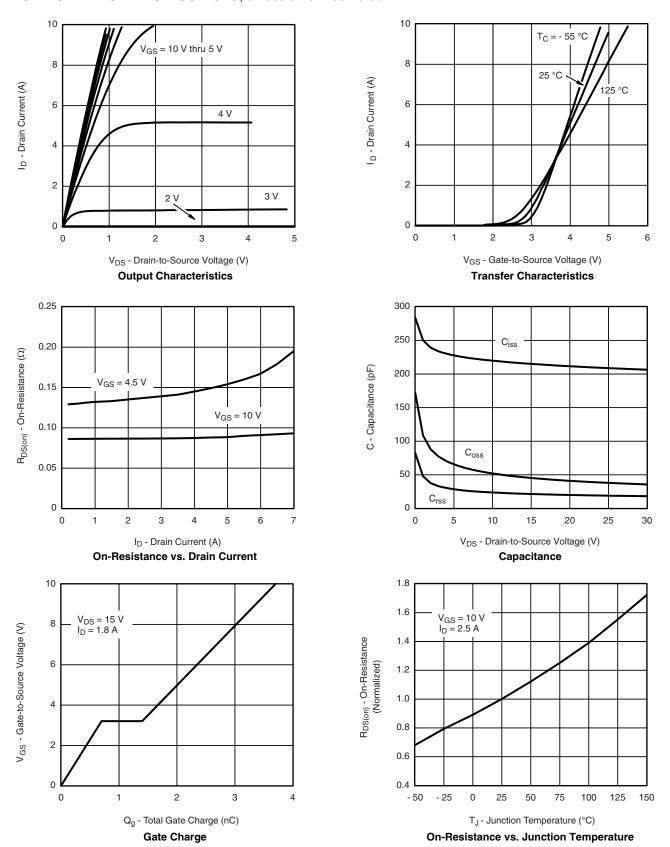
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.



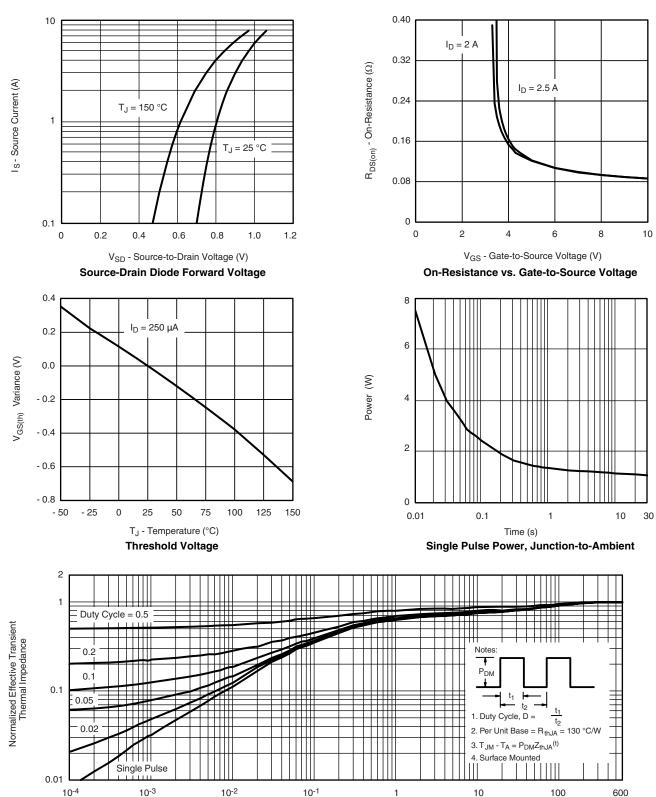
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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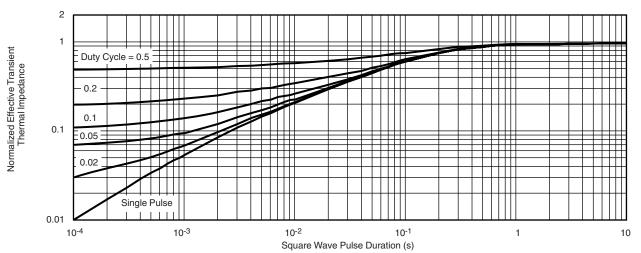
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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Normalized Thermal Transient Impedance, Junction-to-Foot

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