



# P-Channel 1.5 V (G-S) MOSFET

| PRODUCT SUMMARY     |                                    |                    |                       |  |  |  |
|---------------------|------------------------------------|--------------------|-----------------------|--|--|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$               | I <sub>D</sub> (A) | Q <sub>g</sub> (Typ.) |  |  |  |
|                     | 0.023 at $V_{GS} = -4.5 \text{ V}$ | - 7                |                       |  |  |  |
| - 8                 | 0.029 at V <sub>GS</sub> = - 2.5 V | - 6.2              | 28                    |  |  |  |
|                     | 0.036 at V <sub>GS</sub> = - 1.8 V | - 5.2              | 20                    |  |  |  |
|                     | 0.048 at V <sub>GS</sub> = - 1.5 V | - 5                |                       |  |  |  |

|                     |  | ı                  |                       |
|---------------------|--|--------------------|-----------------------|
| V <sub>DS</sub> (V) | $R_{DS(on)}\left(\Omega\right)$            | I <sub>D</sub> (A) | Q <sub>g</sub> (Typ.) |
|                     | $0.023$ at $V_{GS} = -4.5 \text{ V}$       | - 7                |                       |
| - 8                 | $0.029 \text{ at V}_{GS} = -2.5 \text{ V}$ | - 6.2              | 28                    |
| - 0                 | 0.036 at V <sub>GS</sub> = - 1.8 V         | - 5.2              | 20                    |
|                     | 0.048 at V <sub>GS</sub> = - 1.5 V         | - 5                |                       |
|                     |  |                    |                       |
|                     |  |                    |                       |

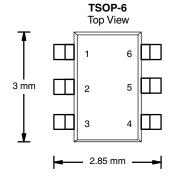
### **FEATURES**

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET: 1.5 V Rated
- Ultra-Low On-Resistance
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



### **APPLICATIONS**

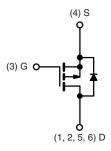
· Load Switch for Portable Devices



Ordering Information: Si3499DV-T1-E3 (Lead (Pb)-free)

Si3499DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 99xxx



P-Channel MOSFET

| <b>ABSOLUTE MAXIMUM RATINGS</b>                                 | (T <sub>A</sub> = 25 °C, unle | ess otherwise                     | noted) |              |      |
|---|-------------------------------|-----------------------------------|--------|--------------|------|
| Parameter   |                               | Symbol                            | 5 s    | Steady State | Unit |
| Drain-Source Voltage  |                               | V <sub>DS</sub>                   | - 8    |              | V    |
| Gate-Source Voltage   |                               | V <sub>GS</sub>                   | ± 5    |              |      |
| Continuous Dunin Courset /T 450 °C\8                            | T <sub>A</sub> = 25 °C        | I <sub>D</sub>                    | - 7    | - 5.3        |      |
| Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup> | T <sub>A</sub> = 85 °C        |                                   | - 3.6  | - 3.9        |      |
| Pulsed Drain Current  |                               | I <sub>DM</sub>                   | - 20   |              | Α    |
| Continuous Source Current (Diode Conduction) <sup>a</sup>       |                               | I <sub>S</sub>                    | - 1.7  | - 0.9        |      |
|   | T <sub>A</sub> = 25 °C        | D                                 | 2      | 1.1          | W    |
| Maximum Power Dissipation <sup>a</sup>                          | T <sub>A</sub> = 85 °C        | $P_{D}$                           | 1      | 0.6          | VV   |
| Operating Junction and Storage Temperature Range                |                               | T <sub>J</sub> , T <sub>stg</sub> | - 55   | to 150       | °C   |

| THERMAL RESISTANCE RATINGS               |              |            |         |         |      |  |
|--|--------------|------------|---------|---------|------|--|
| Parameter                                |              | Symbol     | Typical | Maximum | Unit |  |
| Marrian una lumation ta Analismati       | t ≤ 5 s      | $R_{thJA}$ | 45      | 62.5    |      |  |
| Maximum Junction-to-Ambient <sup>a</sup> | Steady State | ' 'thJA    | 90      | 110     | °C/W |  |
| Maximum Junction-to-Foot (Drain)         | Steady State | $R_{thJF}$ | 25      | 30      |      |  |

a. Surface mounted on 1" x 1" FR4 board.

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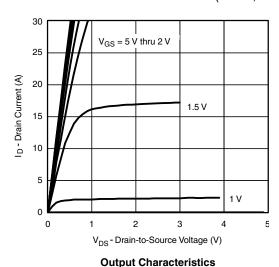
| <b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted) |                     |  |        |        |        |      |  |
|--|---------------------|--|--------|--------|--------|------|--|
| Parameter  | Symbol              | Test Conditions  | Min.   | Тур.   | Max.   | Unit |  |
| Static   |                     |  |        |        |        |      |  |
| Gate Threshold Voltage   | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$  | - 0.35 |        | - 0.75 | V    |  |
| Gate-Body Leakage  | I <sub>GSS</sub>    | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$   |        |        | ± 100  | nA   |  |
| Zara Cata Valtaga Drain Current  | 1                   | V <sub>DS</sub> = - 8 V, V <sub>GS</sub> = 0 V<br>V <sub>DS</sub> = - 8 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C |        |        | - 1    | μΑ   |  |
| Zero Gate Voltage Drain Current  | I <sub>DSS</sub>    |  |        |        | - 10   |      |  |
| On-State Drain Current <sup>a</sup>                                    | I <sub>D(on)</sub>  | V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V   | - 20   |        |        | Α    |  |
|  |                     | V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7 A  |        | 0.019  | 0.023  |      |  |
| Due to Course On Otata Bastatana a                                     | B                   | $V_{GS} = -2.5 \text{ V}, I_D = -6.2 \text{ A}$  |        | 0.024  | 0.029  | Ω    |  |
| Drain-Source On-State Resistance <sup>a</sup>                          | R <sub>DS(on)</sub> | V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 5.2 A  |        | 0.028  | 0.036  | - 52 |  |
|  |                     | V <sub>GS</sub> = - 1.5 V, I <sub>D</sub> = - 3 A  |        | 0.035  | 0.048  |      |  |
| Forward Transconductance <sup>a</sup>                                  | 9 <sub>fs</sub>     | V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 7 A  |        | 28     |        | S    |  |
| Diode Forward Voltage <sup>a</sup>                                     | $V_{SD}$            | $I_S = -1.7 A$ , $V_{GS} = 0 V$  |        | - 0.63 | - 1.1  | V    |  |
| Dynamic <sup>b</sup>   |                     |  |        |        |        |      |  |
| Total Gate Charge  | Qg                  |  |        | 28     | 42     |      |  |
| Gate-Source Charge   | Q <sub>gs</sub>     | $V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7 \text{ A}$   |        | 2.9    |        | nC   |  |
| Gate-Drain Charge  | Q <sub>gd</sub>     |  |        | 5.8    |        |      |  |
| Gate Resistance  | $R_g$               |  | 4      | 8.5    | 13     | Ω    |  |
| Turn-On Delay Time   | t <sub>d(on)</sub>  |  |        | 27     | 40     |      |  |
| Rise Time  | t <sub>r</sub>      | $V_{DD}$ = - 4 V, $R_L$ = 4 $\Omega$   |        | 65     | 100    |      |  |
| Turn-Off Delay Time  | t <sub>d(off)</sub> | $I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$   |        | 210    | 315    | ns   |  |
| Fall Time  | t <sub>f</sub>      |  |        | 110    | 165    |      |  |
| Source-Drain Reverse Recovery Time                                     | t <sub>rr</sub>     | I <sub>F</sub> = - 1.7 A, dI/dt = 100 A/μs   |        | 40     | 70     |      |  |

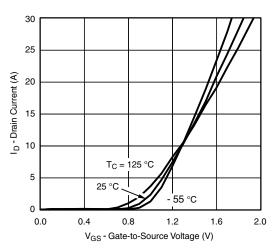
### Notes:

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
  b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

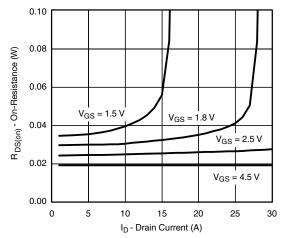




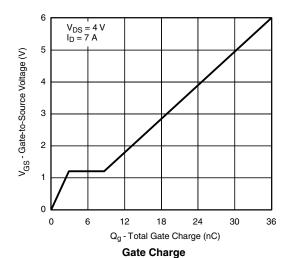
**Transfer Characteristics** 

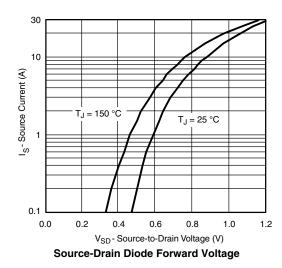


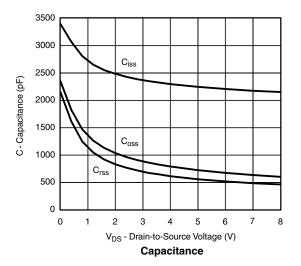
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

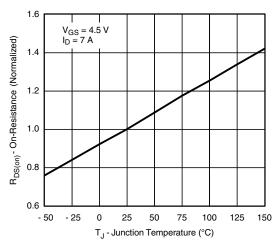


### On-Resistance vs. Drain Current

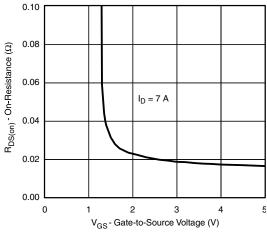








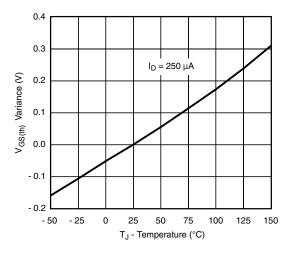
On-Resistance vs. Junction Temperature

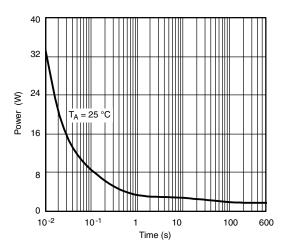


On-Resistance vs. Gate-to-Source Voltage

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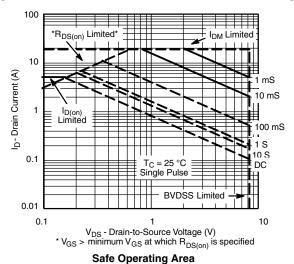
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

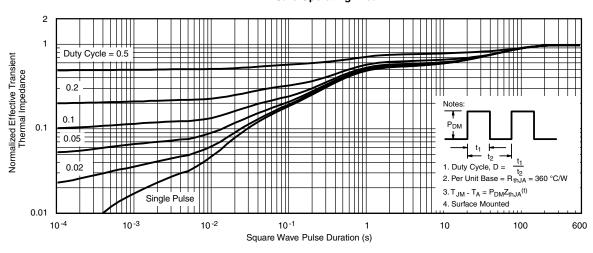




### **Threshold Voltage**

Single Pulse Power

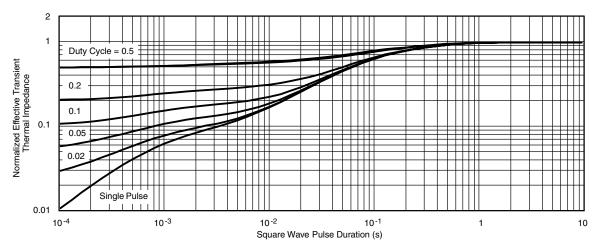




Normalized Thermal Transient Impedance, Junction-to-Ambient



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

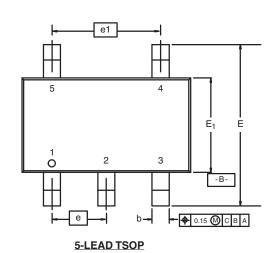
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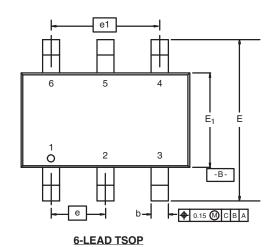


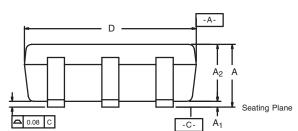


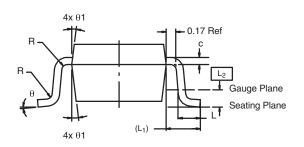
TSOP: 5/6-LEAD

**JEDEC Part Number: MO-193C** 









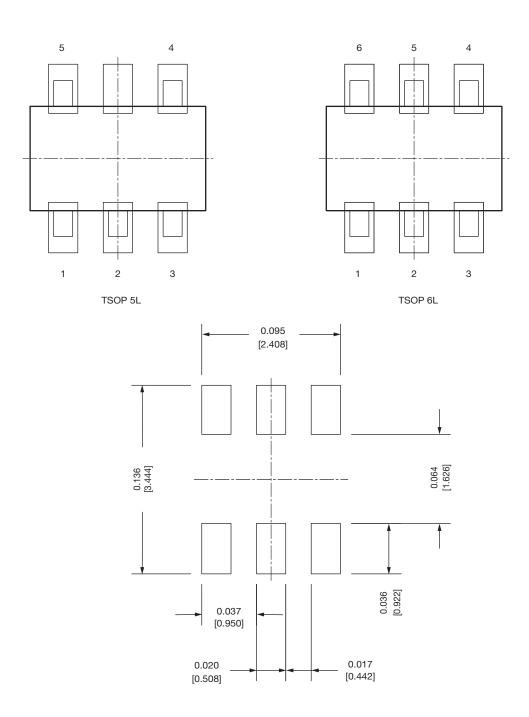
|   | MILLIMETERS |                   |      | ı          | NCHES     |       |  |
|---|-------------|-------------------|------|------------|-----------|-------|--|
| Dim   | Min         | Nom               | Max  | Min        | Nom       | Max   |  |
| Α   | 0.91        | -                 | 1.10 | 0.036      | -         | 0.043 |  |
| A <sub>1</sub>                              | 0.01        | -                 | 0.10 | 0.0004     | -         | 0.004 |  |
| A <sub>2</sub>                              | 0.90        | -                 | 1.00 | 0.035      | 0.038     | 0.039 |  |
| b   | 0.30        | 0.32              | 0.45 | 0.012      | 0.013     | 0.018 |  |
| С   | 0.10        | 0.15              | 0.20 | 0.004      | 0.006     | 0.008 |  |
| D   | 2.95        | 3.05              | 3.10 | 0.116      | 0.120     | 0.122 |  |
| Е   | 2.70        | 2.85              | 2.98 | 0.106      | 0.112     | 0.117 |  |
| E <sub>1</sub>                              | 1.55        | 1.65              | 1.70 | 0.061      | 0.065     | 0.067 |  |
| е   |             | 0.95 BSC          |      | 0.0374 BSC |           |       |  |
| e <sub>1</sub>                              | 1.80        | 1.90              | 2.00 | 0.071      | 0.075     | 0.079 |  |
| L   | 0.32        | -                 | 0.50 | 0.012      | -         | 0.020 |  |
| L <sub>1</sub>                              |             | 0.60 Ref          |      | 0.024 Ref  |           |       |  |
| L <sub>2</sub>                              |             | 0.25 BSC 0.010 BS |      |            | 0.010 BSC |       |  |
| R   | 0.10        | -                 | -    | 0.004      | -         | -     |  |
| θ   | 0°          | 4°                | 8°   | 0°         | 4°        | 8°    |  |
| $\theta_1$                                  |             | 7° Nom 7° Nom     |      |            |           |       |  |
| ECN: C-06593-Rev. I, 18-Dec-06<br>DWG: 5540 |             |                   |      |            |           |       |  |

Document Number: 71200 18-Dec-06

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# Recommended Land Pattern For TSOP-5L / TSOP-6L



### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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