

## N-Channel 8 V (D-S) MOSFET

### PRODUCT SUMMARY

| $V_{DS}$ (V) | $R_{DS(on)}$ ( $\Omega$ ) | $I_D$ (A)         | $Q_g$ (Typ.) |
|--------------|---------------------------|-------------------|--------------|
| 8            | 0.086 at $V_{GS} = 4.5$ V | 1.34 <sup>a</sup> | 7.1          |
|              | 0.093 at $V_{GS} = 2.5$ V | 1.29              |              |
|              | 0.102 at $V_{GS} = 1.8$ V | 1.23              |              |
|              | 0.120 at $V_{GS} = 1.5$ V | 0.7               |              |

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 %  $R_g$  Tested
- Compliant to RoHS Directive 2002/95/EC

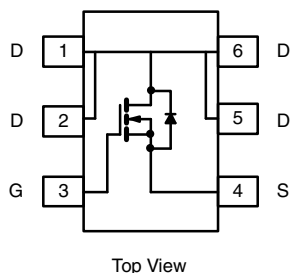


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

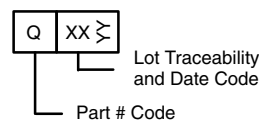
### APPLICATIONS

- Load Switch for Portable Devices

SC-89 (6-LEADS)



Marking Code



Ordering Information: Si1050X-T1-GE3 (Lead (Pb)-free and Halogen-free)

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

| Parameter   | Symbol         | Limit               | Unit |
|---|----------------|---------------------|------|
| Drain-Source Voltage                                    | $V_{DS}$       | 8                   | V    |
| Gate-Source Voltage                                     | $V_{GS}$       | $\pm 5$             |      |
| Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup> | $T_A = 25$ °C  | $I_D$               | A    |
|   | $T_A = 70$ °C  |                     |      |
| Pulsed Drain Current                                    | $I_{DM}$       | 6                   |      |
| Continuous Source-Drain Diode Current                   | $I_S$          | 0.2 <sup>b, c</sup> |      |
| Maximum Power Dissipation <sup>a</sup>                  | $T_A = 25$ °C  | $P_D$               | W    |
|   | $T_A = 70$ °C  |                     |      |
| Operating Junction and Storage Temperature Range        | $T_J, T_{stg}$ | - 55 to 150         | °C   |

### THERMAL RESISTANCE RATINGS

| Parameter                                   | Symbol     | Typical | Maximum | Unit |
|---|------------|---------|---------|------|
| Maximum Junction-to-Ambient <sup>b, d</sup> | $R_{thJA}$ | 440     | 530     | °C/W |
|   |            | 540     | 650     |      |

Notes:

- Based on  $T_C = 25$  °C.
- Surface mounted on 1" x 1" FR4 board.
- $t = 5$  s.
- Maximum under steady state conditions is 650 °C/W.

| SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted) |                                      |   |      |        |       |       |  |
|---|--------------------------------------|---|------|--------|-------|-------|--|
| Parameter   | Symbol                               | Test Conditions   | Min. | Typ.   | Max.  | Unit  |  |
| Static  |                                      |   |      |        |       |       |  |
| Drain-Source Breakdown Voltage                                  | V <sub>DS</sub>                      | V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA  | 8    |        |       | V     |  |
| V <sub>DS</sub> Temperature Coefficient                         | ΔV <sub>DS</sub> /T <sub>J</sub>     | I <sub>D</sub> = 250 μA   |      | 18.2   |       | mV/°C |  |
| V <sub>GS(th)</sub> Temperature Coefficient                     | ΔV <sub>GS(th)</sub> /T <sub>J</sub> |   |      | - 2.55 |       |       |  |
| Gate-Source Threshold Voltage                                   | V <sub>GS(th)</sub>                  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA   | 0.35 |        | 0.9   | V     |  |
| Gate-Source Leakage   | I <sub>GSS</sub>                     | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 5 V  |      |        | ± 100 | nA    |  |
| Zero Gate Voltage Drain Current                                 | I <sub>DSS</sub>                     | V <sub>DS</sub> = 8 V, V <sub>GS</sub> = 0 V  |      |        | 1     | μA    |  |
|   |                                      | V <sub>DS</sub> = 8 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C  |      |        | 10    |       |  |
| On-State Drain Current <sup>a</sup>                             | I <sub>D(on)</sub>                   | V <sub>DS</sub> = ≥ 5 V, V <sub>GS</sub> = 4.5 V  | 6    |        |       | A     |  |
| Drain-Source On-State Resistance <sup>a</sup>                   | R <sub>DS(on)</sub>                  | V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.34 A  |      | 0.071  | 0.086 | Ω     |  |
|   |                                      | V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1.29 A  |      | 0.078  | 0.093 |       |  |
|   |                                      | V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 1.23 A  |      | 0.085  | 0.102 |       |  |
|   |                                      | V <sub>GS</sub> = 1.5 V, I <sub>D</sub> = 0.76 A  |      | 0.092  | 0.120 |       |  |
| Forward Transconductance  | g <sub>fs</sub>                      | V <sub>DS</sub> = 4 V, I <sub>D</sub> = 1.34 A  |      | 4.12   |       | S     |  |
| Dynamic <sup>b</sup>  |                                      |   |      |        |       |       |  |
| Input Capacitance   | C <sub>iss</sub>                     | V <sub>DS</sub> = 4 V, V <sub>GS</sub> = 0 V, f = 1 MHz   |      | 585    |       | pF    |  |
| Output Capacitance  | C <sub>oss</sub>                     |   |      | 190    |       |       |  |
| Reverse Transfer Capacitance                                    | C <sub>rss</sub>                     |   |      | 130    |       |       |  |
| Total Gate Charge   | Q <sub>g</sub>                       | V <sub>DS</sub> = 4 V, V <sub>GS</sub> = 5 V, I <sub>D</sub> = 1.34 A   |      | 7.7    | 11.6  | nC    |  |
|   |                                      | V <sub>DS</sub> = 4 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.34 A   |      | 7.1    | 10.7  |       |  |
|   |                                      |   |      |        | 1.14  |       |  |
|   |                                      |   |      |        | 1.69  |       |  |
| Gate-Source Charge  | Q <sub>gs</sub>                      |   |      |        |       |       |  |
| Gate-Drain Charge   | Q <sub>gd</sub>                      |   |      |        |       |       |  |
| Gate Resistance   | R <sub>g</sub>                       | f = 1 MHz   |      | 3.5    | 4.6   | Ω     |  |
| Turn-On Delay Time  | t <sub>d(on)</sub>                   | V <sub>DD</sub> = 4 V, R <sub>L</sub> = 3.6 Ω<br>I <sub>D</sub> ≅ 1.1 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω |      | 6.8    | 10.2  | ns    |  |
| Rise Time   | t <sub>r</sub>                       |   |      | 35     | 53    |       |  |
| Turn-Off DelayTime  | t <sub>d(off)</sub>                  |   |      | 25     | 37.5  |       |  |
| Fall Time   | t <sub>f</sub>                       |   |      | 6      | 9     |       |  |
| Drain-Source Body Diode Characteristics                         |                                      |   |      |        |       |       |  |
| Pulse Diode Forward Current <sup>a</sup>                        | I <sub>SM</sub>                      |   |      |        | 6     | A     |  |
| Body Diode Voltage  | V <sub>SD</sub>                      | I <sub>S</sub> = 1.0 A  |      | 0.8    | 1.2   | V     |  |
| Body Diode Reverse Recovery Time                                | t <sub>rr</sub>                      | I <sub>F</sub> = 1.0 A, di/dt = 100 A/μs  |      | 18.5   | 28    | nC    |  |
| Body Diode Reverse Recovery Charge                              | Q <sub>rr</sub>                      |   |      | 3.7    | 5.7   | ns    |  |
| Reverse Recovery Fall Time                                      | t <sub>a</sub>                       |   |      | 6.7    |       |       |  |
| Reverse Recovery Rise Time                                      | t <sub>b</sub>                       |   |      | 11.8   |       |       |  |

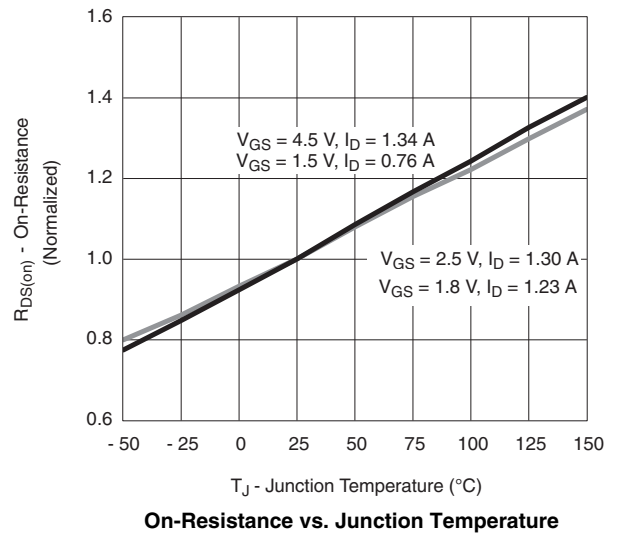
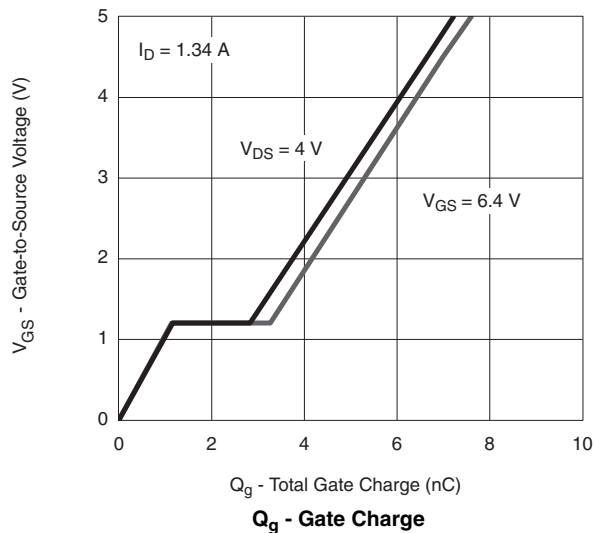
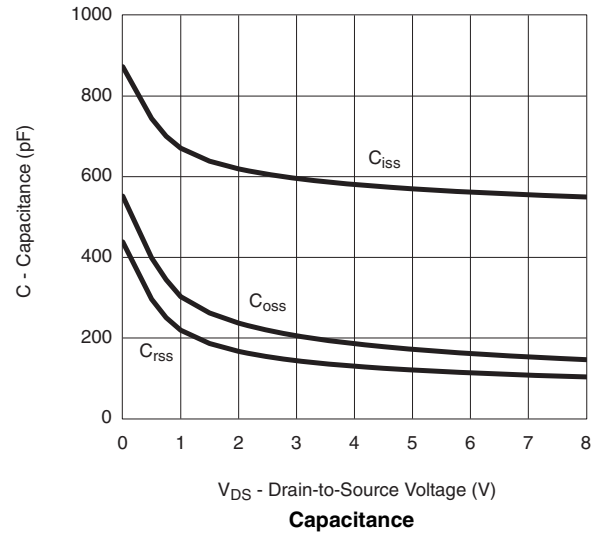
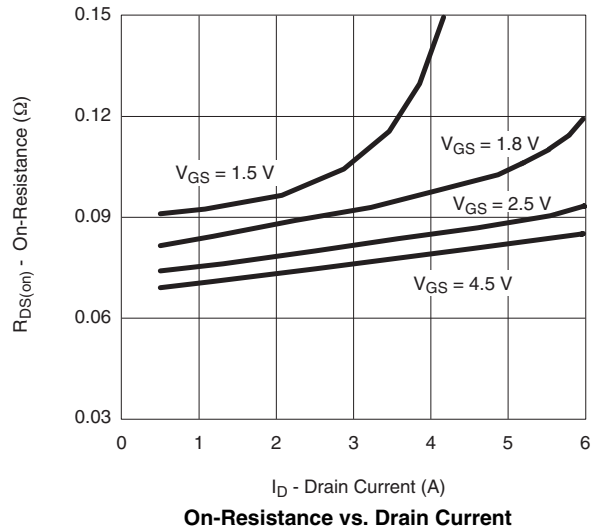
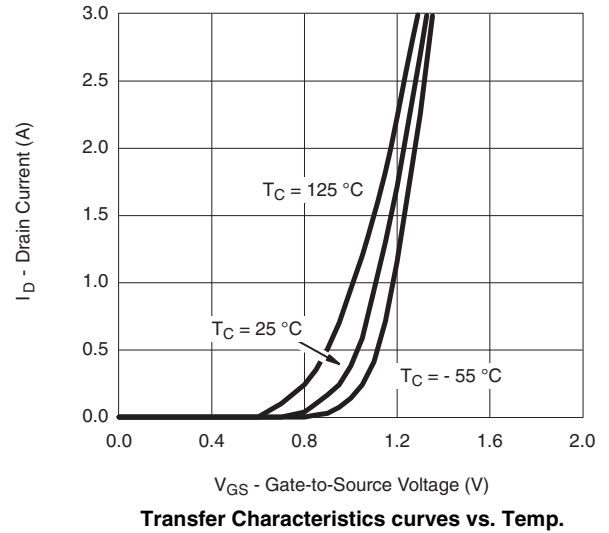
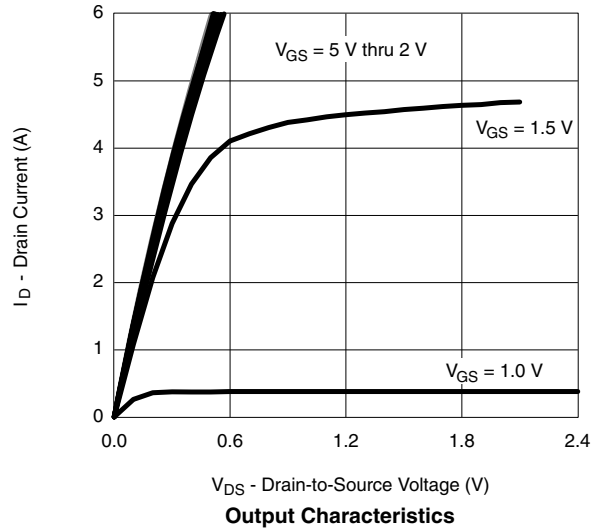
Notes:

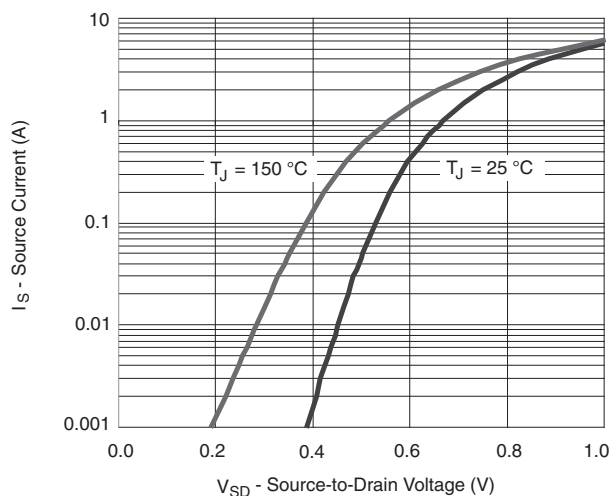
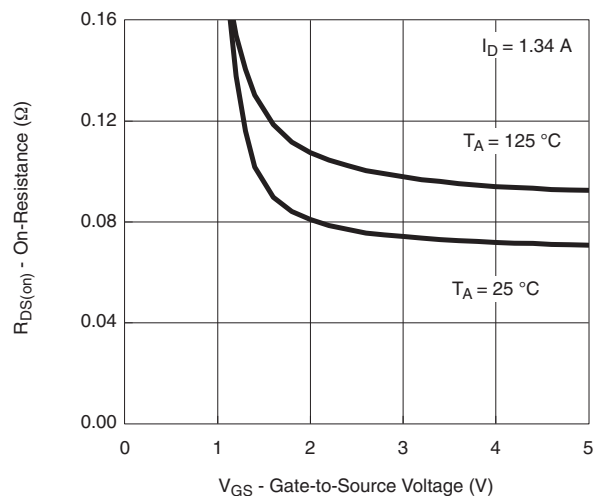
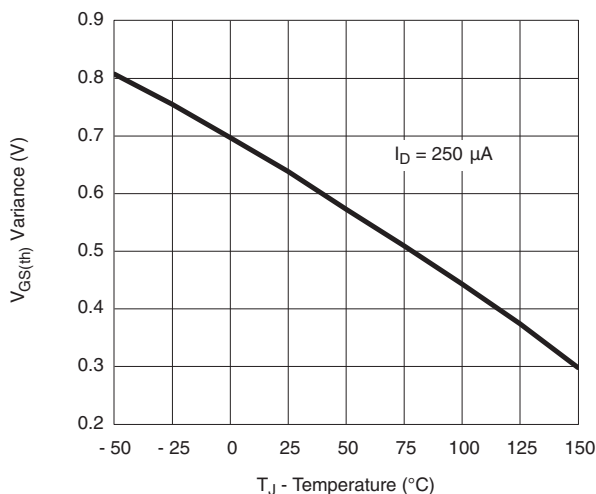
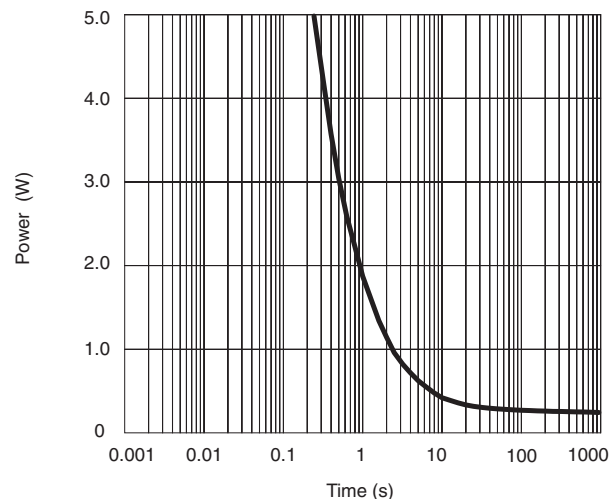
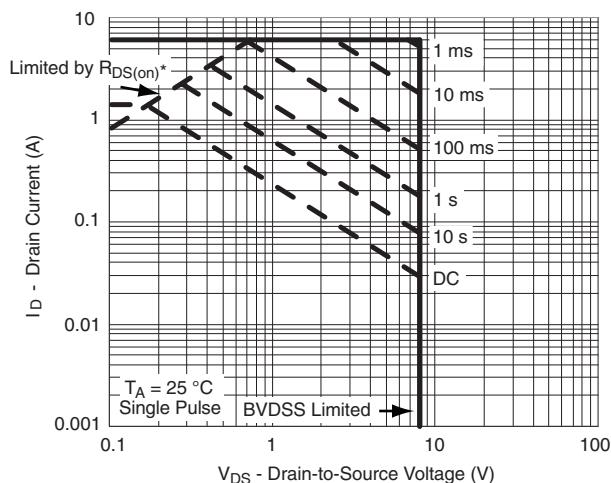
a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

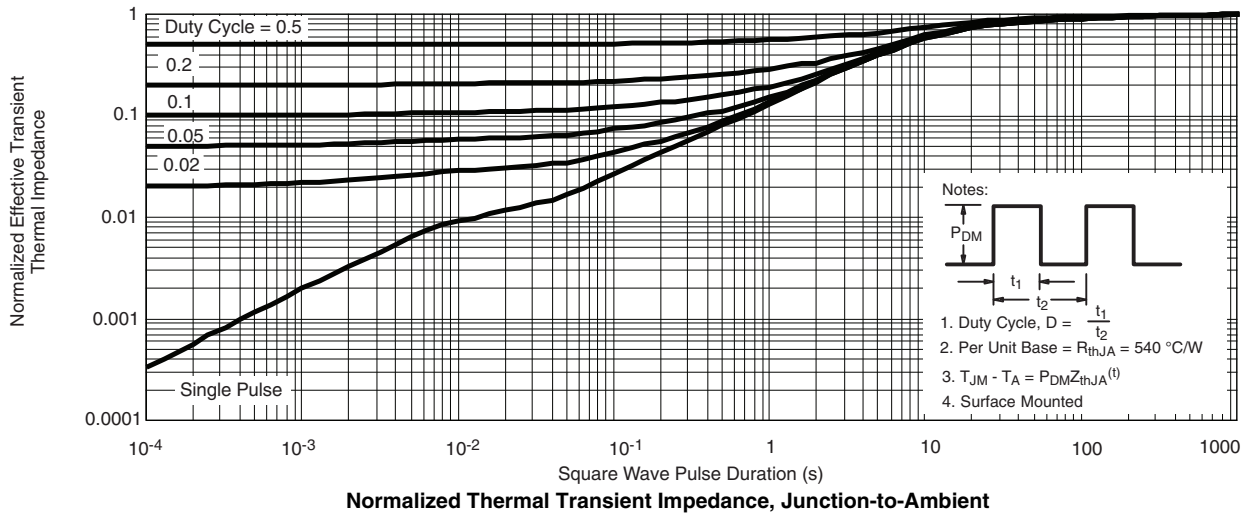


**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

**Source-Drain Diode Forward Voltage**

 **$R_{DS(on)}$  vs  $V_{GS}$  vs Temperature**

**Threshold Voltage**

**Single Pulse Power**


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area, Junction-to-Ambient**

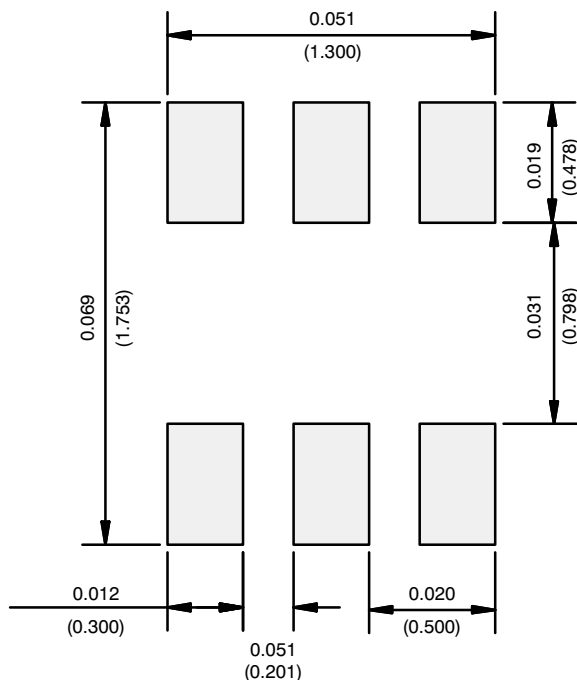
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## RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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