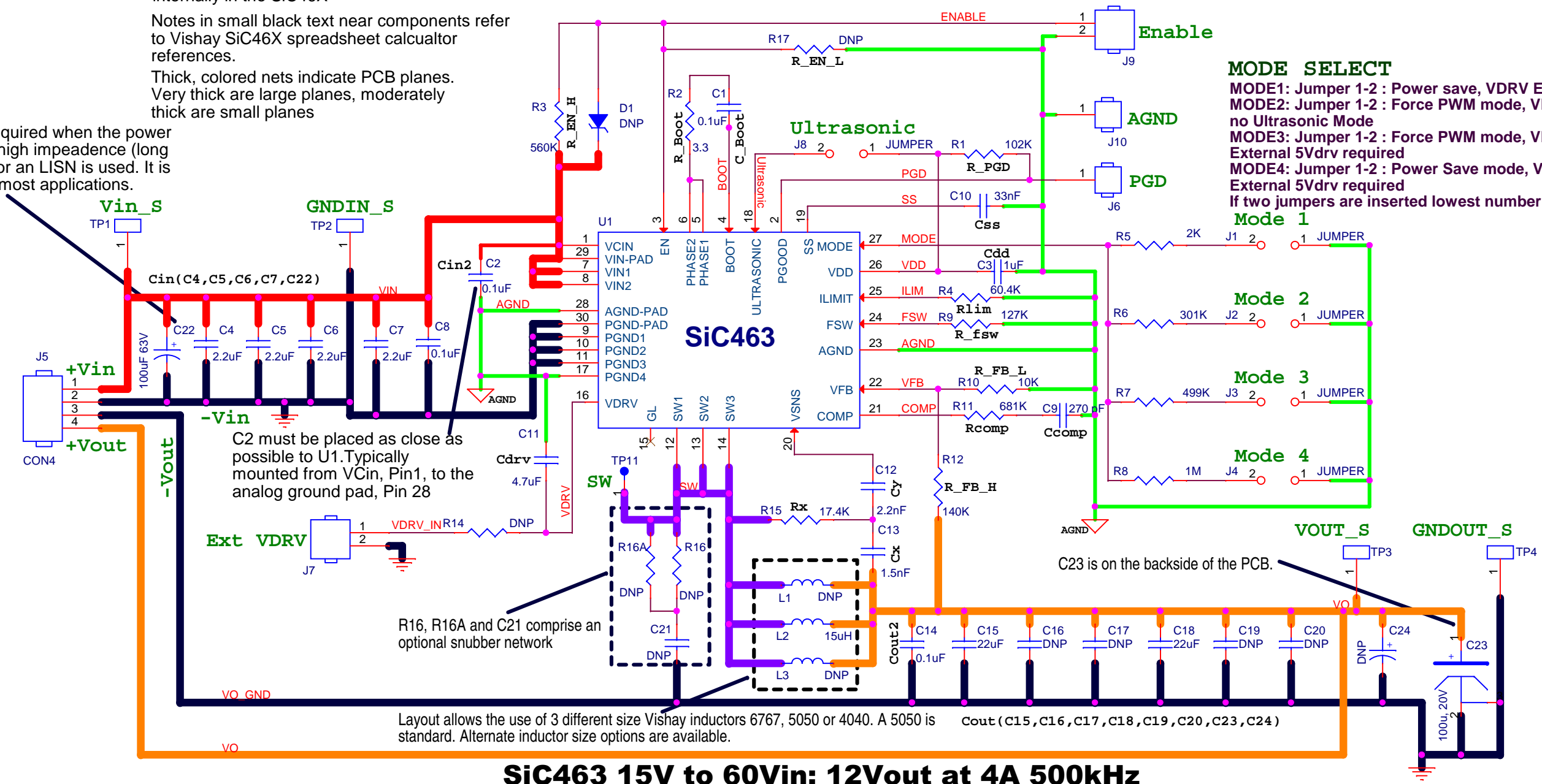


Analog ground (AGND), PGND are tied internally in the SiC46X

Notes in small black text near components refer to Vishay SiC46X spreadsheet calcaultor references.

Thick, colored nets indicate PCB planes. Very thick are large planes, moderately thick are small planes

C22 is be required when the power source has high impedance (long lead wires) or an LISN is used. It is optional for most applications.



MODE SELECT

MODE1: Jumper 1-2 : Power save, VDRV Enabled

MODE2: Jumper 1-2 : Force PWM mode, VDRV Enabled, no Ultrasonic Mode

MODE3: Jumper 1-2 : Force PWM mode, VDRV Disabled, External 5Vdrv required

MODE4: Jumper 1-2 : Power Save mode, VDRV Disabled, External 5Vdrv required

If two jumpers are inserted lowest number dominates

Mode 1

SiC463 15V to 60Vin; 12Vout at 4A 500kHz

Title		
SiC463 Demo Low Power Unified Board 15 to 60Vin 12Vout 4A 500K		
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