RoHS

HALOGEN FREE

GREEN

(5-2008)



## Vishay Semiconductors

### **Bicolor SMD LED**



#### **DESCRIPTION**

These devices have been designed to meet the increasing demand for surface mounting technology.

The package of the VLMKG3400 is the PLCC-4.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

This SMD device consists of a red and green chip. So it is possible to choose the color in one device.

#### PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-4
Product series: bicolor
Angle of half intensity: ± 60°

#### **FEATURES**

- SMD LED with exceptional brightness
- Multicolored
- · Luminous intensity categorized
- EIA and ICE standard package
- Compatible with automatic placement equipment
- Compatible with IR reflow, vapor phase and wave soldering processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit  $I_{Vmax}/I_{Vmin} \le 1.6$
- Preconditioning according to JEDEC® level 4
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches, and symbols
- · General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I <sub>F</sub> (mA)	WAV	WAVELENGTH (nm)		at I <sub>F</sub>	FORWARD VOLTAGE (V)		at I <sub>F</sub>	TECHNOLOGY		
		MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	(IIIA)	
VLMKG3400-GS08	Super red	56	ı	140	20	627	633	639	20	-	1.9	2.6	20	AllnGaP on GaAs
VLIVING3400-G308	Green	35.5	ı	90	20	564	570	575	20	-	2.0	2.6	20	AllnGaP on GaAs
VLMKG3400-GS18	Super red	56	-	140	20	627	633	639	20	-	1.9	2.6	20	AllnGaP on GaAs
VLIVING3400-G316	Green	35.5	-	90	20	564	570	575	20	-	2.0	2.6	20	AllnGaP on GaAs



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25$ °C, unless otherwise specified) <b>VLMKG3400</b>							
PARAMETER	TEST CONDITI	ON	SYMBOL	VALUE	UNIT		
Reverse voltage per diode			$V_{R}$	Not designed for reverse operation	V		
DC forward current per diode	T <sub>amb</sub> ≤ 80 °C	1 chip on	I <sub>F</sub>	30	mA		
Surge forward current per diode			I <sub>FSM</sub>	0.1	Α		
Power dissipation per diode			P <sub>V</sub>	80	mW		
Junction temperature			Tj	125	°C		
Operating temperature range			T <sub>amb</sub>	-40 to +100	°C		
Storage temperature range			T <sub>stg</sub>	-40 to +100	°C		
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	1 chip on 2 chips on	R <sub>thJA</sub>	560 780	K/W		

OPTICAL AND ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25  ^{\circ}C$ , unless otherwise specified) VLMKG3400, SUPER RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I <sub>F</sub> = 20 mA	VLMKG3400	Ι <sub>V</sub>	56	=	140	mcd
Dominant wavelength	$I_F = 20 \text{ mA}$		$\lambda_{d}$	627	633	639	nm
Peak wavelength	I <sub>F</sub> = 20 mA		$\lambda_{p}$	-	643	-	nm
Angle of half intensity	I <sub>F</sub> = 20 mA		φ	-	± 60	-	0
Forward voltage	I <sub>F</sub> = 20 mA		$V_{F}$	-	1.9	2.6	V
Reverse current	V <sub>R</sub> = 5 V		I <sub>R</sub>	-	-	10	μΑ
Junction capacitance	$V_R = 0 V, f = 1 MHz$		C <sub>j</sub>	-	15	-	pF

OPTICAL AND ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25$ °C, unless otherwise specified) VLMKG3400, GREEN							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I <sub>F</sub> = 20 mA	VLMKG3400	Ι <sub>V</sub>	35.5	-	90	mcd
Dominant wavelength	I <sub>F</sub> = 20 mA		$\lambda_{d}$	564	570	575	nm
Peak wavelength	I <sub>F</sub> = 20 mA		$\lambda_{p}$	-	572	-	nm
Angle of half intensity	I <sub>F</sub> = 20 mA		φ	-	± 60	-	٥
Forward voltage	I <sub>F</sub> = 20 mA		$V_{F}$	-	2.0	2.6	V
Reverse current	V <sub>R</sub> = 5 V		I <sub>R</sub>	-	-	10	μA
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>j</sub>	-	15	-	pF

CROSSING TABLE					
VISHAY	OSRAM				
VLMKG3400	LSGT676				



LU	LUMINOUS INTENSITY CLASSIFICATION AND GROUP COMBINATIONS, VLMKG3400							
		SUPER RED						
		P2 56 mcd to 71 mcd	Q1 71 mcd to 90 mcd	Q2 90 mcd to 112 mcd	R1 112 mcd to 140 mcd			
	N2 35.5 mcd to 45 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400			
G R E	P1 45 mcd to 56 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400			
E	P2 56 mcd to 71 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400			
	Q1 71 mcd to 90 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400			

#### Note

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel. In order to ensure availability, single wavelength groups will not be orderable.

COLOR CLASSIFICATION						
	DOMINANT WAV	ELENGTH (nm)				
GROUP	GREEN					
	MIN.	MAX.				
4	564	567				
5	566	569				
6	568	571				
7	570	573				
8	572	575				

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

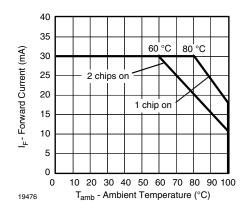


Fig. 1 - Forward Current vs. Ambient Temperature

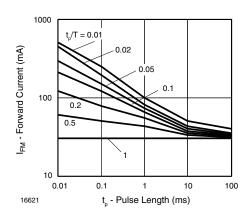


Fig. 2 - Forward Current vs. Pulse Duration

<sup>•</sup> Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm$  11 %.

Wavelengths are tested at a current pulse duration of 25 ms.

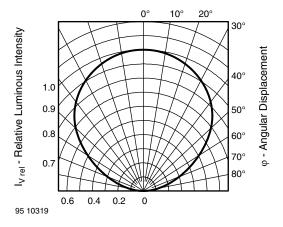


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

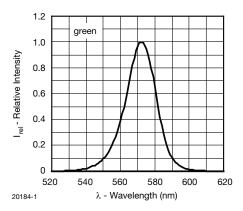


Fig. 4 - Relative Intensity vs. Wavelength

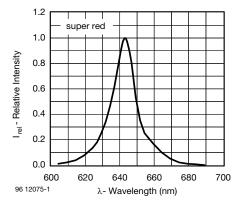


Fig. 5 - Relative Intensity vs. Wavelength

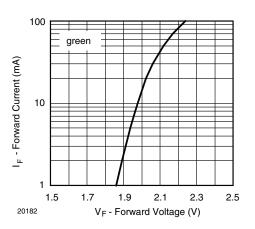


Fig. 6 - Forward Current vs. Forward Voltage

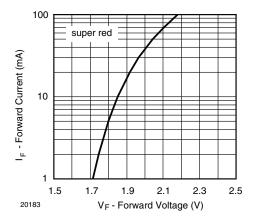


Fig. 7 - Forward Current vs. Forward Voltage

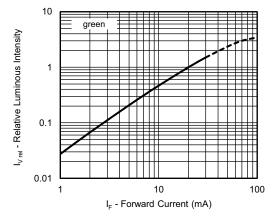
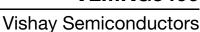


Fig. 8 - Relative Luminous Intensity vs. Forward Current





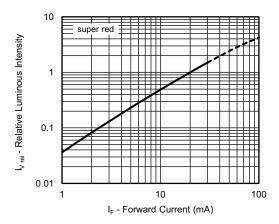


Fig. 9 - Relative Luminous Intensity vs. Forward Current

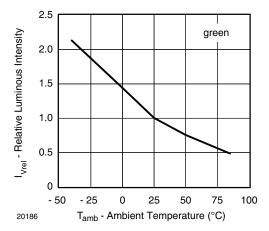


Fig. 10 - Relative Luminous Intensity vs. Ambient Temperature

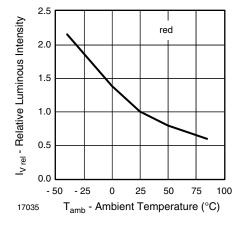


Fig. 11 - Relative Luminous Intensity vs. Ambient Temperature

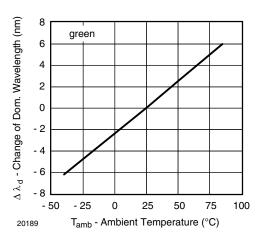


Fig. 12 - Change of Dominant Wavelength vs.
Ambient Temperature

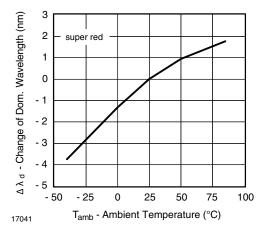


Fig. 13 - Change of Dominant Wavelength vs.
Ambient Temperature

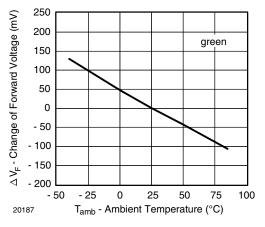


Fig. 14 - Change of Forward Voltage vs. Ambient Temperature





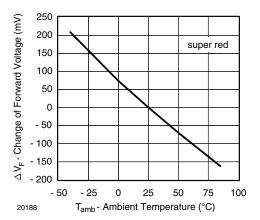
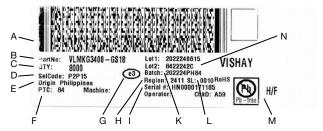


Fig. 15 - Change of Forward Voltage vs. Ambient Temperature

### **BAR CODE PRODUCT LABEL** (example)



A. 2D barcode

B. Part No: Vishay part number

C. QTY: quantity

D. SelCode: selection bin code

E. Country of origin

F. PTC: production plant code

G. Termination finish

H. Region code

I. Serial#: serial number

K. Batch number: year, week, country code, plant code

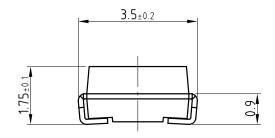
L. SL: storage location

M. Environmental symbols: RoHS, lead (Pb)-free, halogen-free

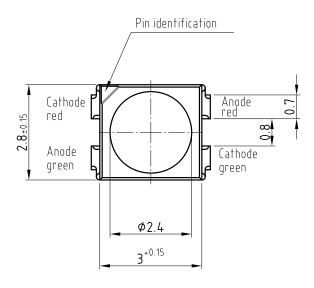
N. Lot numbers

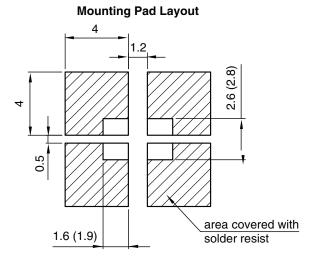


### **PACKAGE DIMENSIONS** in millimeters









Dimensions: IR and Vaporphase (Wave Soldering)

Drawing-No.: 6.541-5057.02-4 Issue: 2; 30.05.07

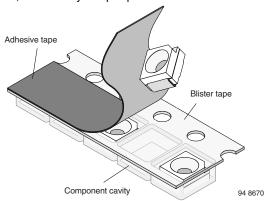
20190



#### METHOD OF TAPING / POLARITY AND TAPE AND REEL

#### SMD LED (VLM.3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



#### TAPING OF VLM.3...

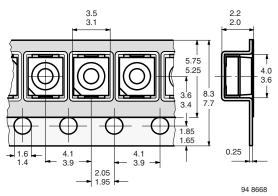


Fig. 16 - Tape Dimensions in mm for PLCC-2

### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

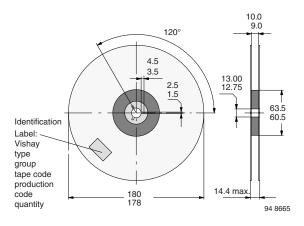


Fig. 17 - Reel Dimensions - GS08

### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

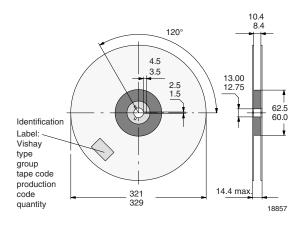


Fig. 18 - Reel Dimensions - GS18

#### **SOLDERING PROFILE**

#### IR Reflow Soldering Profile for lead (Pb)-free soldering Preconditioning acc. to JEDEC level 4

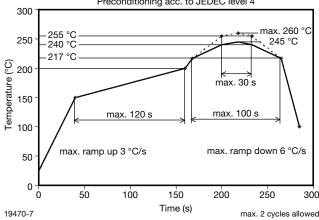


Fig. 19 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

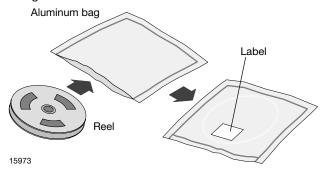


#### TTW Soldering (according to CECC00802) 300 Lead temperature 250 235 °C to Full line: typical Second 260 °C Dotted lines: First wa 200 Temperature (°C) process limits ca 2 K/s ca. 200 K/s 150 00 °C to 100 ca. 5 K/s 50 Forced cooling 0 100 150 200 250 Time (s) 948626-1

Fig. 20 - Double Wave Soldering of Opto Devices (all Packages)

#### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

#### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

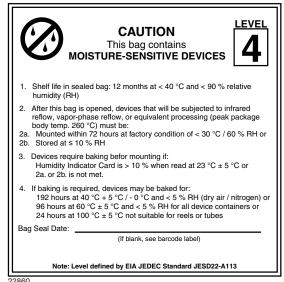
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen)

96 h at 60 °C + 5 °C and < 5 % RH for all device containers

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 4 label is included on all dry bags.



Example of JESD22-A112 level 4 label

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

### **VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



## **Legal Disclaimer Notice**

Vishay

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