



## IR Receiver Modules for Remote Control Systems



### DESCRIPTION

The TSOP37S40 series are miniaturized SMD IR receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a PCB, the epoxy package contains an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding. The demodulated output signal can be directly decoded by a microprocessor. The TSOP37S40 is compatible with 12, 15, and 20 bit Sony codes. It is optimized to suppress almost all spurious pulses from energy saving fluorescent lamps but will also suppress some data signals.

These components have not been qualified according to automotive specifications.

### FEATURES

- Individual IC settings to reach maximum performance
- Immunity against noise (lamps, LCD TV, Wi-Fi)
- Low supply current
- Photo detector and preamplifier in one package
- Supply voltage: 2.0 V to 5.5 V
- Material categorization:  
for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

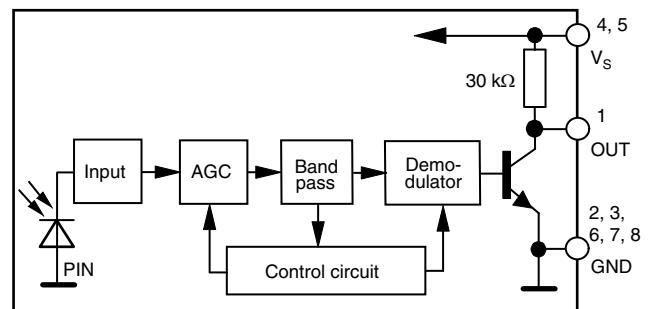
### LINKS TO ADDITIONAL RESOURCES



### DESIGN SUPPORT TOOLS

- [3D models](#)
- [Window size calculator](#)

### BLOCK DIAGRAM



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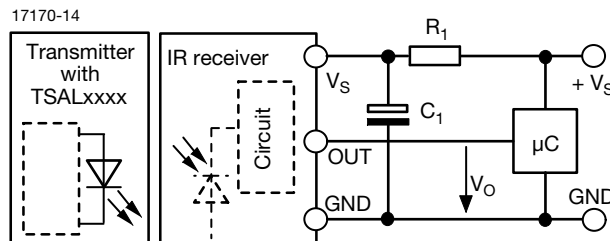


**ORDERING CODE**

**Taping:**

TSOP37...TT1 - top view taped, 1800 pcs/reel

**APPLICATION CIRCUIT**



R<sub>1</sub> and C<sub>1</sub> recommended in case there are strong ripple or spikes on the supply line.

PARTS TABLE	
AGC	SONY (AGC-S)
Carrier frequency	40 kHz
Package	Belogog
Pinning	1 = OUT, 2, 3, 6, 7, 8 = GND, 4, 5 = V <sub>S</sub>
Dimensions (mm)	3.95 W x 3.95 H x 0.8 D
Mounting	SMD
Application	Remote control
Best remote control code	( <sup>1</sup> ) Sony 12, 15, and 20 bit IR-code
Special options	• Extended temperature range: <a href="http://www.vishay.com/doc?82738">www.vishay.com/doc?82738</a>

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V <sub>S</sub>	-0.3 to +6	V
Supply current		I <sub>S</sub>	3	mA
Output voltage		V <sub>O</sub>	-0.3 to (V <sub>S</sub> + 0.3)	V
Output current		I <sub>O</sub>	5	mA
Junction temperature		T <sub>j</sub>	100	°C
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW

**Note**

- Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.



ELECTRICAL AND OPTICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		$V_S$	2.0	-	5.5	V
Supply current	$V_S = 3.3\text{ V}$ , $E_v = 0$	$I_{SD}$	0.25	0.35	0.45	mA
	$E_v = 40\text{ klx}$ , sunlight	$I_{SH}$	-	0.45	-	mA
Transmission distance	$E_v = 0$ , IR diode TSAL6200, $I_F = 50\text{ mA}$ , test signal see Fig. 1	$d$	-	18	-	m
Output voltage low	$I_{OSL} = 0.5\text{ mA}$ , $E_e = 0.7\text{ mW/m}^2$ , test signal see Fig. 1	$V_{OSL}$	-	-	100	mV
Minimum irradiance	Test signal: RC5 code	$E_{e\text{ min.}}$	-	0.2	0.4	$\text{mW/m}^2$
	Test signal: NEC code	$E_{e\text{ min.}}$	-	0.25	0.5	$\text{mW/m}^2$
Maximum irradiance	$t_{pi} - 5/f_0 < t_{po} < t_{pi} + 5/f_0$ , test signal see Fig. 1	$E_{e\text{ max.}}$	30	-	-	$\text{W/m}^2$
Directivity	Angle of half transmission distance	$\phi_{1/2}$	-	$\pm 75$	-	$^{\circ}$

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

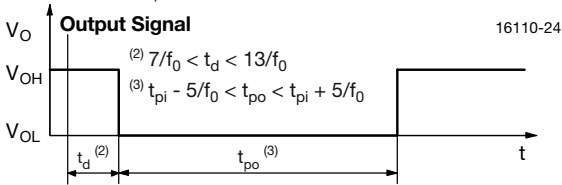
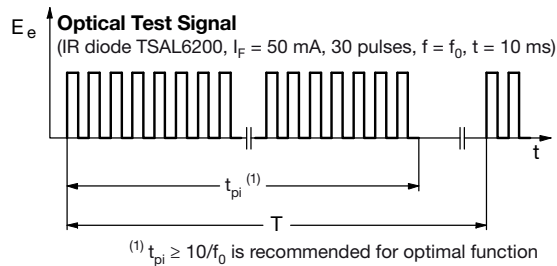


Fig. 1 - Output Function

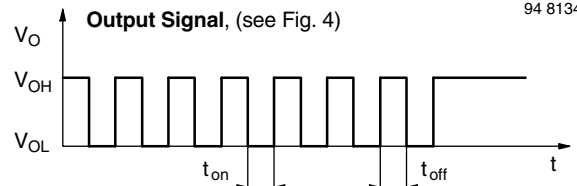
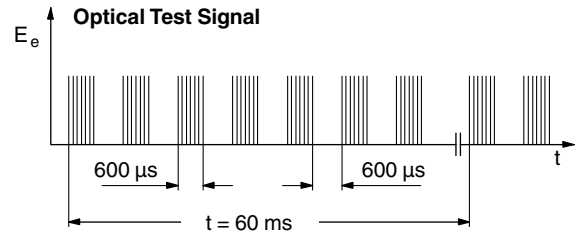


Fig. 3 - Output Function

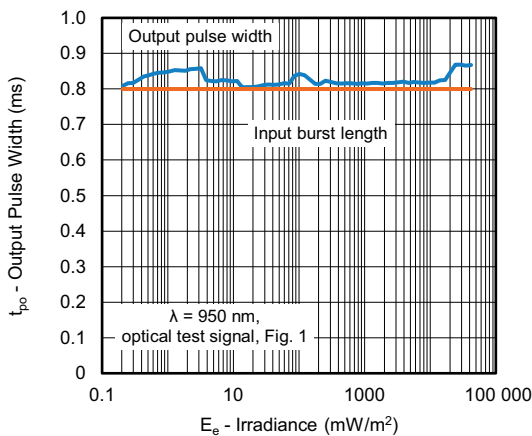


Fig. 2 - Output Pulse Width vs. Irradiance

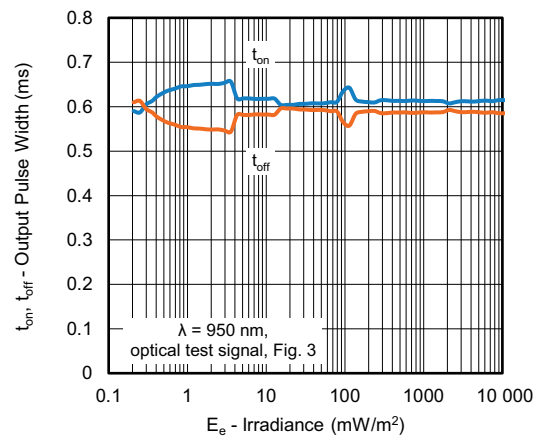


Fig. 4 - Output Pulse Diagram

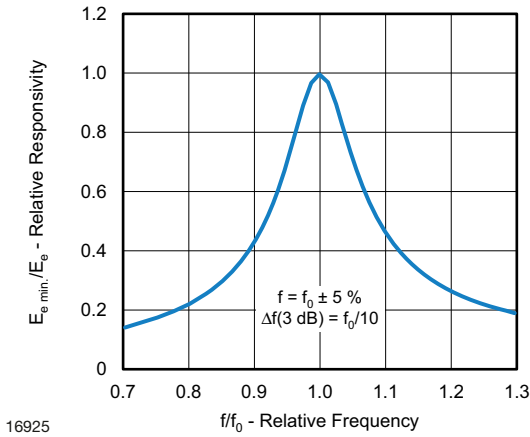


Fig. 5 - Frequency Dependence of Responsivity

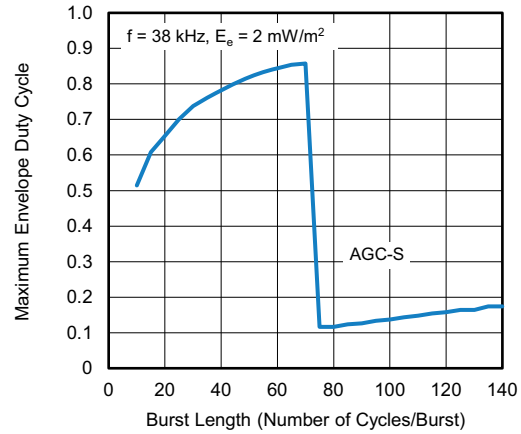


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

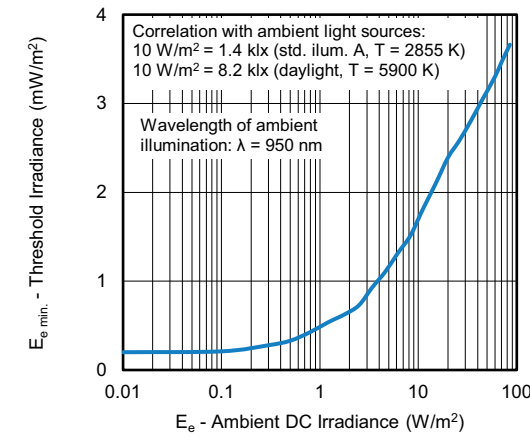


Fig. 6 - Sensitivity in Bright Ambient

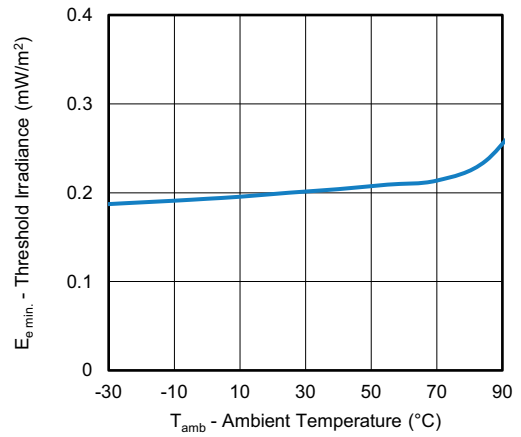


Fig. 9 - Sensitivity vs. Ambient Temperature

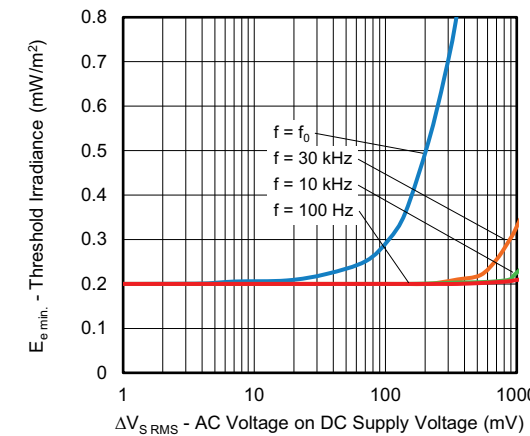


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

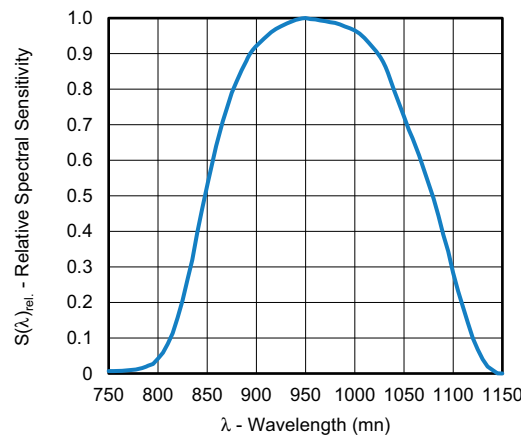


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength



21947

Fig. 11 - Directivity

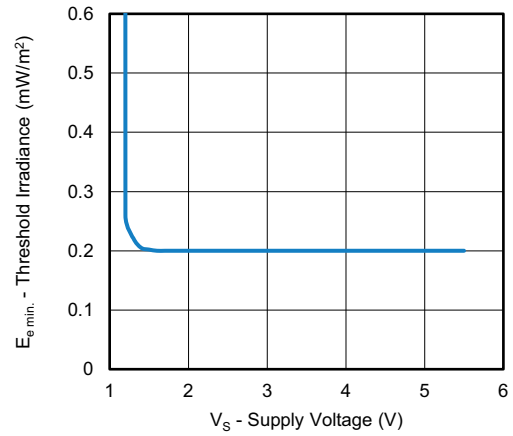


Fig. 12 - Sensitivity vs. Supply Voltage



**SUITABLE DATA FORMAT**

The TSOP37S40 series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device’s band-pass center frequency (e.g. 40 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP37S40 in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver’s output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

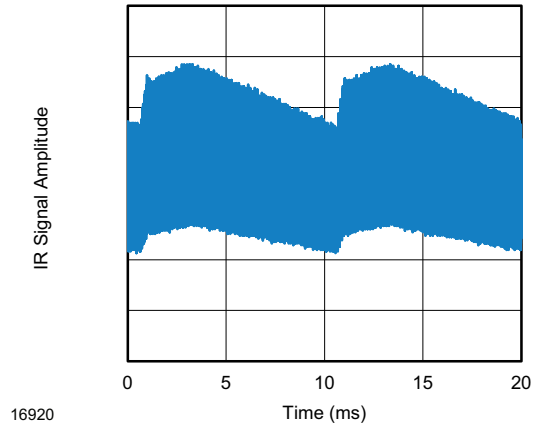


Fig. 13 - IR Signal from Fluorescent Lamp With Low Modulation

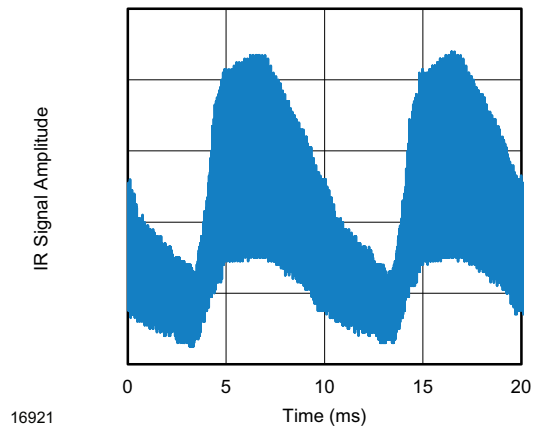
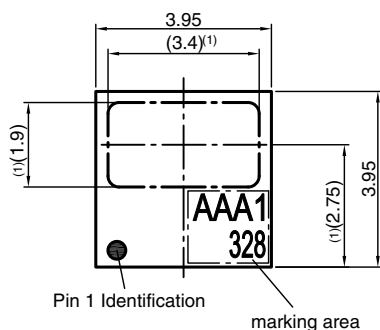
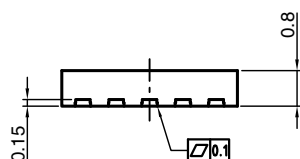
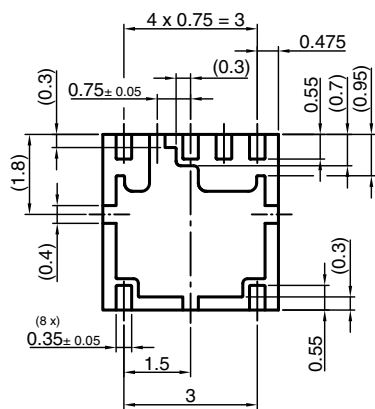


Fig. 14 - IR Signal from Fluorescent Lamp With High Modulation

	<b>TSOP37S40</b>
Minimum burst length	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 12 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 9 x burst length
Maximum number of continuous short bursts/second	1700
Suppression of interference from fluorescent lamps	Most common disturbance patterns are suppressed



**PACKAGE DIMENSIONS** in millimeters

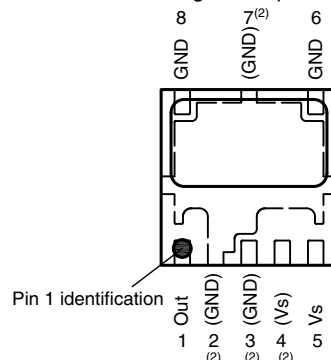


Drawing-No.: 6.550-5315.01-4  
Issue: 2; 12.02.14

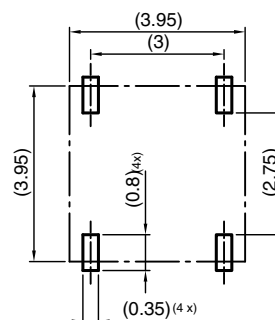
Not indicated tolerances ± 0.1



Pinning from topview



Proposed pad layout from component side (dim. for reference only)



**Notes**

- (1) Optically effective area
- (2) Pins connected internally. It is not necessary to connect externally

**ASSEMBLY INSTRUCTIONS**

**Reflow Soldering**

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured

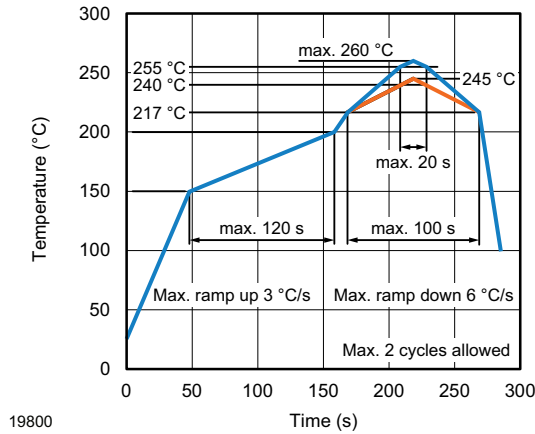
- Handling after reflow should be done only after the work surface has been cooled off

**Manual Soldering**

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

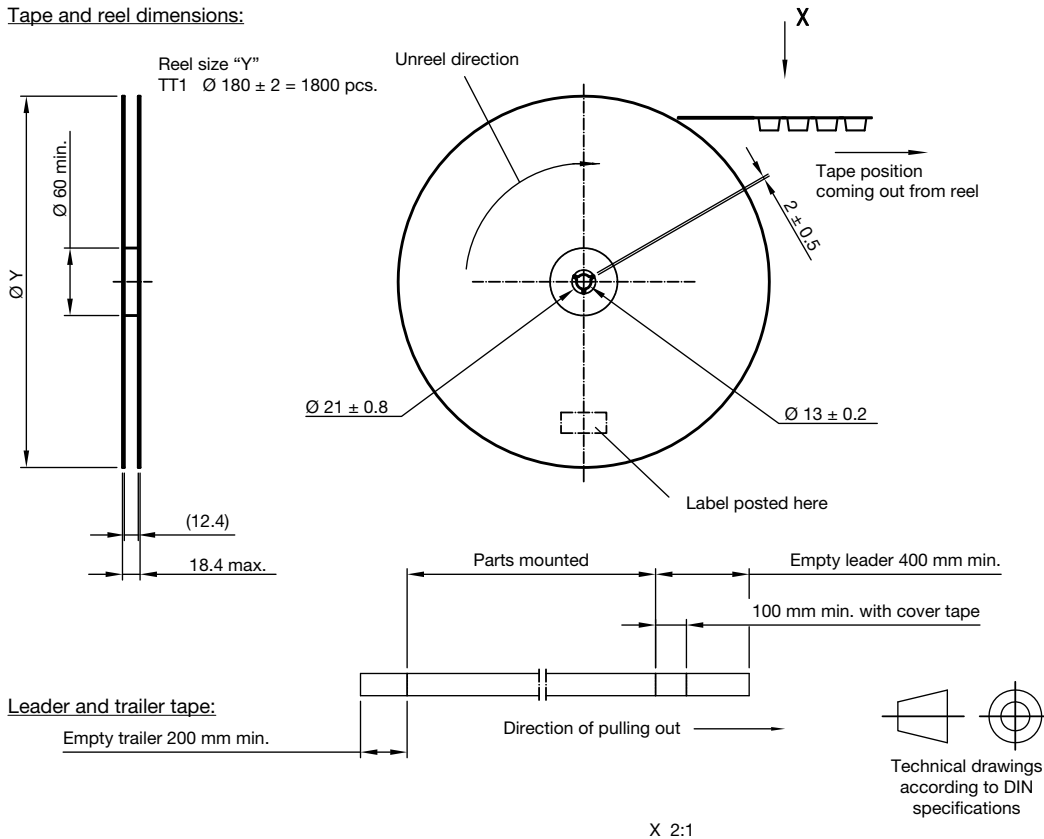


**VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE**

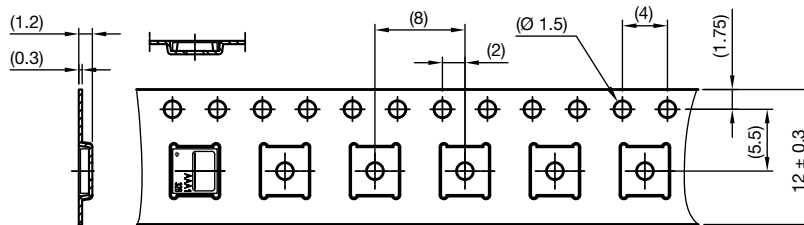


**TAPING VERSION TSOP37... DIMENSIONS** in millimeters

Tape and reel dimensions:



X 2:1



Drawing-No.: 9.700-5347.01-4  
Issue: 2; 07.03.18

Not indicated tolerances ± 0.1





**OUTER PACKAGING**

The sealed reel is packed into a pizza box.

<b>CARTON BOX DIMENSIONS</b> in millimeters			
	THICKNESS	WIDTH	LENGTH
Pizza box (Panhead, Heimdall, and Belobog) (taping in reels)	50	340	340

**LABEL**

**Standard bar code labels for finished goods**

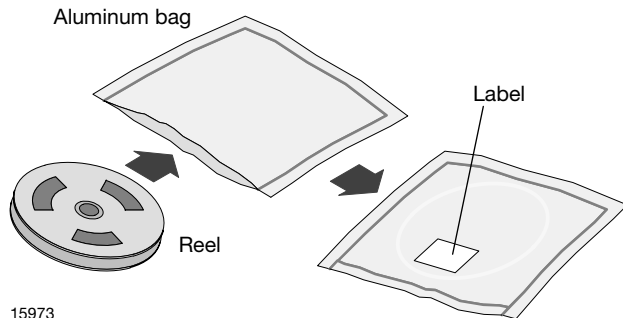
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

<b>VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL</b> (finished goods)		
PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
LONG BAR CODE TOP	TYPE	LENGTH
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
SHORT BAR CODE BOTTOM	TYPE	LENGTH
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17



**DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



15973

**FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

**RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:  
 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen)  
 or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers  
 or 24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 3 label is included on all dry bags.

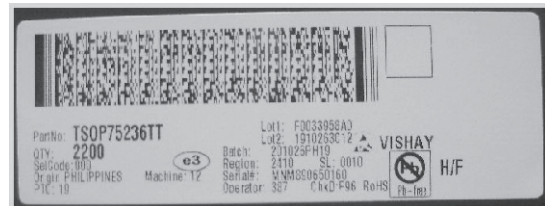
**ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

**VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

**BAR CODE PRODUCT LABEL (example)**



22178



22650

EIA JEDEC standard J-STD-020 level 3 label is included on all dry bags



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