

BYT51A, BYT51B, BYT51D, BYT51G, BYT51J, BYT51K, BYT51M

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Vishay Semiconductors

Standard Avalanche Sinterglass Diode



FEATURES

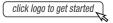
- · Glass passivated junction
- · Hermetically sealed package
- · Low reverse current
- AEC-Q101 qualified
- for definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

949539

DESIGN SUPPORT TOOLS



Models Available

MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 369 mg

• Material categorization:

APPLICATIONS

Rectification diode

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY		
BYT51M	BYT51M-TR	5000 per 10" tape and reel	25 000		
BYT51M	BYT51M-TAP	5000 per ammopack	25 000		

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BYT51A	V _R = 50 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51B	V _R = 100 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51D	V _R = 200 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51G	V _R = 400 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51J	V _R = 600 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51K	V _R = 800 V; I _{F(AV)} = 1.5 A	SOD-57		
BYT51M	V _R = 1000 V; I _{F(AV)} = 1.5 A	SOD-57		

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
	See electrical characteristics	BYT51A	$V_R = V_{RRM}$	50	V	
		BYT51B	$V_R = V_{RRM}$	100	V	
		BYT51D	$V_R = V_{RRM}$	200	V	
Reverse voltage = repetitive peak reverse voltage		BYT51G	$V_R = V_{RRM}$	400	V	
= ropolitivo podik rovorso voltago		BYT51J	$V_R = V_{RRM}$	600	V	
		BYT51K	$V_R = V_{RRM}$	800	V	
		BYT51M	$V_R = V_{RRM}$	1000	V	
Peak forward surge current	$t_p = 10$ ms, half sine wave		I _{FSM}	50	Α	
Repetitive peak forward current			I _{FRM}	9	Α	
Average forward current	I = 10 mm		I _{F(AV)}	1.5	Α	
Average forward current	On PC board		I _{F(AV)}	1	Α	
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C	
Non repetitive reverse avalanche energy	I(BR)R = 1 A		ER	20	mJ	

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER TEST CONDITION		SYMBOL	VALUE	UNIT		
Junction ambient	Lead length I = 10 mm, T _L = constant	R _{thJA}	45	K/W		
Junction ambient	On PC board with spacing 25 mm	R _{thJA}	100	K/W		

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 1 A	V_{F}	-	0.95	1.1	V
Forward voitage	I _F = 1 A, T _j = 175 °C	V _F	-	-	1	V
Reverse current	$V_R = V_{RRM}$	I _R	-	-	1	μA
neverse current	$V_R = V_{RRM}$, $T_j = 150$ °C	I _R	-	-	100	μA
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, i_R = 0.25 \text{ A}$	t _{rr}	-	-	4	μs

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

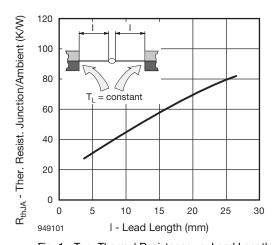


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

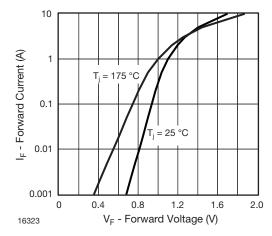


Fig. 2 - Forward Current vs. Forward Voltage

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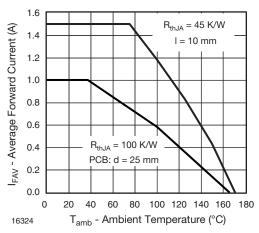


Fig. 3 - Max. Average Forward Current vs.
Ambient Temperature

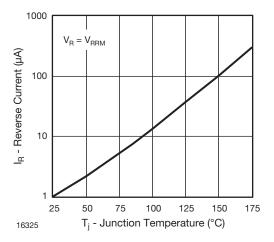


Fig. 4 - Reverse Current vs. Junction Temperature

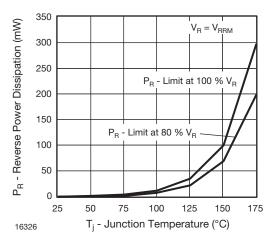


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

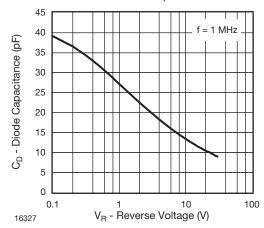
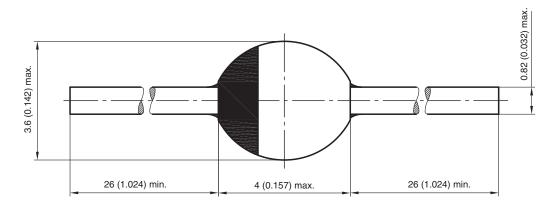


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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