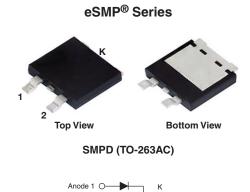


Vishay General Semiconductor

# Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier



Anode 2 O-

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15.0 A			
V <sub>RRM</sub>	170 V			
I <sub>FSM</sub>	260 A			
$V_{F}$ at $I_{F}$ = 5.0 A ( $T_{A}$ = 125 °C)	0.66 V			
T <sub>J</sub> max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configurations	Common cathode			

Cathodde

## **FEATURES**

- AUTOMOTIVE Very low profile - typical height of 1.7 mm
- · Low forward voltage drop, low power losses
- High efficiency operation



COMPLIANT

HALOGEN

FREE

Available

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

## **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test Polarity: As marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER		SYMBOL	V30D170C	UNIT
Device marking code			V30D170C	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	170	V
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	30	٨
	per diode		15	A
Peak forward surge current 8.3 ms single half a superimposed on rated load	sine-wave	I <sub>FSM</sub>	260	A
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +175	°C
Storage temperature range		T <sub>STG</sub>	-55 to +175	°C

Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_1 < 1/R_{0.IA}$ 





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A		V <sub>F</sub> <sup>(1)</sup>	0.72	-	V
	I <sub>F</sub> = 10 A	T <sub>A</sub> = 25 °C		0.78	-	
	I <sub>F</sub> = 15 A			0.8	0.88	
	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.56	-	
	I <sub>F</sub> = 10 A			0.64	-	
	I <sub>F</sub> = 15 A			0.66	0.73	
Reverse current at rated $V_R$ per diode	V <sub>R</sub> = 140 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	mA
		T <sub>A</sub> = 125 °C		1.5	-	mA
	V <sub>R</sub> = 170 V	T <sub>A</sub> = 25 °C		-	0.2	mA
		T <sub>A</sub> = 125 °C		3	10	mA
Typical junction capacitance	4.0 V,	1 MHz	CJ	1200	-	pF

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER SYMBOL V30D170C		V30D170C	UNIT	
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	1.1	°C/W	
	R <sub>0JA</sub> <sup>(2)(3)</sup>	50		

### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub> - junction-to-mount

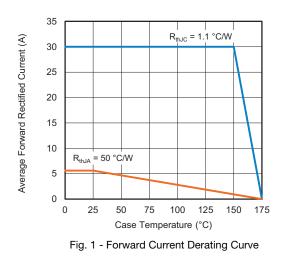
(3) Free air, without heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V30D170C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
V30D170CHM3/I (1)	0.55		2000/reel	13" diameter plastic tape and reel	

#### Note

(1) AEC-Q101 qualified

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)



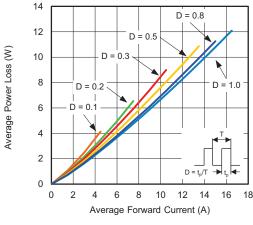


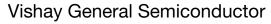
Fig. 2 - Forward Power Loss Characteristics

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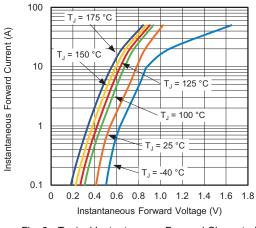


Fig. 3 - Typical Instantaneous Forward Characteristics

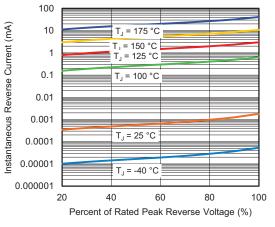


Fig. 4 - Typical Reverse Characteristics

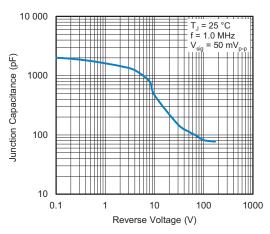


Fig. 5 - Typical Junction Capacitance

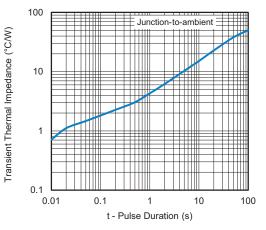


Fig. 6 - Typical Transient Thermal Impedance

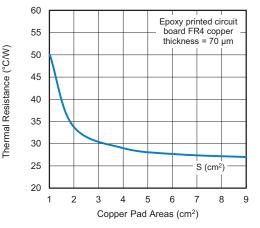


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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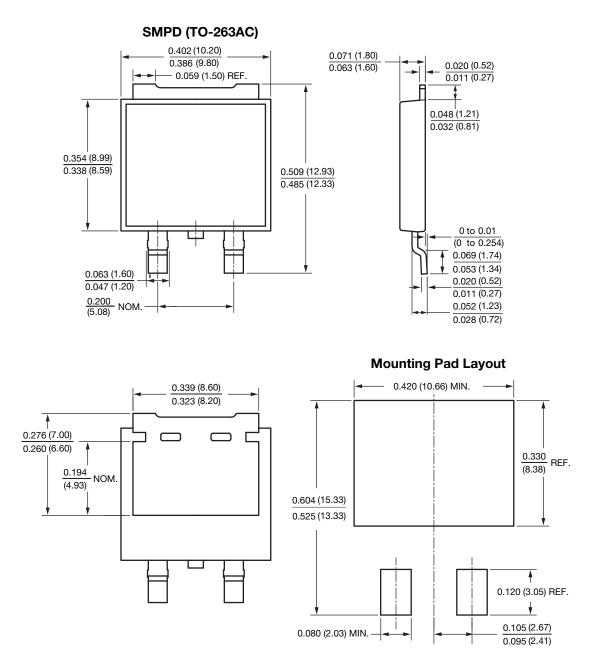
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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