COMPLIANT

HALOGEN FREE

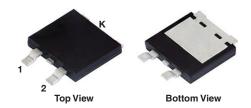


## Vishay General Semiconductor

# Dual Low-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.29 \text{ V}$  at  $I_F = 5.0 \text{ A}$ 

### eSMP<sup>®</sup> Series SMPD (TO-263AC)





#### **DESIGN SUPPORT TOOLS AVAILABLE**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	250 A			
V <sub>F</sub> at I <sub>F</sub> = 20 A (T <sub>A</sub> = 125 °C)	0.40 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V40D45C	UNIT	
Device marking code			V40D45C		
Maximum repetitive peak reverse voltage		$V_{RRM}$	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	40	^	
	per diode		20	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	250	А	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C	
Storage temperature range		T <sub>STG</sub>	-55 to +150		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $<sup>^{(2)}</sup>$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$ 



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.41	-	. v
	$I_F = 10 A$			0.44	-	
	I <sub>F</sub> = 20 A			0.49	0.57	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.29	-	
	I <sub>F</sub> = 10 A			0.33	-	
	I <sub>F</sub> = 20 A			0.4	0.48	
Reverse current per diode	V <sub>R</sub> = 45 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	2.5	- mA
	v <sub>R</sub> = 45 v	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C		19	60	
Typical junction capacitance	4.0 V, 1 MHz		CJ	4500	-	pF

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V40D45C	UNIT	
Typical thermal resistance per device	R <sub>0</sub> JC (1)	0.8	°C/W	
	R <sub>0JA</sub> (2)(3)	45		

#### Notes

- (1) Mounted on infinite heatsink
- (2) The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0,JA</sub>
- (3) Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V40D45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V40D45CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

#### Note

### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

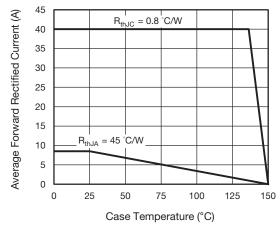


Fig. 1 - Maximum Forward Current Derating Curve

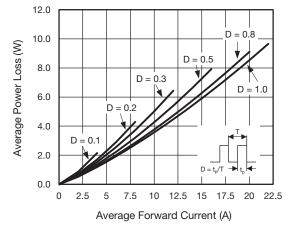


Fig. 2 - Average Power Loss Characteristics

<sup>(1)</sup> AEC-Q101 qualified



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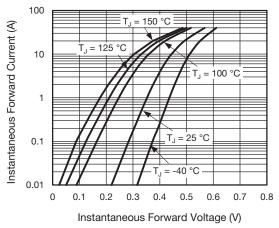


Fig. 3 - Typical Instantaneous Forward Characteristics

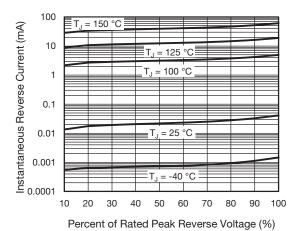


Fig. 4 - Typical Reverse Leakage Characteristics

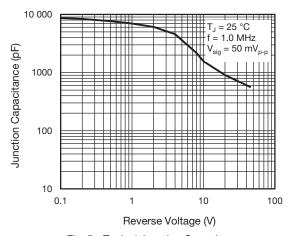


Fig. 5 - Typical Junction Capacitance

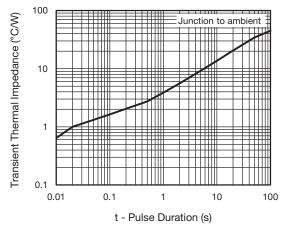


Fig. 6 - Typical Transient Thermal Impedance

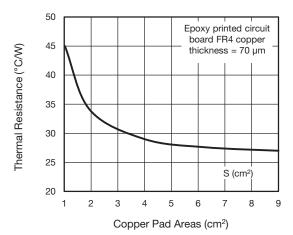
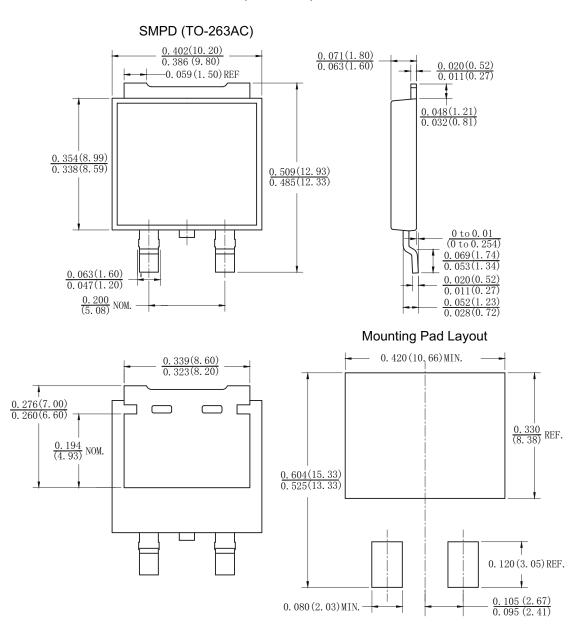


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas



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#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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