AUTOMOTIVE GRADE

COMPLIANT

HALOGEN FREE



## Vishay General Semiconductor

# High Current Density Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.35 \text{ V}$  at  $I_F = 5 \text{ A}$ 





#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	20 A		
$V_{RRM}$	45 V		
I <sub>FSM</sub>	200 A		
$V_F$ at $I_F = 20$ A ( $T_A = 125$ °C)	0.51 V		
T <sub>J</sub> max.	175 °C		
Package	SlimDPAK (TO-252AE)		
Circuit configuration	Single		

#### **FEATURES**

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

#### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20PWM45	UNIT	
Device marking code		V20PWM45		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (Fig. 1)	I <sub>F(AV)</sub> (1)	20	А	
ak forward surge current 8.3 ms single half sine-wave I <sub>FSM</sub>		200	А	
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +175	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +175	°C	

#### Notes

<sup>(1)</sup> With infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$ 



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.47	-	V
	I <sub>F</sub> = 10 A			0.51	-	
	I <sub>F</sub> = 20 A			0.58	0.66	
	$I_F = 5.0 A$	T <sub>A</sub> = 125 °C		0.35	-	
	I <sub>F</sub> = 10 A			0.42	-	
	I <sub>F</sub> = 20 A			0.51	0.59	
Reverse current	V - 45 V	$V_R = 45 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	T <sub>A</sub> = 25 °C	-	0.7	- mA
	V <sub>R</sub> = 45 V		IR (=)	7	20	
Typical junction capacitance	4.0 V, 1 MHz		CJ	3100	-	pF

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu$ s pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	V20PWM45	UNIT		
Typical thermal resistance	R <sub>0</sub> JA (1)(2)	55	°C/W	
	R <sub>0JM</sub> (3)	1.8		

#### Notes

- $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- $^{(3)}$  Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
V20PWM45-M3/I	0.20	I	4500	13" diameter plastic tape and reel	
V20PWM45HM3/I (1)	0.20	I	4500	13" diameter plastic tape and reel	

#### Note

(1) AEC-Q101 qualified

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### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

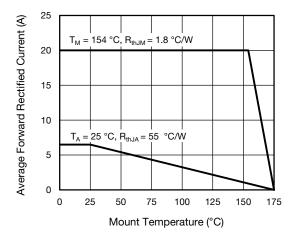


Fig. 1 - Maximum Forward Current Derating Curve

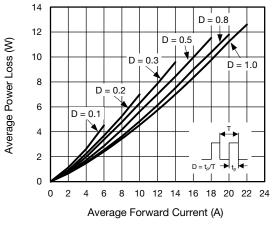


Fig. 2 - Forward Power Loss Characteristics

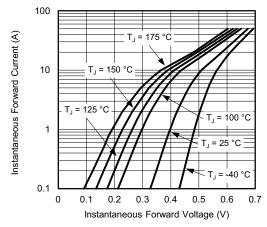


Fig. 3 - Typical Instantaneous Forward Characteristics

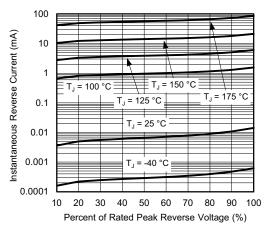


Fig. 4 - Typical Reverse Leakage Characteristics

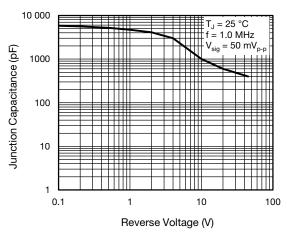


Fig. 5 - Typical Junction Capacitance

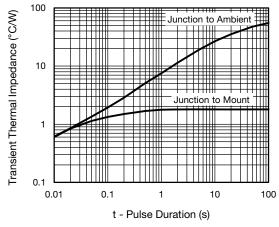


Fig. 6 - Typical Transient Thermal Impedance



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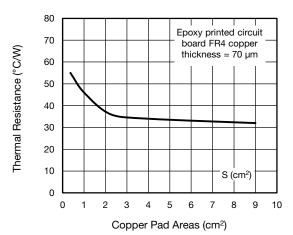
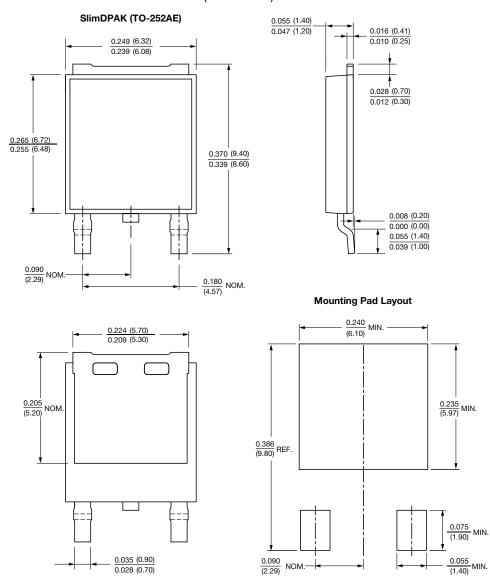


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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