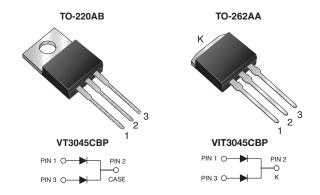


Vishay General Semiconductor

TMBS® (Trench MOS Barrier Schottky) Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.30 \text{ V}$ at $I_F = 5.0 \text{ A}$



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 15 A				
V _{RRM}	45 V				
I _{FSM}	200 A				
V_F at $I_F = 15$ A	0.39 V				
T _{OP} max. (AC mode)	150 °C				
T _J max. (DC forward current)	200 °C				
Package	TO-220AB, TO-262AA				
Circuit configurations	Common cathode				

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

• High efficiency operation

• Solder dip 275 °C max. 10 s, per JESD 22-B106

06 HALOGEN

- T_J 200 °C max. in solar bypass mode application
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: TO-220AB, TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VT3045CBP	VIT3045CBP	UNIT		
Maximum repetitive peak reverse voltage		V_{RRM}	45		V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} (1)	30		А	
	per diode		15			
Peak forward surge current 8.3 ms single half sine-way on rated load per diode	I _{FSM}	200		Α		
Operating junction and storage temperature range (AC mode)		T _{OP} , T _{STG}	-40 to +150		°C	
Junction temperature in DC forward current without reverse bias, $t \leq 1\ h$		T _J ⁽²⁾	≤ 2	00	°C	

Notes

⁽²⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.42	=	V	
	I _F = 7.5 A			0.44	-		
	I _F = 15 A			0.49	0.57		
	I _F = 5 A			0.30	-		
	I _F = 7.5 A			0.33	-		
	I _F = 15 A			0.39	0.48		
Reverse current per diode	V - 45 V	T _A = 25 °C	1 (2)	=	2000	μA	
	$V_R = 45 \text{ V}$ $T_A = 125 \text{ °C}$	I _R ⁽²⁾	17	50	mA		

Notes

⁽¹⁾ With heatsink

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VT3045CBP	VIT3045CBP	UNIT		
Typical thermal resistance	per diode	D	1.6		°C/W	
	per device	$R_{ heta JC}$	0.85			

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	VT3045CBP-M3/4W	1.89	4W	50/tube	Tube		
TO-262AA	VIT3045CBP-M3/4W	1.45	4W	50/tube	Tube		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

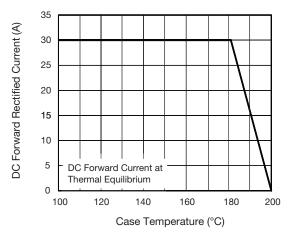


Fig. 1 - Maximum Forward Current Derating Curve

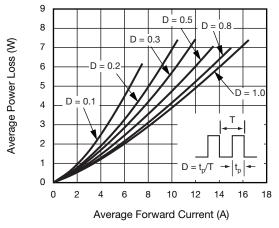


Fig. 2 - Forward Power Loss Characteristics Per Diode

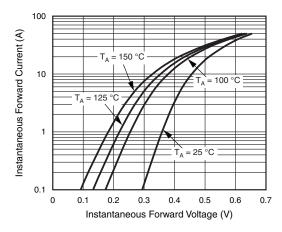


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

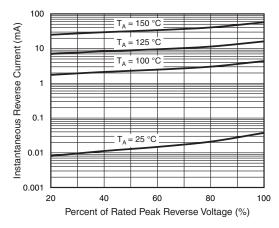


Fig. 4 - Typical Reverse Characteristics Per Diode





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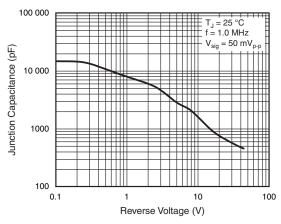


Fig. 5 - Typical Junction Capacitance Per Diode

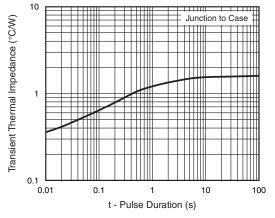
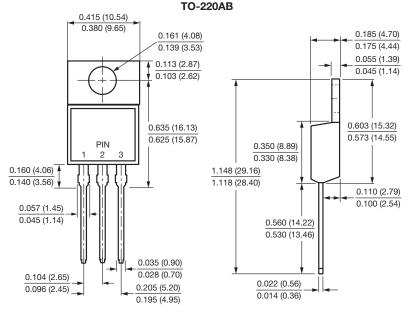
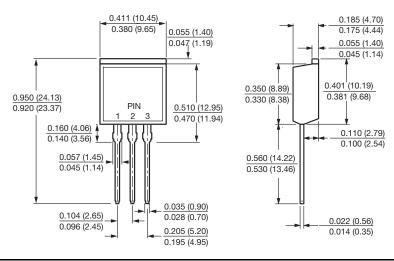


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



TO-262AA





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