IRFP340

Vishay Siliconix



TO-247AC

PRODUCT SUMMARY

 Q_g (max.) (n \overline{C})

V_{DS} (V)

R_{DS(on)} (Ω)

Q_{qs} (nC)

Q_{ad} (nC)

Configuration

Power MOSFET

S

N-Channel MOSFET

0.55

400

62

10

30

Single

 $V_{GS} = 10 V$

FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- · Isolated central mounting hole
- Fast switching
- Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247AC package is preferred for commercial-industrial applications where higher power levels preclude the use of TO-220AB devices. The TO-247AC is similar but superior to the earlier TO-218 package because its isolated mounting hole. It also provides greater creepage distances between pins to meet the requirements of most safety specifications.

ORDERING INFORMATION	
Package	TO-247AC
Lead (Pb)-free	IRFP340PbF

PARAMETER			SYMBOL	LIMIT	UNIT
Drain-source voltage			V _{DS}	400	v
Gate-source voltage			V _{GS}	± 20	v
Continuous drain current Vos at		T _C = 25 °C	I	11	
Continuous drain current V_{GS} at 10 V $T_C = 100 \degree C$			I _D	6.9	А
Pulsed drain Current ^a			I _{DM}	44	
Linear derating factor				1.2	W/°C
Single pulse avalanche energy ^b			E _{AS}	480	mJ
Repetitive avalanche current ^a			I _{AR}	11	А
Repetitive avalanche energy ^a			E _{AR}	15	mJ
Maximum power dissipation	T _C = 25 °C		PD	150	W
Peak diode recovery dV/dt ^c			dV/dt	4.0	V/ns
Operating junction and storage temperature range			T _J , T _{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) ^d for 10 s				300 ^d	U
Mounting torque	6.22 or M2 (COLONIA COLONIA		10	lbf · in
Mounting torque	6-32 or M3 screw			1.1	N ⋅ m

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. V_{DD} = 50 V, starting T_J = 25 °C, L = 6.9 mH, R_g = 25 Ω , I_{AS} = 11 A (see fig. 12)

c. $I_{SD} \le 11$ A, dl/dt ≤ 120 A/µs, $V_{DD} \le V_{DS}$, $T_J \le 150$ °C

d. 1.6 mm from case

S22-0051-Rev. C, 24-Jan-2022

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THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R _{thJA}	-	40	
Case-to-sink, flat, greased surface	R _{thCS}	0.24	-	°C/W
Maximum junction-to-case (drain)	R _{thJC}	-	0.83	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static					•	•	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 2$	50 µA	400	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25	°C, I _D = 1 mA	-	0.49	-	V/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 2$	250 µA	2.0	-	4.0	V
Gate-source leakage	I _{GSS}	$V_{GS} = \pm 20 V$		-	-	± 100	nA
7		$V_{DS} = 400 \text{ V}, \text{ V}_{GS}$	_S = 0 V	-	-	25	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 320 V, V _{GS}	_S = 0 V, T _J = 125 °C	-	-	250	μA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 6.6 A ^b	-	-	0.55	Ω
Forward transconductance	9 _{fs}	$V_{DS} = 50 \text{ V}, \text{ I}_{D} =$	6.6 A ^b	7.7	-	-	S
Dynamic		•			•	•	•
Input capacitance	C _{iss}	V _{GS} = 0 V,		-	1400	-	
Output capacitance	C _{oss}	$V_{DS} = 25 V,$		-	400	-	рF
Reverse transfer capacitance	C _{rss}	f = 1.0 MHz, see	fig. 5	-	130	-	
Total Gate charge	Qg			-	-	62	
Gate-source charge	Q _{gs}	V _{GS} = 10 V	$I_D = 10 \text{ A}, V_{DS} = 320 \text{ V},$ see fig. 6 and 13 ^b	-	-	10	nC
Gate-drain charge	Q _{gd}		See lig. 6 and 16	-	-	30	
Turn-on delay time	t _{d(on)}			-	14	-	
Rise time	t _r	V _{DD} = 200 V, I _D =		-	27	-	
Turn-off delay time	t _{d(off)}	$R_g = 9.1 \Omega, R_D =$	20 Ω , see fig. 10 ^b	-	50	-	ns
Fall time	t _f			-	24	-	
Internal drain inductance	L _D	Between lead,	D J	-	5.0	-	
Internal source inductance	L _S	6 mm (0.25") fro package and ce die contact		-	13	-	nH
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I _S	MOSFET symbo	ID.	-	-	11	
Pulsed diode forward current ^a	I _{SM}	showing the integral reverse p - n junction die	ode	-	-	44	A
Body diode voltage	V _{SD}	$T_{\rm J} = 25 \ ^{\circ}{\rm C}, \ I_{\rm S} = 100 \ ^{\circ}{\rm C}$	11 A, V _{GS} = 0 V ^b	-	-	2.0	V
Body diode reverse recovery time	t _{rr}	T = 05 °C	10 A, dl/dt = 100 A/µs ^b	-	330	660	ns
Body diode reverse recovery charge	Q _{rr}	$I_{\rm J} = 25^{-1}$ C, $I_{\rm F} = 1$	$10 \text{ A}, \text{ al/at} = 100 \text{ A/}\mu\text{S}^{10}$	-	2.5	5.9	μC
Forward turn-on time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L			s and LD)		

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. Pulse width \leq 300 µs; duty cycle \leq 2 %

2



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

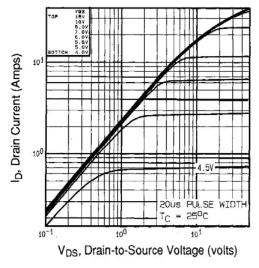


Fig. 1 - Typical Output Characteristics, $T_C = 25$ °C

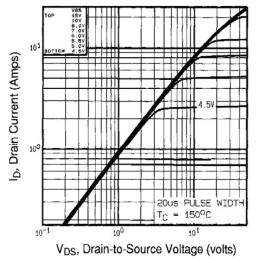


Fig. 2 - Typical Output Characteristics, $T_C = 150$ °C

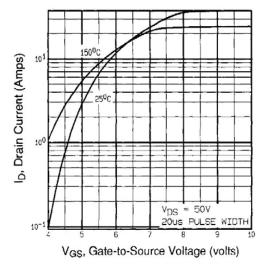


Fig. 3 - Typical Transfer Characteristics

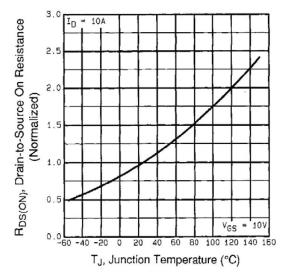


Fig. 4 - Normalized On-Resistance vs. Temperature

3





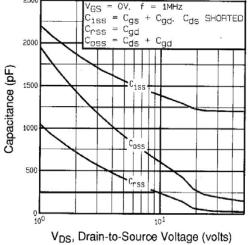


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

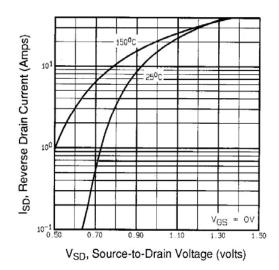


Fig. 7 - Typical Source-Drain Diode Forward Voltage

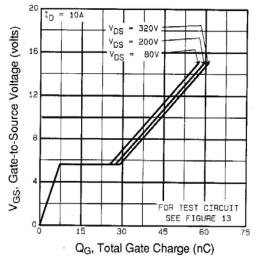
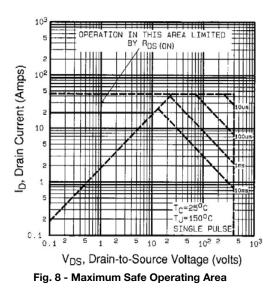


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



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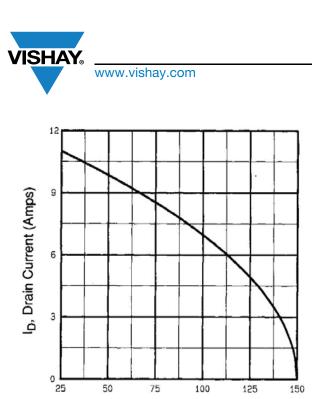
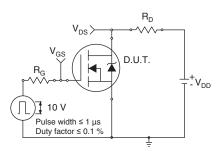


Fig. 9 - Maximum Drain Current vs. Case Temperature

T_C, Case Temperature (°C)



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Fig. 10 - Switching Time Test Circuit

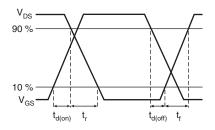


Fig. 11 - Switching Time Waveforms

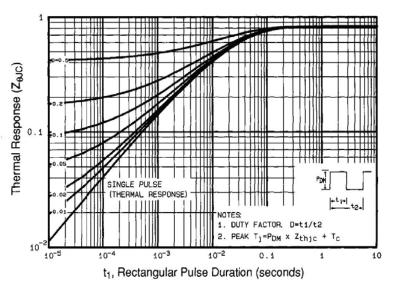


Fig. 12 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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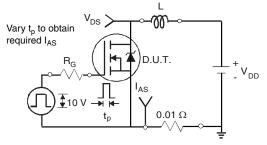


Fig. 13 - Unclamped Inductive Test Circuit

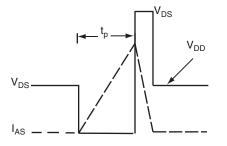
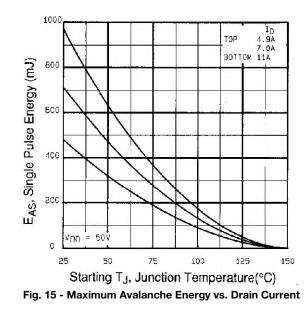
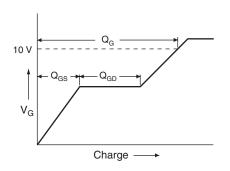


Fig. 14 - Unclamped Inductive Waveforms







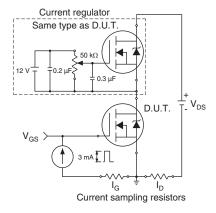
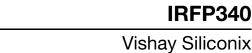


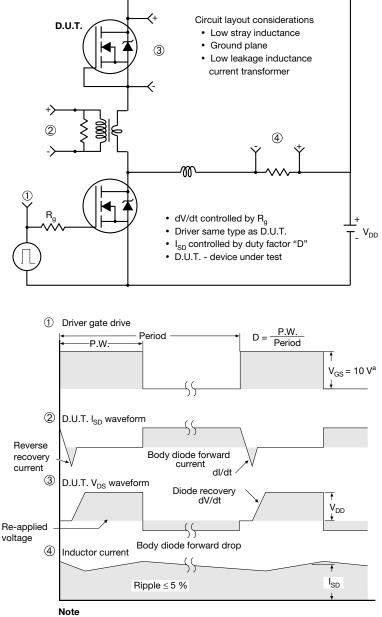
Fig. 17 - Gate Charge Test Circuit

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Peak Diode Recovery dV/dt Test Circuit



a. V_{GS} = 5 V for logic level devices

Fig. 18 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91222.





TO-247AC (High Voltage)

VERSION 1: FACILITY CODE = 9





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	М	ILLIMETERS		
DIM.	MIN.	NOM.	MAX.	NOTES
А	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.17	1.27	1.37	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6, 8
b5	2.87	3.00	3.18	
С	0.40	0.50	0.60	6
c1	0.40	0.50	0.56	
D	20.40	20.55	20.70	4

		MILLIMETERS	S	
DIM.	MIN.	NOM.	MAX.	NOTES
D1	16.46	16.76	17.06	5
D2	0.56	0.66	0.76	
E	15.50	15.70	15.87	4
E1	13.46	14.02	14.16	5
E2	4.52	4.91	5.49	3
е		5.46 BSC		
L	14.90	15.15	15.40	
L1	3.96	4.06	4.16	6
ØР	3.56	3.61	3.65	7
Ø P1		7.19 ref.		
Q	5.31	5.50	5.69	
S		5.51 BSC		

Notes

- ⁽¹⁾ Package reference: JEDEC[®] TO247, variation AC
- (2) All dimensions are in mm
- ⁽³⁾ Slot required, notch may be rounded
- ⁽⁴⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁵⁾ Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- (7) Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition



VERSION 2: FACILITY CODE = Y



	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
A	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	254	
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51	BSC	

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- ⁽²⁾ Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- ⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- ⁽⁷⁾ Outline conforms to JEDEC outline TO-247 with exception of dimension c

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VERSION 3: FACILITY CODE = N



MILLIMETERS		MILLIMETER		MILLIN	IETERS
DIM.	MIN.	MAX.	DIM.	MIN.	MAX
А	4.65	5.31	D2	0.51	1.35
A1	2.21	2.59	E	15.29	15.87
A2	1.17	1.37	E1	13.46	-
b	0.99	1.40	e	5.46	BSC
b1	0.99	1.35	k	0.:	254
b2	1.65	2.39	L	14.20	16.10
b3	1.65	2.34	L1	3.71	4.29
b4	2.59	3.43	N	7.62	BSC
b5	2.59	3.38	Р	3.56	3.66
С	0.38	0.89	P1	-	7.39
c1	0.38	0.84	Q	5.31	5.69
D	19.71	20.70	R	4.52	5.49
D1	13.08	-	S	5.51	BSC

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

⁽²⁾ Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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