

Standard Recovery Diodes, (Stud Version), 380 A



- Wide current range
- High voltage ratings up to 3200 V
- · High surge current capabilities
- Stud cathode and stud anode version
- Standard JEDEC® types
- · Compression bonded encapsulations
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



TYPICAL APPLICATIONS

- Converters
- Power supplies
- · Machine tool controls
- · High power drives
- · Medium traction applications

PRIMARY CHARACTERISTICS			
I _{F(AV)} 380 A			
Package	DO-9 (DO-205AB)		
Circuit configuration	Single		

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VS-SD	UNITS	
PANAIVIETEN	TEST CONDITIONS	16 to 20	25 to 32	UNITS
1		380	380	A
I _{F(AV)}	T _C	100	70	°C
I _{F(RMS)}		595	425	
1	50 Hz	6050	6050	Α
I _{FSM}	60 Hz	6335	6335	
l ² t	50 Hz	183	183	kA ² s
1-1	60 Hz	167	167	KA-S
V _{RRM}	Range	1600 to 2000	2500 to 3200	V
T_J		-40 to +180	-40 to +150	°C

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	$\begin{aligned} & I_{RRM} \text{ MAXIMUM} \\ \text{AT T}_{J} &= T_{J} \text{ MAXIMUM} \\ & \text{mA} \end{aligned}$		
	16	1600	1700			
	20	2000	2100			
VS-SD300N/R	25	2500	2600	15		
	28	2800	2900			
	32	3200	3300			



FORWARD CONDUCTION							
PARAMETER SYMBOL TEST CONDITIONS		DITIONE	SD300N/R		LINUTO		
PARAMETER	STIVIBUL	TEST CONDITIONS		16 to 20	25 to 32	UNITS	
					380	270	Α
Maximum average forward current		180° conduction, half sine wave		100	100	°C	
at case temperature	I _{F(AV)}	100 Cond	uction, nan sine	e wave	300	380	Α
					125	70	°C
Maximum RMS forward current	I _{F(RMS)}	DC at T _C =	88 °C (02 to 2	4), $T_C = 91 ^{\circ}\text{C} (25 \text{ to } 32)$	595	425	
		t = 10 ms	No voltage		6050		
Maximum peak, one-cycle forward,	leo.	t = 8.3 ms	reapplied		6335		A
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}	Sinusoidal half wave, initial T _J = T _J maximum	5090		
		t = 8.3 ms	reapplied		5330		
	l ² t	t = 10 ms	No voltage		183		- kA ² s
Maximum I ² t for fusing		t = 8.3 ms	reapplied		167		
Waximum From tusing		t = 10 ms	100 % V _{RRM}		129		
		t = 8.3 ms	reapplied	118			
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 1	0 ms, no voltaç	ge reapplied	18	30	kA²√s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum 0.95		95	V
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$ 1.05		05	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum				75	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$ 0.66		66	
Maximum forward voltage drop	V_{FM}	$I_{pk} = 1180 \text{ A}, T_J = T_J \text{ maximum},$ $t_p = 10 \text{ ms sinusoidal wave}$		1.83	1.83	V	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER SYMBOL TEST C	CVMDOL	TEST CONDITIONS	SD30	UNITS	
	TEST CONDITIONS	16 to 20	25 to 32		
Maximum junction operating temperature range	TJ	-4		-40 to 150	°C
Maximum storage temperature range	T _{Stg}	-55 to 200		200	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation 0.11		11	K/W
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat, and greased 0.04		04	N/W
Maximum allowed mounting torque ± 10 %		Not-lubricated threads 27		7	Nm
Approximate weight			25	50	g
Case style		See dimensions (link at the end of datasheet)	DO-9	9 (DO-205AI	B)

△R _{thJC} CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.019	0.013			
120°	0.023	0.023			
90°	0.028	0.030	$T_J = T_J$ maximum	K/W	
60°	0.042	0.044			
30°	0.073	0.074			

Note

The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC



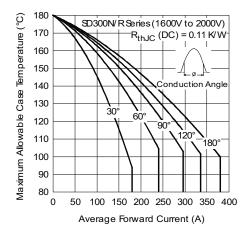


Fig. 1 - Current Ratings Characteristics

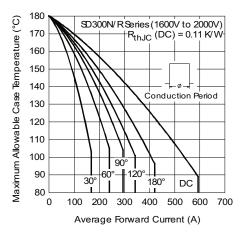


Fig. 2 - Current Ratings Characteristics

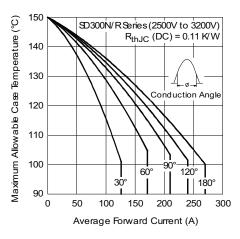


Fig. 3 - Current Ratings Characteristics

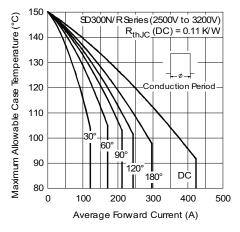


Fig. 4 - Current Ratings Characteristics

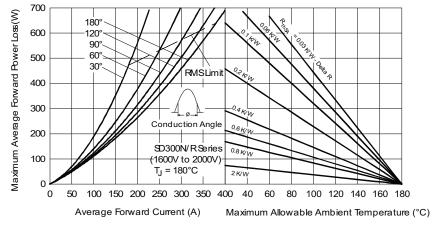


Fig. 5 - Forward Power Loss Characteristics

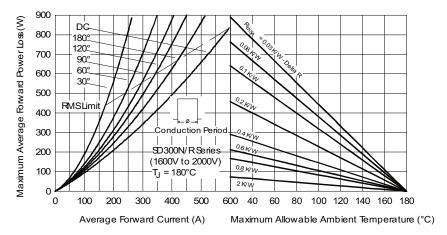


Fig. 6 - Forward Power Loss Characteristics

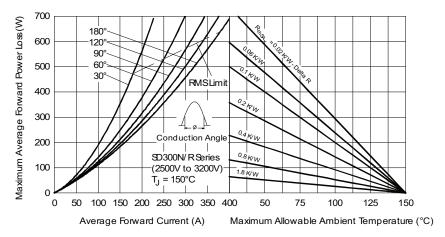


Fig. 7 - Forward Power Loss Characteristics

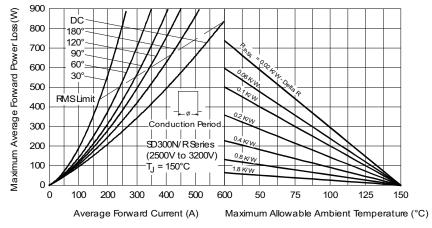


Fig. 8 - Forward Power Loss Characteristics

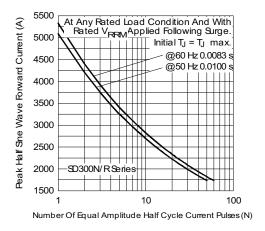


Fig. 9 - Maximum Non-Repetitive Surge Current

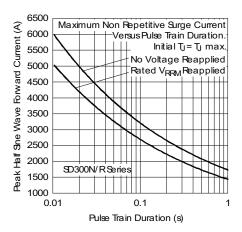


Fig. 10 - Maximum Non-Repetitive Surge Current

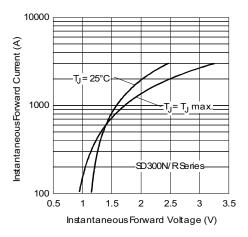


Fig. 11 - Forward Voltage Drop Characteristics

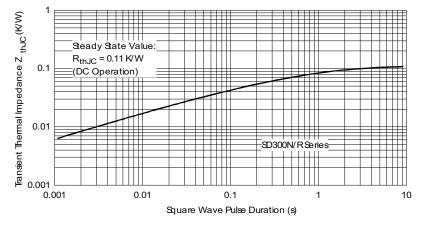
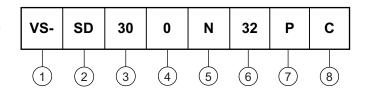


Fig. 12 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code

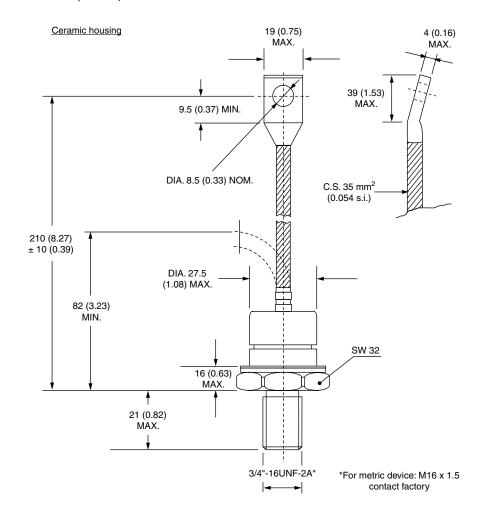


- 1 Vishay semiconductors product
- 2 Diode
- 3 Essential part number
- 4 0 = standard recovery
- 5 • N = stud normal polarity (cathode to stud)
 - R = stud reverse polarity (anode to stud)
- 6 Voltage code x 100 = V_{RRM} (see Voltage Ratings table)
- 7 P = stud base DO-9 (DO-205AB) 3/4" 16UNF-2A
- 8 C = ceramic housing

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95301			

DO-205AB (DO-9)

DIMENSIONS in millimeters (inches)





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