- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-20MQ060TRNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	2	А		
V <sub>RRM</sub>		60	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	40	А		
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.68	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-20MQ060NTRPbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	60	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	00	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS	
Maximum average forward current		50 % duty cycle at $T_C = 110$ °C, r On PC board 9 mm <sup>2</sup> island (0.013		2		
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at $T_{\rm C}$ = 120 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	— A	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	40		
non-repetitive surge current See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	10	A	
Non-repetitive avalanche energy E <sub>A</sub>		T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 4 mH		2.0	mJ	
Repetitive avalanche current		Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		1.0	А	

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www.vishay.com



SMA (DO-214AC)

PRODUCT SUMMARY		
Package	SMA (DO-214AC)	
I <sub>F(AV)</sub>	2 A	
V <sub>R</sub>	60 V	
V <sub>F</sub> at I <sub>F</sub>	See Electrical table	
I <sub>RM</sub>	7.5 mA at 125 °C	
T <sub>J</sub> max.	150 °C	
Diode variation	Single	
E <sub>AS</sub>	2.0 mJ	

# VS-20MQ060NTRPbF

**Vishay Semiconductors** 

RoHS COMPLIANT





## Vishay Semiconductors

ELECTRICAL	SPECIFICATIONS
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PARAMETER	SYMBOL	TES	T CONDITIONS	VALUES	UNITS
		2 A		0.78	V
		1.5 A	T <sub>J</sub> = 25 °C	0.71	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1 A		0.63	
See fig. 1	VFM (1)	2 A		0.68	
		1.5 A	T <sub>J</sub> = 125 °C	0.63	
		1 A		0.57	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V _ Dated V	0.5	mA
		T <sub>J</sub> = 125 °C	$V_{R}$ = Rated $V_{R}$	7.5	
Threshold voltage	V <sub>F(TO)</sub>	TT		0.45	V
Forward slope resistance	r <sub>t</sub>	T <sub>J</sub> = T <sub>J</sub> maximum 86.8		mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$ , $T_J = 25 \ ^\circ C$ , test signal = 1 MHz		31	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

Γ

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (DO-214AC) (similar D-64)	2	Н

#### Note

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ 



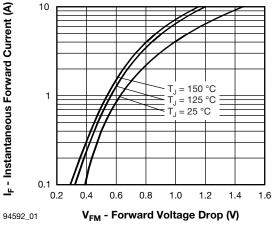


Fig. 1 - Maximum Forward Voltage Drop Characteristics

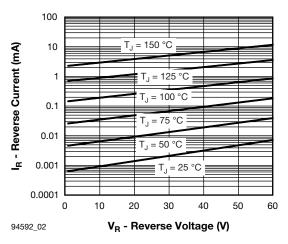


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

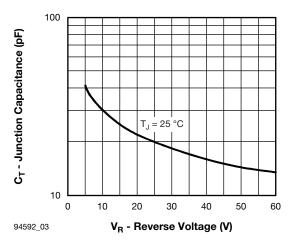


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

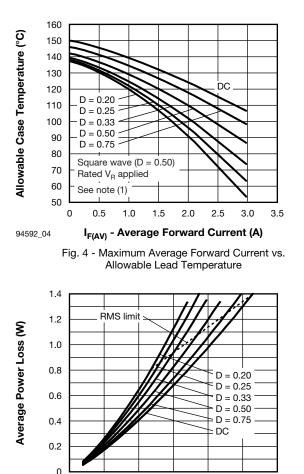
#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

# VS-20MQ060NTRPbF

## **Vishay Semiconductors**





0

0.4

0.8

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

1.2

1.6

2.0

2.4

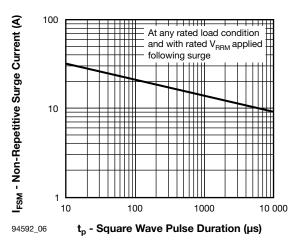


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

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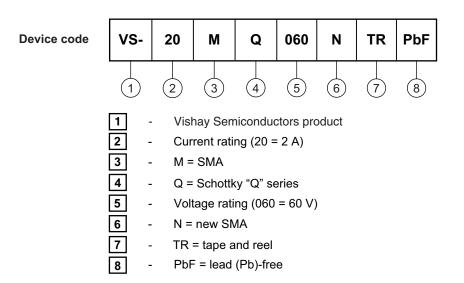
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## **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-20MQ060NTRPbF	5AT	7500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?95396		



## **Outline Dimensions**

## **Vishay Semiconductors**

SMA

### **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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