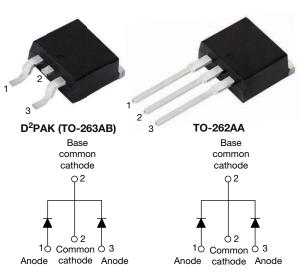


Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 20 A



VS-43CTQ...S-M3

VS-43CTQ...-1-M3

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _R	80 V, 100 V			
V _F at I _F	0.67 V			
I _{RM} max.	11 mA at 125 °C			
T _J max.	175 °C			
E _{AS}	7.50 mJ			
Package	D ² PAK (TO-263AB), TO-262AA			
Circuit configuration	Common cathode			

FEATURES

• High

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop



- HALOGEN high purity, temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	А		
V _{RRM}		80/100	V		
I _{FSM}	t _p = 5 μs sine	850	А		
V _F	20 A_{pk} , $T_J = 125 \ ^{\circ}C$ (per leg)	0.67	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-43CTQ080S-M3 VS-43CTQ080-1-M3	VS-43CTQ100S-M3 VS-43CTQ100-1-M3	UNITS
Maximum DC reverse voltage	V _R	80	100	V
Maximum working peak reverse voltage	V _{RWM}	00	100	v

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ABSOLUTE MAXIN	UM RATINO	às				
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average	per leg				20	
forward current See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 135 °C	, rectangular waveform	40	А
Maximum peak one cycle	non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	850	A
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	275	
Non-repetitive avalanche	energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 0.50 \ A, \ L = 60$	mH	7.50	mJ
Repetitive avalanche curre	ent per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T_J maxim		0.50	А

ELECTRICAL SPECIFICATION	S				
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		20 A	T ₁ = 25 °C	0.81	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 0	0.98	0.81 0.98 V 0.67 0.81 V 1 mA 0.71 0.71 V V 0.43 mΩ 1480 pF 8.0 nH
See fig. 1	VFM V	20 A	T _ 105 °C	0.67	
		40 A	T _J = 125 °C	0.81	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	m۸
See fig. 2	IRM (17	T _J = 125 °C	VR - Haleu VR	11	ШA
Threshold voltage	V _{F(TO)}	$T_{.1} = T_{.1}$ maximum		0.71	V
Forward slope resistance	r _t	ij = ij maximum		0.43	mΩ
Maximum junction capacitance per leg	CT	V _R = 5 V _{DC} (test signal range	e 100 kHz to 1 MHz), 25 °C	1480	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mr	n from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

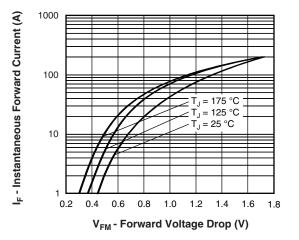
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

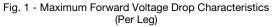
THERMAL - MECHA	NICAL SPI	ECIFICAT	IONS			
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage	ge	T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance junction to case per leg	,	D		2.0		
Maximum thermal resistance junction to case per package	,	R _{thJC}	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approvimate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
			C_{2222} at the $D^2 DAK (TO, 262AD)$	43CT0	2080S	
Marking davias	Case style D ² PAK (TO-263AB) 43CT0		Q100S			
Marking device			Case style TO 26244	43CTC	080-1	
			Case style TO-262AA	43CTC	43CTQ100-1	



VS-43CTQ...S-M3, VS-43CTQ...-1-M3 Series

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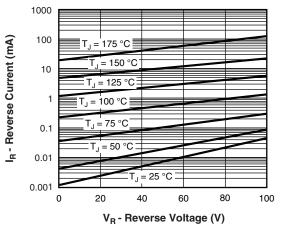


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

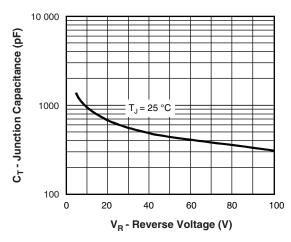
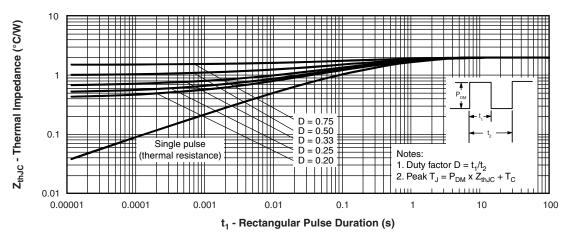


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

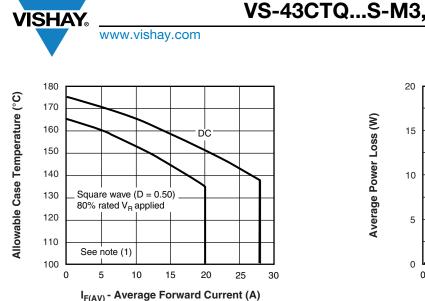


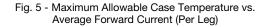


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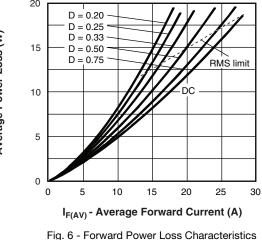
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(Per Leg)

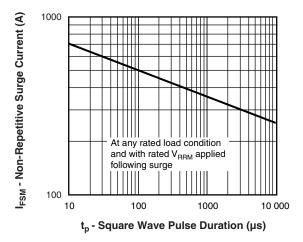


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

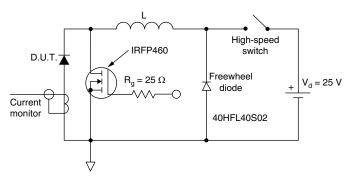


Fig. 8 - Unclamped Inductive Test Circuit

Note

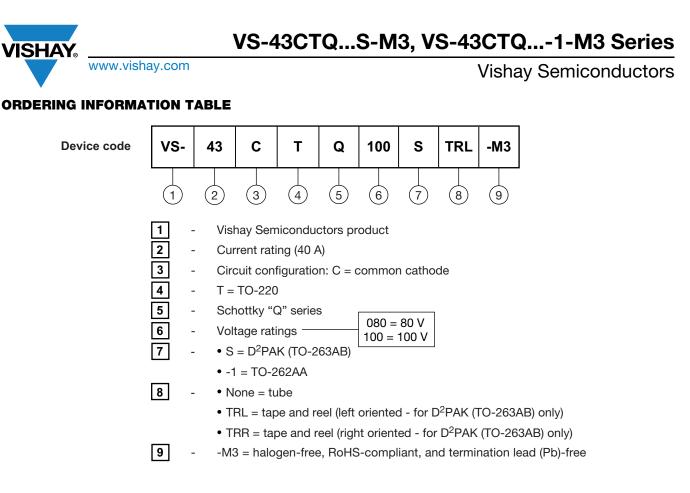
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \; \mathsf{power} \; \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \; \mathsf{x} \; \mathsf{V_{\mathsf{FM}}} \; \mathsf{at} \; (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \; (\mathsf{see} \; \mathsf{fig.} \; \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \; \mathsf{power} \; \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \; \mathsf{x} \; \mathsf{I}_{\mathsf{R}} \; (\mathsf{1} - \mathsf{D}); \; \mathsf{I}_{\mathsf{R}} \; \mathsf{at} \; \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \; \mathsf{V} \\ \end{array}$

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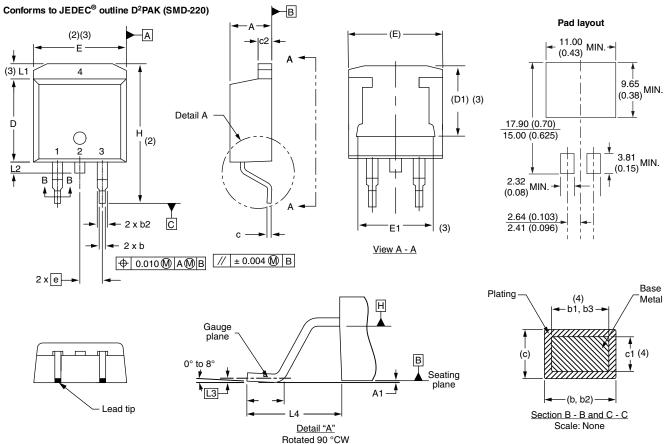
ORDERING INFORMATION		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-43CTQ100S-M3	50	Antistatic plastic tubes
VS-43CTQ100STRL-M3	800	13" diameter plastic tape and reel
VS-43CTQ100STRR-M3	800	13" diameter plastic tape and reel
VS-43CTQ100-1-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS					
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164			
Dimensions	TO-262AA	www.vishay.com/doc?96165			
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444			
Fart marking mormation	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			
SPICE model		www.vishay.com/doc?95065			

Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	OL MILLIMETERS INCHES				NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	2.54 BSC		0.100 BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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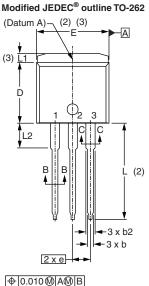
Outline Dimensions

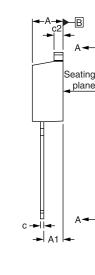


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TO-262AA

DIMENSIONS in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14 1.78		0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
 ⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis of the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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