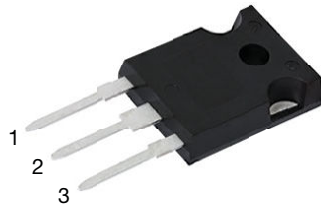
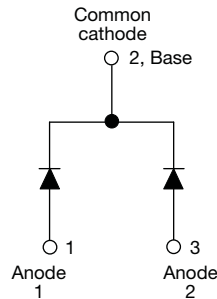


## Ultrafast Rectifier, FRED Pt<sup>®</sup>, 2 x 30 A


**TO-247AC 3L**


### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

### DESCRIPTION / APPLICATIONS

This series is the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, welding, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters, and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

### PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 30 A
$V_R$	300 V
$V_F$ at $I_F$	0.9 V
$t_{rr}$ (typical)	50 ns
$T_J$ max.	175 °C
Package	TO-247AC 3L
Circuit configuration	Common cathode

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		300	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 133\text{ °C}$	30	A
per leg			60	
Non-repetitive peak surge current per leg	$I_{FSM}$	$T_J = 25\text{ °C}$ , $t_p = 10\text{ ms}$	300	
Operating junction and storage temperature range	$T_J, T_{Stg}$		-55 to +175	°C

### ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	300	-	-	V
Forward voltage	$V_F$	$I_F = 30\text{ A}$	-	1.03	1.2	
		$I_F = 30\text{ A}$ , $T_J = 125\text{ °C}$	-	0.9	1.0	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	0.1	5	$\mu\text{A}$
		$T_J = 125\text{ °C}$ , $V_R = V_R$ rated	-	15	100	
Junction capacitance	$C_T$	$V_R = 300\text{ V}$	-	40	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	3.5	-	nH



DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, di <sub>F</sub> /dt = 50 A/μs, V <sub>R</sub> = 30 V		-	38	-	ns
		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 200 V	-	50	-	
		T <sub>J</sub> = 125 °C		-	77	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 200 V	-	5.3	-	A
		T <sub>J</sub> = 125 °C		-	11.3	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 200 V	-	130	-	nC
		T <sub>J</sub> = 125 °C		-	440	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.9	1.1	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	40	
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.4	-	
Weight			-	6.0	-	g
			-	0.22	-	oz.
Mounting torque			6.0 (12)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 3L	60CPU03W			

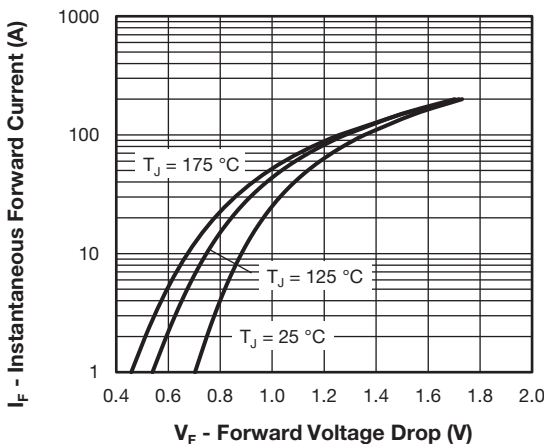


Fig. 1 - Typical Forward Voltage Drop Characteristics

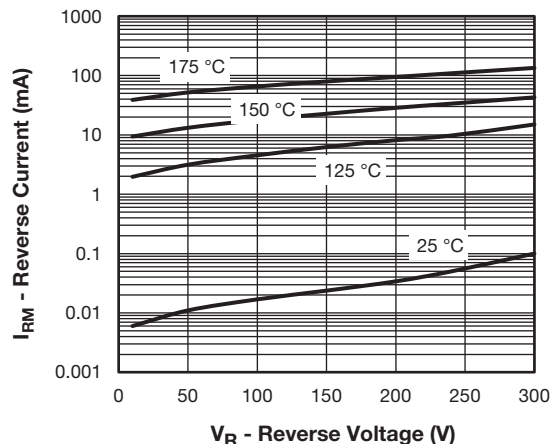


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

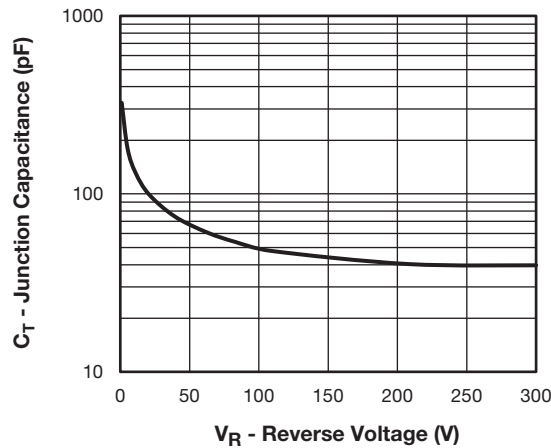


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

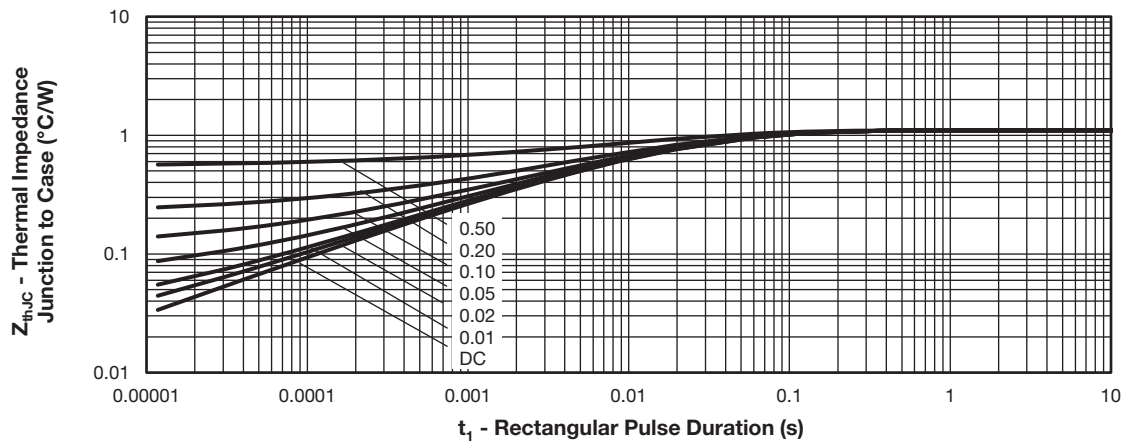


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

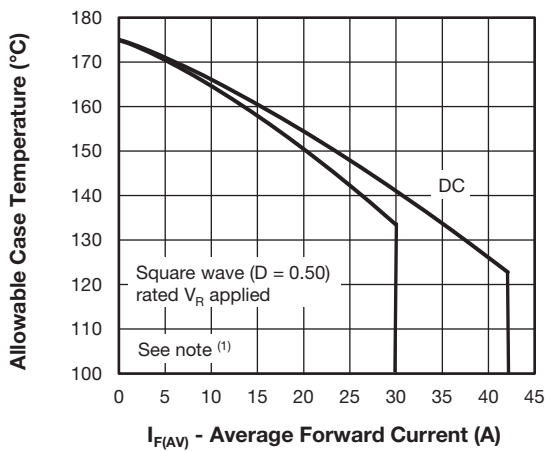


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

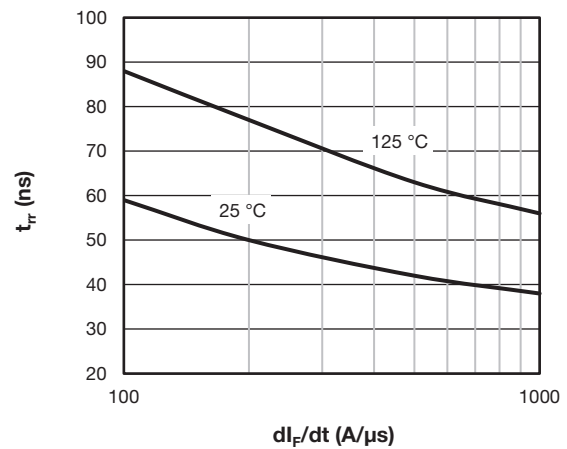


Fig. 6 - Typical Reverse Recovery Time vs.  $di_F/dt$

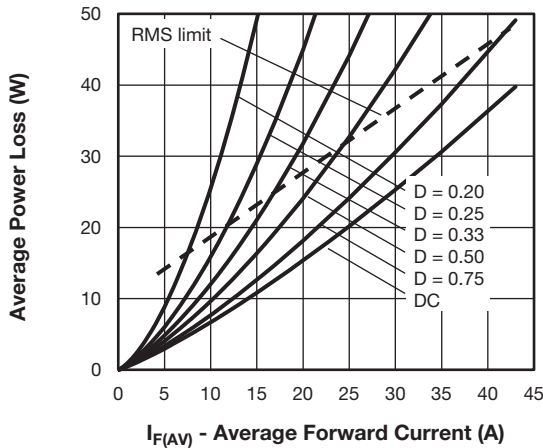


Fig. 7 - Forward Power Loss Characteristics

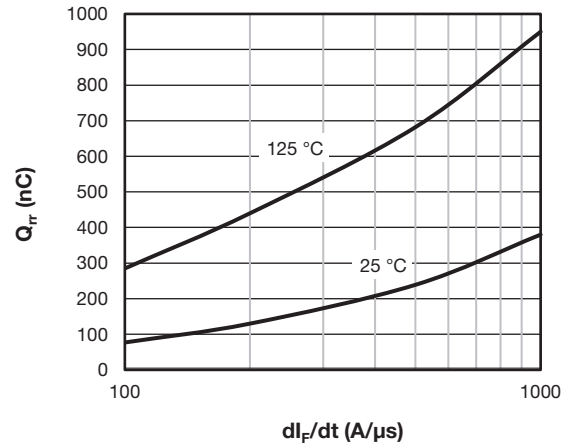
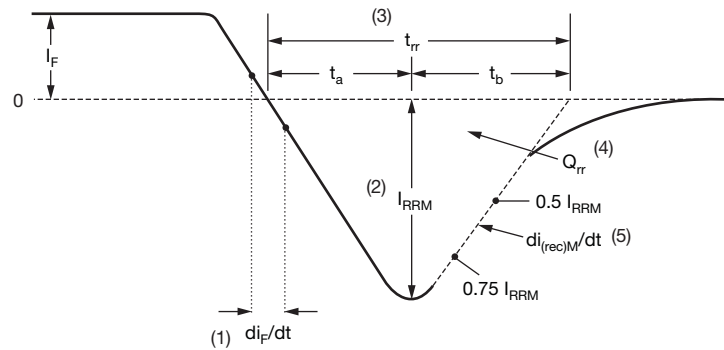


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $Pd$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);
- $Pd_{REV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

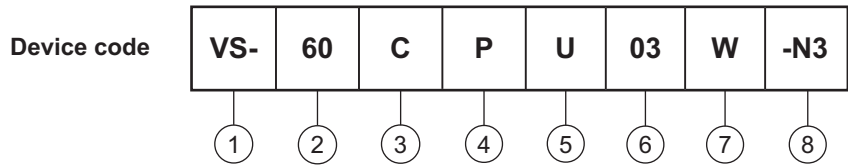


- (1)  $di_F/dt$  - rate of change of current through zero crossing
  - (2)  $I_{RRM}$  - peak reverse recovery current
  - (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
  - (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
- (5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Current rating (60 = 60 A)
- 3** - Circuit configuration: C = common cathode
- 4** - P = TO-247AC
- 5** - U = ultrafast rectifier
- 6** - Voltage code (03 = 300 V)
- 7** - Special
- 8** - Environmental digit:  
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER PACKAGE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-60CPU03W-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?96138">www.vishay.com/doc?96138</a>
Part marking information	<a href="http://www.vishay.com/doc?95007">www.vishay.com/doc?95007</a>





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