VS-8TQ060-M3, VS-8TQ080-M3, VS-8TQ100-M3

Vishay Semiconductors

High Performance Schottky Rectifier, 8 A



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PRIMARY CHARACTERISTICS					
I _{F(AV)}	8 A				
V _R	60 V, 80 V, 100 V				
V _F at I _F	0.58 V				
I _{RM} max.	7 mA at 125 °C				
T _J max.	175 °C				
E _{AS}	7.5 mJ				
Package	TO-220AC 2L				
Circuit configuration	Single				

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL CHARACTERISTICS VALUES UN					
I _{F(AV)}	Rectangular waveform	8	А		
V _{RRM}	Range	60 to 100	V		
I _{FSM}	t _p = 5 μs sine	850	А		
VF	8 A _{pk} , T _J = 125 °C	0.58	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-8TQ060-M3	VS-8TQ080-M3	VS-8TQ100-M3	UNITS	
Maximum DC reverse voltage	V _R	60	80	100	V	
Maximum working peak reverse voltage	V _{RWM}	00	00	100	v	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward current, see fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 157$ °C	8	А			
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	А		
surge current, see fig. 7		10 ms sine or 6 ms rect. pulse			~		
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 60 \text{ mH}$		7.50	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxim	0.50	А			

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		8 A	T.I = 25 °C	0.72	V	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	16 A	1j=25 C	0.88		
	¥FM ⁽¹⁾	8 A	T 105 %C	0.58		
		16 A	T _J = 125 °C	0.69		
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = rated $V_{\rm B}$	0.55	mA	
See fig. 2	IRM ("	T _J = 125 °C	$v_{\rm R} = rateu v_{\rm R}$	7	IIIA	
Maximum junction capacitance	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		500	pF	
Typical series inductance	L _S	Measured lead to lead 5 m	8	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	TJ, T _{Stg}		-55 to +175	°C			
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	2.0	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50	0,11			
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque			6 (5)	kgf ⋅ cm			
Mounting torque maximum			12 (10)	(lbf ⋅ in)			
			8TC	060			
Marking device		Case style TO-220AC 2L	8TQ080				
			8TQ100				



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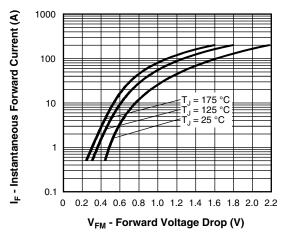


Fig. 1 - Maximum Forward Voltage Drop Characteristics

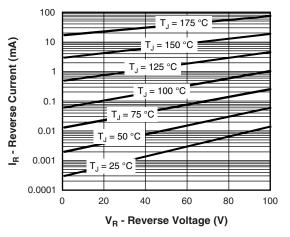


Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage

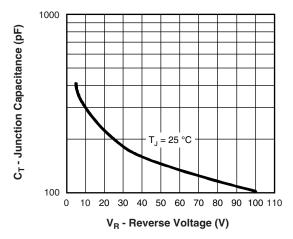


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

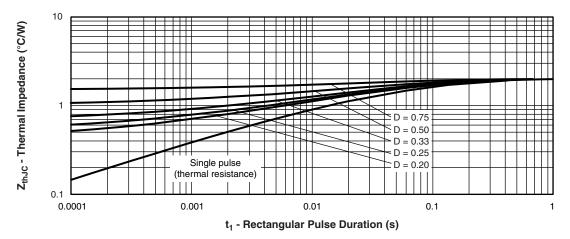
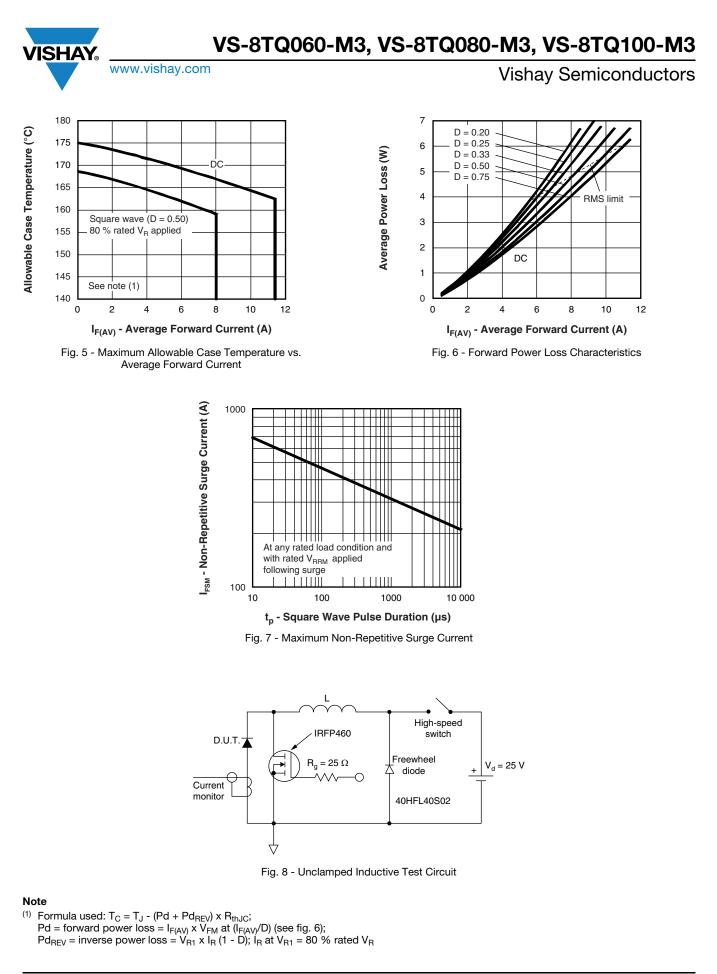


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE

Device code	vs	1	8	т	Q	100	-M3	
)	2	3	4	5	6	J
	1	-	Visl	nay Sen	niconduc	ctors pro	oduct	
	2	-	Cur	rent rati	ng (8 = 8	8 A)		
	3	-	Pac	kage:				
			T =	TO-220				
	4	-	Sch	ottky "C	" series		Г	060 = 60 \
	5	-	Volt	age rati	ngs —			080 = 80 \
	6	-	Env	vironmer	ntal digit			100 = 100
			Ma	- halor	non froo	DALC	compli	ant and to

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-8TQ060-M3	50	Antistatic plastic tubes				
VS-8TQ080-M3	50	Antistatic plastic tubes				
VS-8TQ100-M3	50	Antistatic plastic tubes				

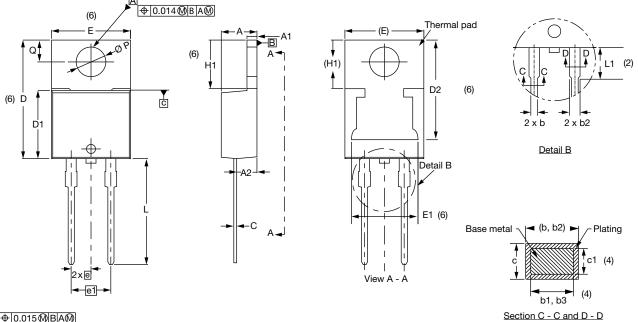
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96156			
Part marking information	www.vishay.com/doc?95391			
SPICE model	www.vishay.com/doc?96227			



Vishay Semiconductors

TO-220AC 2L

DIMENSIONS in millimeters and inches



⊕0.015@BA@



SYMBOL	MILLIMETERS INCHES		NOTES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

Conforms to JEDEC	® outline TO-220AC

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

⁽⁴⁾ Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2, and E1
- ⁽⁷⁾ Outline conforms to JEDEC[®] TO-220, except D2

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⁽³⁾ Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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Revision: 01-Jan-2024