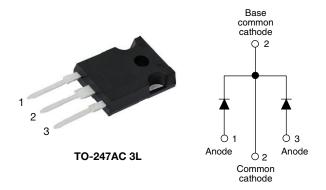


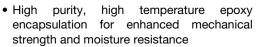
# High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
$V_{R}$	60 V				
V <sub>F</sub> at I <sub>F</sub>	0.62 V				
I <sub>RM</sub> typ.	100 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	13 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-MBR4060WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	Α			
$V_{RRM}$		60	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	1020	Α			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.62	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-MBR4060WT-N3	UNITS
Maximum DC reverse voltage	$V_R$	60	V
Maximum working peak reverse voltage	$V_{RWM}$	60	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per leg		1	T 100 %C 50 %/ distributed and an extensive section of the section		20		
forward current	per device	I <sub>F(AV)</sub>	T <sub>C</sub> = 108 °C, 50 % duty cycle, rectangular waveform		40	A	
Maximum peak one cycle	Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	1020		
non-repetitive surge current per leg		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	265		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.5 A, L = 11.5 mH		13	mJ	
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.5	Α	





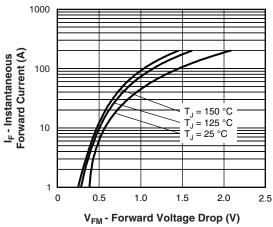
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drap	V (1)	20 A	T <sub>J</sub> = 25 °C	0.72	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	20 A	T <sub>J</sub> = 125 °C	0.62	V	
Maximum instantaneous reverse current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	Rated DC voltage	1.0	mA	
iviaximum instantaneous reverse current		T <sub>J</sub> = 125 °C	hated DC voltage	160		
Typical reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 125 ^{\circ}\text{C}$ $V_R = \text{Rated } V_R$		100	mA	
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> , (test signal range 100 kHz to 1 MHz) 25 °C		720	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of terminal to mounting plane		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>			V/µs	

### Note

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	2.20		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	1.10	°C/W	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>	DC operation	50		
Approximate weight				6	g	
Approximate weight				0.21	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
Marking device			Case style TO-247AC 3L	MBR40	D60WT	







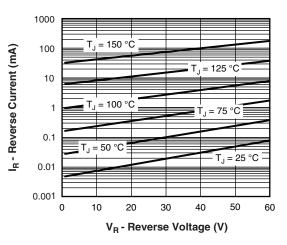


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

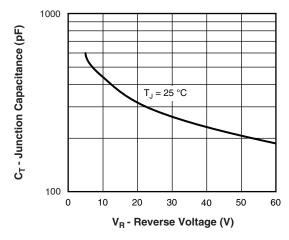


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

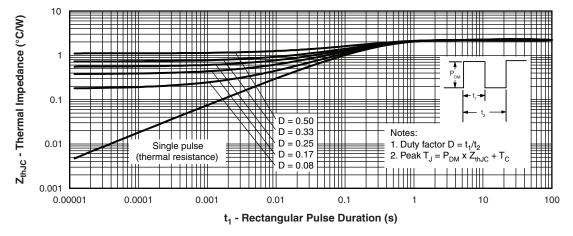


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

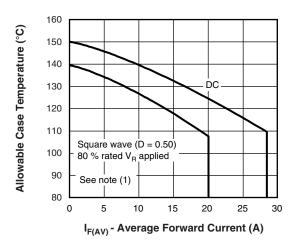


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

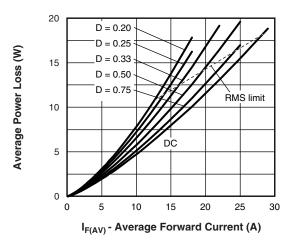


Fig. 6 - Forward Power Loss Characteristics

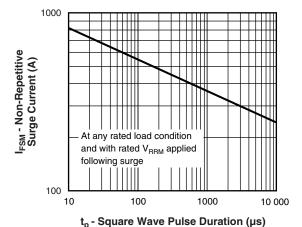


Fig. 7 - Maximum Non-Repetitive Surge Current

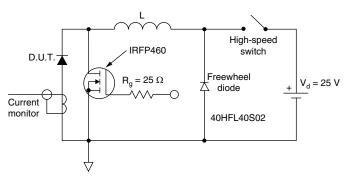


Fig. 8 - Unclamped Inductive Test Circuit

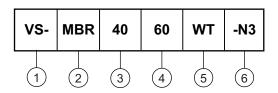
### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



### **ORDERING INFORMATION TABLE**

Device code



- 1 Vishay Semiconductors product
- 2 Schottky MBR series
- Current rating (40 = 40 A)
  - Voltage rating (60 = 60 V)
- 5 Circuit configuration:
  Center tap (dual) TO-247
- 6 Environmental digit
  - -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBR4060WT-N3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96138</u>					
Part marking information	www.vishay.com/doc?95007				



### **TO-247AC 3L**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254		)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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