RoHS

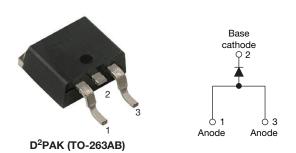
COMPLIANT HALOGEN

FREE



Vishay Semiconductors

High Voltage Surface Mount Input Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS							
I _{F(AV)} 10 A							
V_{R}	1200 V						
V _F at I _F	1.1 V						
I _{FSM}	160 A						
T _J max.	150 °C						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						

FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- On-board and off-board EV / HEV battery chargers

DESCRIPTION

The VS-10ETS12SLHM3 rectifier series has been optimized for very low forward voltage drop, with moderate leakage.

OUTPUT CURRENT IN TYPICAL APPLICATIONS								
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS								
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	А					

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Sinusoidal waveform	10	Α						
V _{RRM}		1200	V						
I _{FSM}		160	Α						
V_{F}	10 A, T _J = 25 °C	1.1	V						
T _J		-40 to +150	°C						

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-10ETS12SLHM3	1200	1300	0.5



ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180° conduction half sine wave	10					
Maximum peak one cycle non-repetitive surge current	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	135	А				
		10 ms sine pulse, no voltage reapplied	160					
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	91	A ² s				
waximum i-t for fusing		10 ms sine pulse, no voltage reapplied	applied 130					
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1290	A²√s				

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST (CONDITIONS	VALUES	UNITS			
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C	1.1	V				
Forward slope resistance	r _t	T _{.1} = 150 °C	20	mΩ				
Threshold voltage	V _{F(TO)}	1J = 150 C	0.82	V				
Maximum rayaraa laakaga ayrrant		T _J = 25 °C	V - rotod V	0.05	mΛ			
Maximum reverse leakage current	IRM	T _J = 150 °C	V _R = rated V _{RRM}	0.50	mA			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W				
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} (1)		62	C/VV				
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Marking device		Case style D ² PAK (TO-263AB)	10ETS	12SH				

Note

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W.

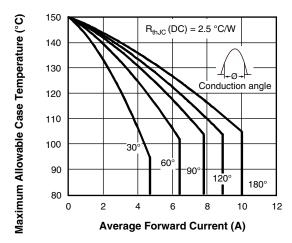


Fig. 1 - Current Rating Characteristics

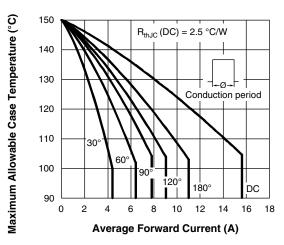


Fig. 2 - Current Rating Characteristics

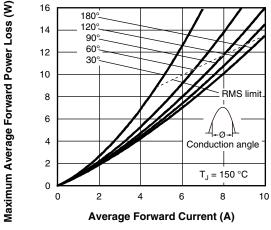


Fig. 3 - Forward Power Loss Characteristics

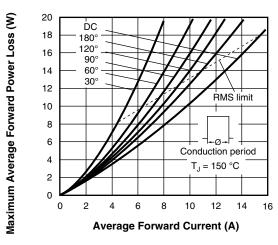


Fig. 4 - Forward Power Loss Characteristics

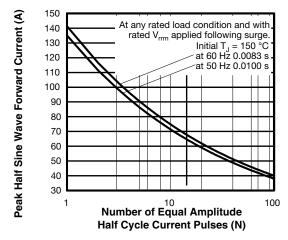


Fig. 5 - Maximum Non-Repetitive Surge Current

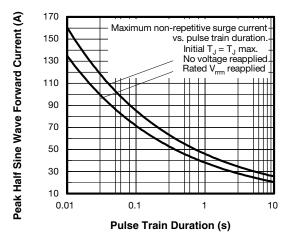


Fig. 6 - Maximum Non-Repetitive Surge Current

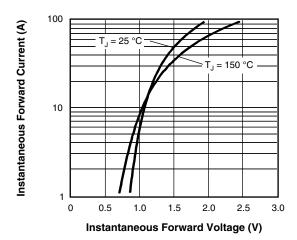


Fig. 7 - Forward Voltage Drop Characteristics

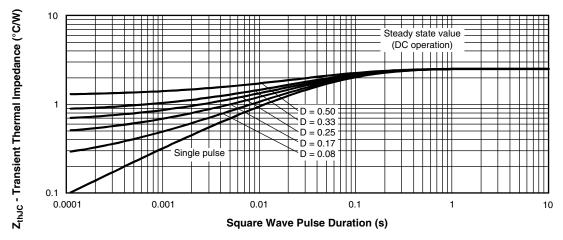
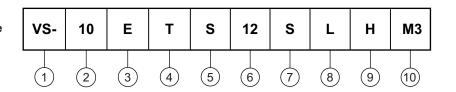


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semicondutors product
- 2 Current rating (10 = 10 A)
- 3 Circuit configuration:

E = single

4 - Package:

 $T = D^2PAK (TO-263AB)$

5 - Type of silicon:

S = standard recovery rectifier

- 7 S = surface mountable
- L = tape and reel (left oriented), for different orientation, contact factory
- 9 H = AEC-Q101 qualified
- 10 Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	PACKAGING DESCRIPTION							
VS-10ETS12SLHM3	800	800	13" diameter reel					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?96317					



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	INCHES		NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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Vishay

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