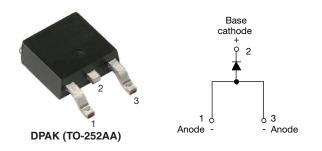
Vishay Semiconductors

# Surface Mount Fast Soft Recovery Rectifier Diode, 8 A



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SHAY

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	8 A				
V <sub>R</sub>	1200 V				
V <sub>F</sub> at I <sub>F</sub>	1.3 V				
I <sub>FSM</sub>	150 A				
t <sub>rr</sub>	80 ns				
T <sub>J</sub> max.	150 °C				
Package	DPAK (TO-252AA)				
Circuit configuration	Single				
Snap factor	0.6				

#### FEATURES

- Glass passivated pellet chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Flexible solution for reliable AC power rectification
- High surge, low  $V_{\text{F}}$  rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### APPLICATIONS

- On-board and off-board EV / HEV battery chargers
- Renewable energy inverters

#### DESCRIPTION

The VS-8EWF12SLHM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time, low forward voltage drop and low leakage current.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Sinusoidal waveform	8	A		
V <sub>RRM</sub>		1200	V		
I <sub>FSM</sub>		150	A		
V <sub>F</sub>	8 A, T <sub>J</sub> = 25 °C	1.3	V		
t <sub>rr</sub>	1 A, 100 A/µs	80	ns		
TJ	Range	-40 to +150	°C		

VOLTAGE RATINGS			
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> AT 150 °C mA
VS-8EWF12SLHM3	1200	1300	4

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I <sub>F(AV)</sub>	$T_{C}$ = 96 °C, 180° conduction half sine wave	8		
Maximum peak one cycle	I <sub>FSM</sub>	10 ms sine pulse, rated V <sub>RRM</sub> applied	125	А	
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	150		
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated V <sub>RRM</sub> applied	78	A <sup>2</sup> s	
Maximum 1-t for fusing		10 ms sine pulse, no voltage reapplied 110		A-5	
Maximum I <sup>2</sup> √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1100	A²√s	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		UNITS
Maximum forward voltage drop	V <sub>FM</sub>	8 A, T <sub>J</sub> = 25 °C		1.3	V
Forward slope resistance	r <sub>t</sub>	T.I = 150 °C		25.6	mΩ
Threshold voltage	V <sub>F(TO)</sub>	1j = 150 C		0.93	V
Maximum reverse leakage current	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$		$V_{B} = Rated V_{BBM}$	0.1	mA
Maximum reverse leakage current	IRM	T <sub>J</sub> = 150 °C	VR = naieu VRRM	4	ША

RECOVERY CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	· •	
Reverse recovery time	t <sub>rr</sub>	In at 8 Aug	270	ns	I <sub>FM</sub>	
Reverse recovery current	I <sub>rr</sub>	I <sub>F</sub> at 8 A <sub>pk</sub> 25 A/μs	4.2	А		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	1	μC		
Snap factor	S		0.6			

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-40 to +150	°C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	2.5	°C/W	
Typical thermal resistance, junction to ambient (PCB mount)	R <sub>thJA</sub> <sup>(1)</sup>		50		
An an an internation of the state of the sta			1	g	
Approximate weight			0.03	oz.	
Marking device		Case style DPAK (TO-252AA) 8EWF12SH		12SH	

#### Note

(1) When mounted on 1" square (650 mm<sup>2</sup>) PCB of FR-4 or G-10 material 4 oz. (140 µm) copper 40 °C/W

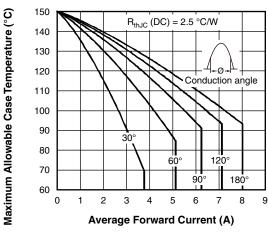


Fig. 1 - Current Rating Characteristics

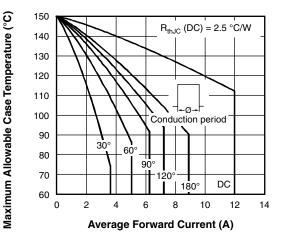


Fig. 2 - Current Rating Characteristics

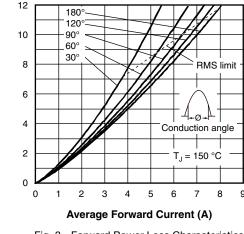
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Fig. 3 - Forward Power Loss Characteristics

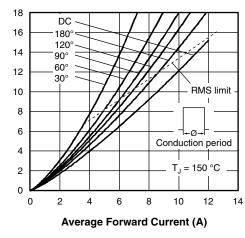


Fig. 4 - Forward Power Loss Characteristics

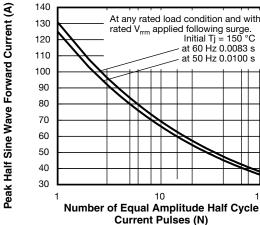


Fig. 5 - Maximum Non-Repetitive Surge Current

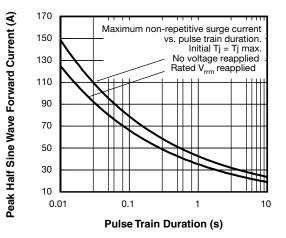


Fig. 6 - Maximum Non-Repetitive Surge Current

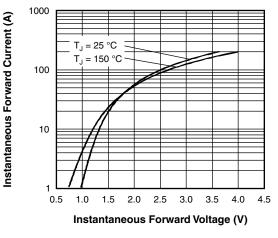


Fig. 7 - Forward Voltage Drop Characteristics

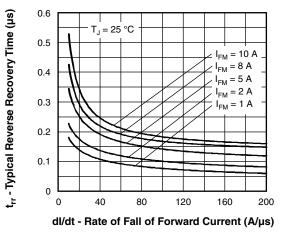


Fig. 8 - Recovery Time Characteristics, T<sub>J</sub> = 25 °C

Maximum Average Forward Power Loss (W)

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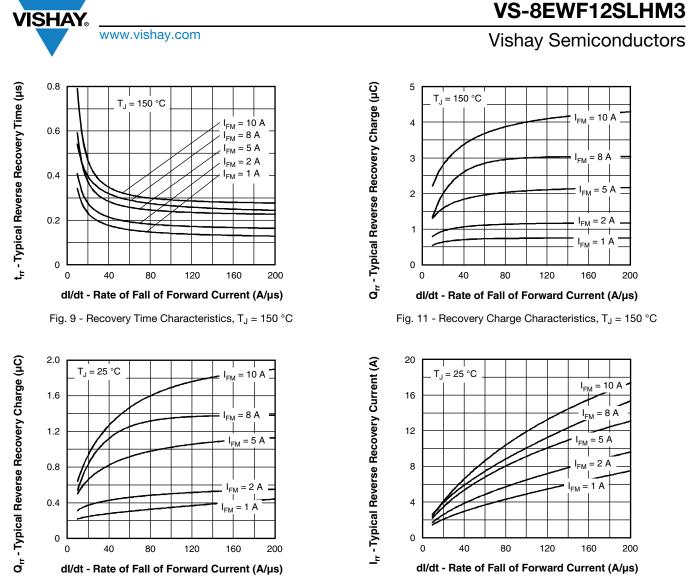


Fig. 10 - Recovery Charge Characteristics,  $T_J$  = 25  $^\circ\text{C}$ 

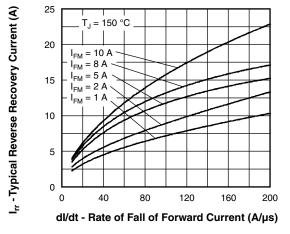


Fig. 12 - Recovery Current Characteristics,  $T_J = 25 \ ^{\circ}C$ 

Fig. 13 - Recovery Current Characteristics,  $T_J$  = 150  $^\circ\text{C}$ 

## VS-8EWF12SLHM3

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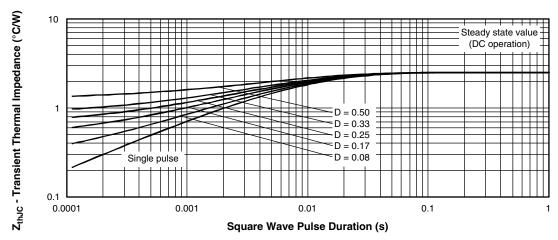


Fig. 14 - Thermal Impedance Z<sub>thJC</sub> Characteristics

#### ORDERING INFORMATION TABLE

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SHAY

Device code	VS-	8	Е	w	F	12	S	L	н	М3
	1	2	3	4	5	6	7	8	9	10
	1 .	- Visl	hay Sen	nicondu	ctors pro	oduct				
	2 ·	- Cur	rent rati	ng (8 =	8 A)					
	3 ·		cuit conf single	iguratio	า:					
	4	- Pac	kage:	(TO 25)						
	5	- Тур	e of sili		,	ior				
	6			t recove de x 100	-	_	12 = 12	00 V		
	7	- S = surface mountable								
	8	• L=	tape an	d reel (l	eft orien	ted), fo	r differe	nt orien	tation co	ontact fa
	9 -	• Н=	AEC-Q	101 qua	lified					
	10 -			ntal digit en-free,		complia	nt, and	termina	tions lea	ad (Pb)-1

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-8EWF12SLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?96495				
SPICE model	www.vishay.com/doc?97057				

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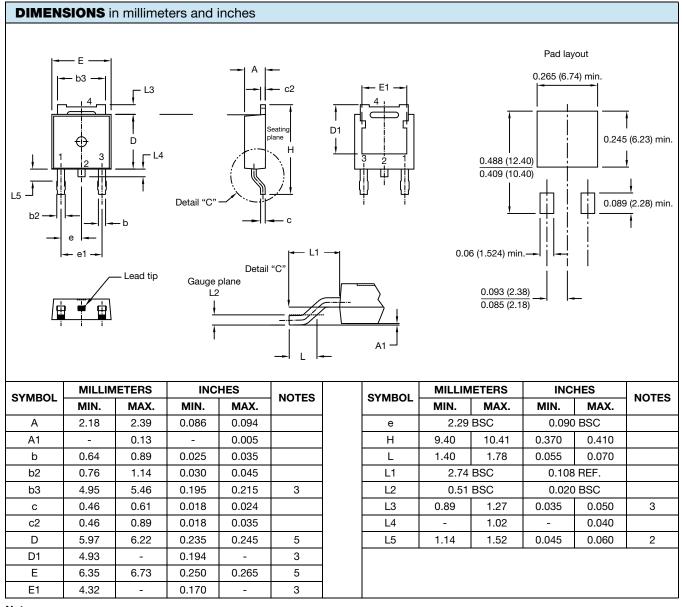
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### **Outline Dimensions**



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# DPAK (TO-252AA)



#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(5)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA, except for D1 dimension



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