

“Low Side Chopper” IGBT SOT-227, 650 V, 100 A



SOT-227

FEATURES

- Trench IGBT technology
- Higher switching frequency up to 150 kHz
- Square RBSOA
- Low $V_{CE(on)}$
- FRED Pt® Gen 4 clamping diode
- Fully isolated package
- Very low internal inductance (≤ 5 nH typical)
- Industry standard outline
- UL approved file E78996 
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

PRIMARY CHARACTERISTICS

V_{CES}	650 V
I_C DC	50 A at 117 °C
$V_{CE(on)}$ typical at 100 A, 25 °C	1.72 V
I_F DC	50 A at 90 °C
Package	SOT-227
Circuit configuration	Low side chopper

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages
- Lower conduction losses and switching losses
- Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V_{CES}		650	V
Continuous collector current	I_C	$T_C = 25$ °C	94	A
		$T_C = 80$ °C	70	
Pulsed collector current	I_{CM}	$V_{GE} = 15$ V	240	
Clamped inductive load current	I_{LM}		250	
Diode continuous forward current	I_F	$T_C = 25$ °C	74	
		$T_C = 80$ °C	54	V
Single pulse forward current	I_{FSM}	10 ms sine or 6 ms rectangular pulse, $T_J = 25$ °C	340	
Gate to emitter voltage	V_{GE}		± 20	
Power dissipation, IGBT	P_D	$T_C = 25$ °C	230	W
		$T_C = 80$ °C	146	
Power dissipation, diode	P_D	$T_C = 25$ °C	143	
		$T_C = 80$ °C	90	V
RMS isolation voltage	V_{ISOL}	Any terminal to case, $t = 1$ min	2500	

**ELECTRICAL SPECIFICATIONS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	$V_{BR(CE)}$	$V_{GE} = 0\text{ V}$, $I_C = 0.4\text{ mA}$	650	-	-	V
Collector to emitter voltage	$V_{CE(on)}$	$V_{GE} = 15\text{ V}$, $I_C = 100\text{ A}$	-	1.72	2.10	
		$V_{GE} = 15\text{ V}$, $I_C = 100\text{ A}$, $T_J = 125\text{ }^{\circ}\text{C}$	-	2.01	-	
Gate threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$, $I_C = 1.0\text{ mA}$	2.8	4.0	5.3	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_J$	$V_{CE} = V_{GE}$, $I_C = 1.0\text{ mA}$ ($25\text{ }^{\circ}\text{C}$ to $125\text{ }^{\circ}\text{C}$)	-	-9.73	-	mV/ $^{\circ}\text{C}$
Transfer characteristics	V_{GE}	$V_{CE} = 20\text{ V}$, $I_C = 100\text{ A}$	-	6.4	-	V
Collector to emitter leakage current	I_{CES}	$V_{GE} = 0\text{ V}$, $V_{CE} = 650\text{ V}$	-	0.3	80	μA
		$V_{GE} = 0\text{ V}$, $V_{CE} = 650\text{ V}$, $T_J = 125\text{ }^{\circ}\text{C}$	-	30	-	
Diode reverse breakdown voltage	V_{BR}	$I_R = 1.0\text{ mA}$	650	-	-	V
Diode forward voltage drop	V_{FM}	$I_F = 100\text{ A}$, $V_{GE} = 0\text{ V}$	-	2.39	2.71	V
		$I_F = 100\text{ A}$, $V_{GE} = 0\text{ V}$, $T_J = 125\text{ }^{\circ}\text{C}$	-	2.07	-	
Diode reverse leakage current	I_{RM}	$V_R = 650\text{ V}$	-	0.4	60	μA
		$V_R = 650\text{ V}$, $T_J = 125\text{ }^{\circ}\text{C}$	-	100	-	
Gate to emitter leakage current	I_{GES}	$V_{GE} = \pm 20\text{ V}$	-	-	± 300	nA

SWITCHING CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Q _g	I _C = 100 A, V _{CC} = 520 V, V _{GE} = 15 V		-	235	-	nC
Gate to emitter charge (turn-on)	Q _{ge}			-	37	-	
Gate to collector charge (turn-on)	Q _{gc}			-	69	-	
Turn-on switching loss	E _{on}	I _C = 100 A, V _{CC} = 325 V, V _{GE} = 15 V, R _g = 10 Ω, L = 500 μH, T _J = 25 °C	Energy losses include tail and diode recovery	-	2.7	-	mJ
Turn-off switching loss	E _{off}			-	0.59	-	
Total switching loss	E _{tot}			-	3.29	-	
Turn-on delay time	t _{d(on)}			-	140	-	
Rise time	t _r			-	52	-	
Turn-off delay time	t _{d(off)}			-	149	-	
Fall time	t _f			-	12	-	
Turn-on switching loss	E _{on}			-	3.16	-	
Turn-off switching loss	E _{off}			-	0.72	-	
Total switching loss	E _{tot}			I _C = 100 A, V _{CC} = 325 V, V _{GE} = 15 V, R _g = 10 Ω, L = 500 μH, T _J = 125 °C		-	
Turn-on delay time	t _{d(on)}	-	140			-	
Rise time	t _r	-	45			-	
Turn-off delay time	t _{d(off)}	-	159			-	
Fall time	t _f	-	16			-	
Reverse bias safe operating area	RBSOA	T _J = 175 °C, I _C = 250 A, R _g = 10 Ω, V _{GE} = 15 V to 0 V, V _{CC} = 400 V, V _P = 650 V				Fullsquare	
Diode reverse recovery time	t _{rr}	I _F = 50 A, dI _F /dt = 500 A/μs, V _R = 400 V		-	84	-	ns
Diode peak reverse current	I _{rr}			-	7.2	-	A
Diode recovery charge	Q _{rr}			-	300	-	nC
Diode reverse recovery time	t _{rr}	I _F = 50 A, dI _F /dt = 500 A/μs, V _R = 400 V, T _J = 125 °C		-	143	-	ns
Diode peak reverse current	I _{rr}			-	16	-	A
Diode recovery charge	Q _{rr}			-	1135	-	nC

**THERMAL AND MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T_J, T_{Stg}		-40	-	175	°C
Junction to case	R_{thJC}		-	-	0.65	°C/W
IGBT			-	-	1.05	
Diode			-	-	-	
Case to heatsink	R_{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style		SOT-227				

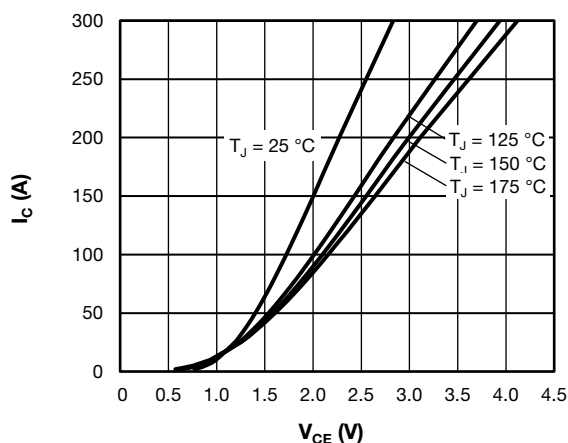
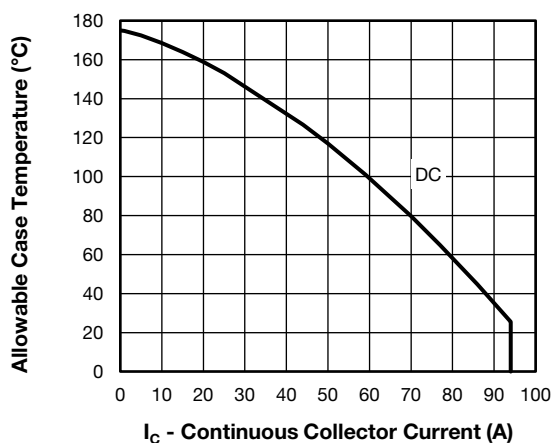
Fig. 1 - Typical Trench IGBT Output Characteristics, $V_{GE} = 15\text{ V}$ 

Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs. Case Temperature

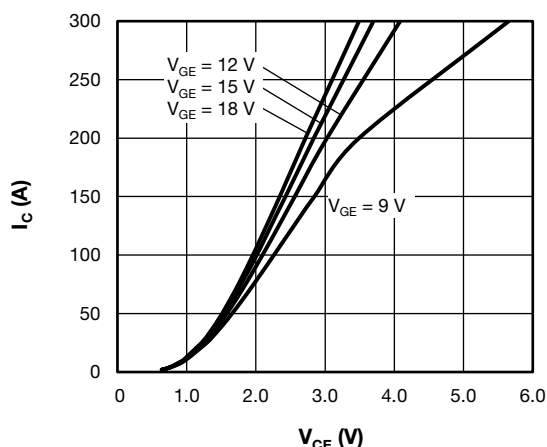
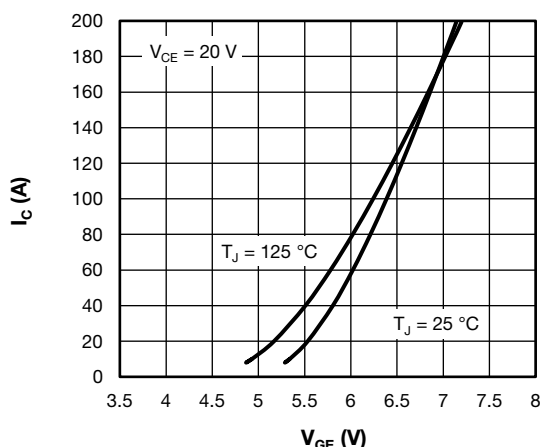
Fig. 2 - Typical Trench IGBT Output Characteristics, $T_J = 125\text{ °C}$ 

Fig. 4 - Typical Trench IGBT Transfer Characteristics

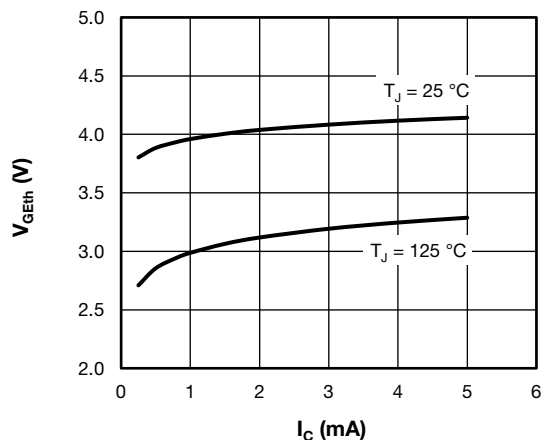
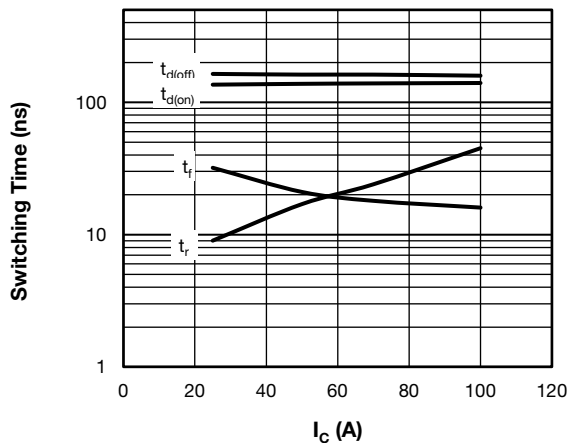
$T_J = 125\text{ }^{\circ}\text{C}$, $V_{CC} = 325\text{ V}$, $R_g = 10\text{ }\Omega$, $V_{GE} = +15\text{ V}/-15\text{ V}$, $L = 500\text{ }\mu\text{H}$


Fig. 5 - Typical Trench IGBT Gate Threshold Voltage


Fig. 8 - Typical Trench IGBT Switching Time vs. I_C
(with Antiparallel Diode)

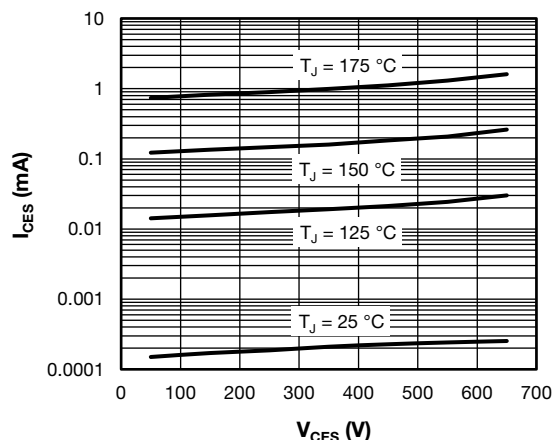
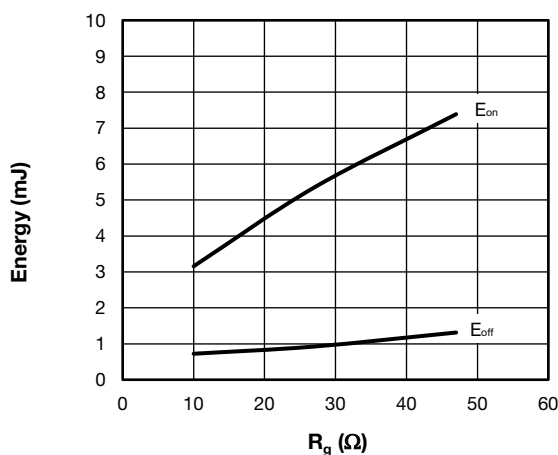
 $T_J = 125\text{ }^{\circ}\text{C}$, $V_{CC} = 325\text{ V}$, $R_g = 10\text{ }\Omega$, $V_{GE} = +15\text{ V}/-15\text{ V}$, $L = 500\text{ }\mu\text{H}$


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current


Fig. 9 - Typical Trench IGBT Energy Loss vs. R_g
(with Antiparallel Diode)

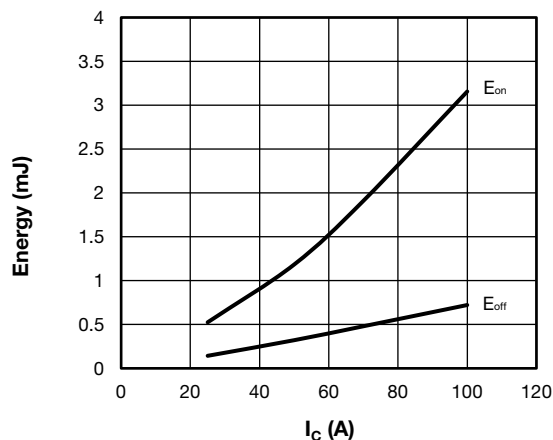
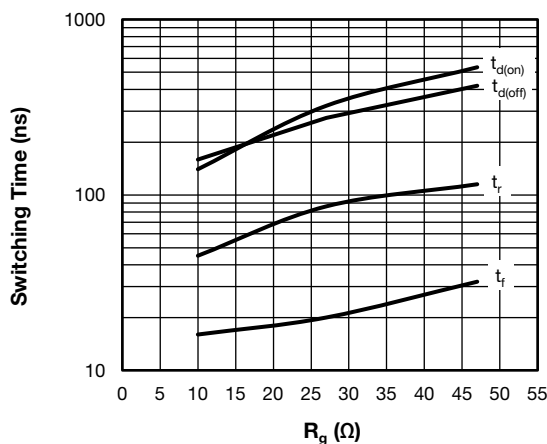
 $T_J = 125\text{ }^{\circ}\text{C}$, $V_{CC} = 325\text{ V}$, $I_C = 100\text{ A}$, $V_{GE} = +15\text{ V}/-15\text{ V}$, $L = 500\text{ }\mu\text{H}$

Fig. 7 - Typical Trench IGBT Energy Loss vs. I_C
(with Antiparallel Diode)


Fig. 10 - Typical Trench IGBT Switching Time vs. R_g
(with Antiparallel Diode)

$T_J = 125^\circ\text{C}$, $V_{CC} = 325\text{ V}$, $I_C = 100\text{ A}$, $V_{GE} = +15\text{ V}/-15\text{ V}$, $L = 500\text{ }\mu\text{H}$

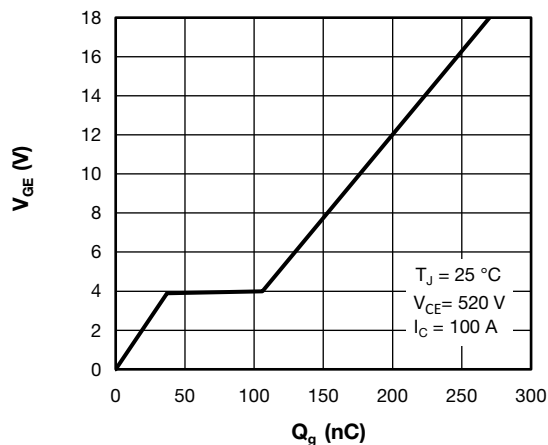


Fig. 11 - Typical Trench IGBT Gate Charge vs. Gate to Emitter Voltage

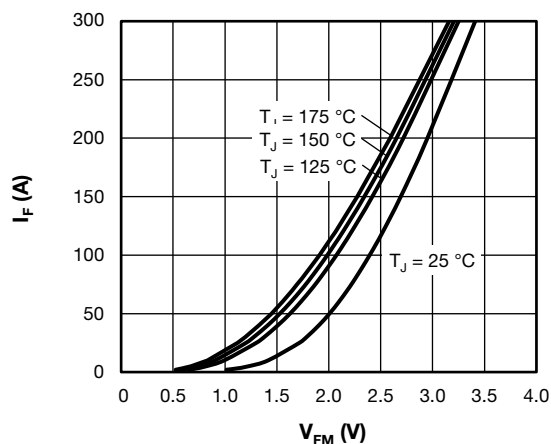


Fig. 12 - Typical Diode Forward Characteristics

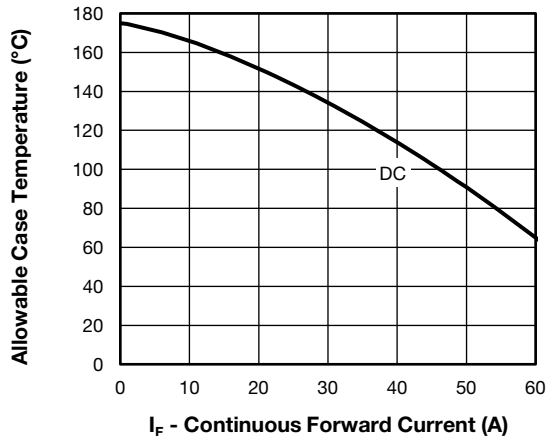


Fig. 13 - Maximum Diode Continuous Forward Current vs. Case Temperature

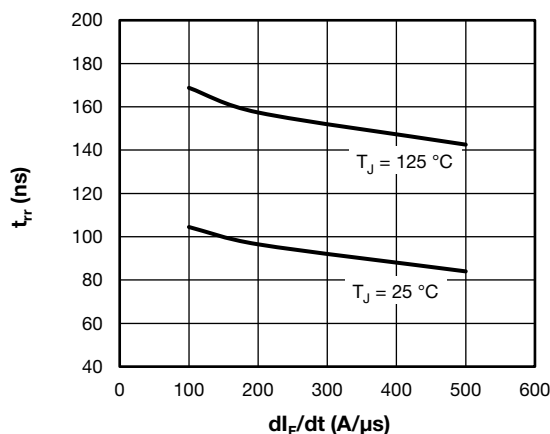


Fig. 14 - Typical Diode Reverse Recovery Time vs. di_F/dt
 $I_F = 50\text{ A}$, $V_{CC} = 400\text{ V}$

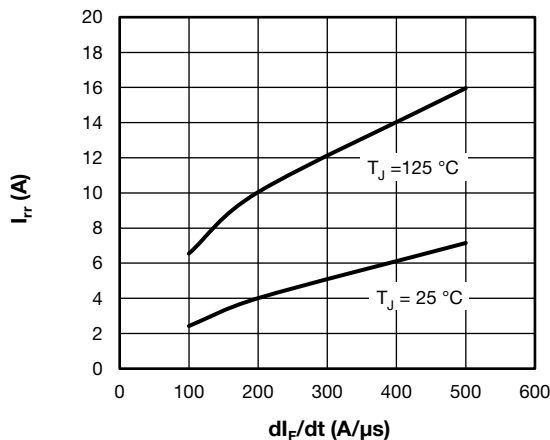


Fig. 15 - Typical Diode Reverse Recovery Current vs. di_F/dt
 $I_F = 50\text{ A}$, $V_{CC} = 400\text{ V}$

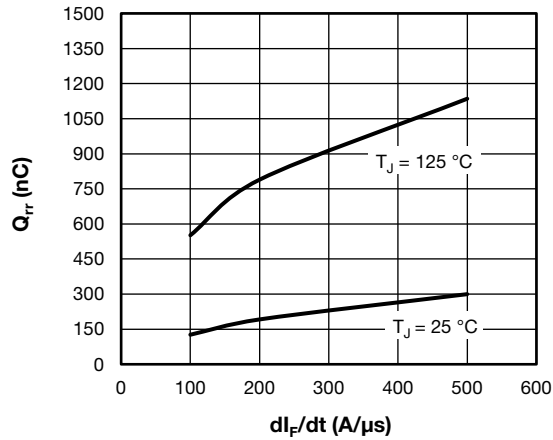


Fig. 16 - Typical Diode Reverse Recovery Charge vs. dI_F/dt
 $I_F = 50$ A, $V_{CC} = 400$ V

Fig. 17 - Trench IGBT Reverse BIAS SOA
 $T_J = 175$ °C, $I_C = 250$ A, $R_g = 10$ Ω, $V_{GE} = +15$ V/0 V, $V_{CC} = 400$ V,
 $V_p = 650$ V

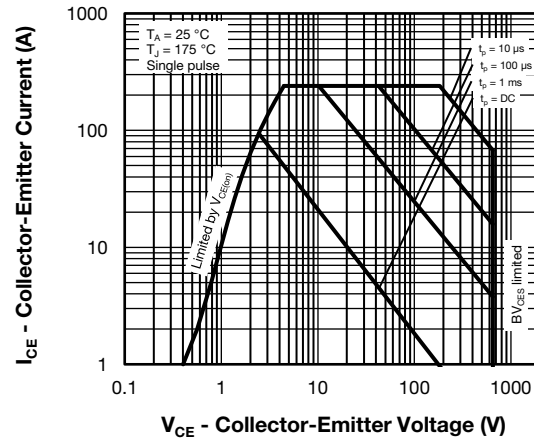


Fig. 18 - Trench IGBT Safe Operating Area

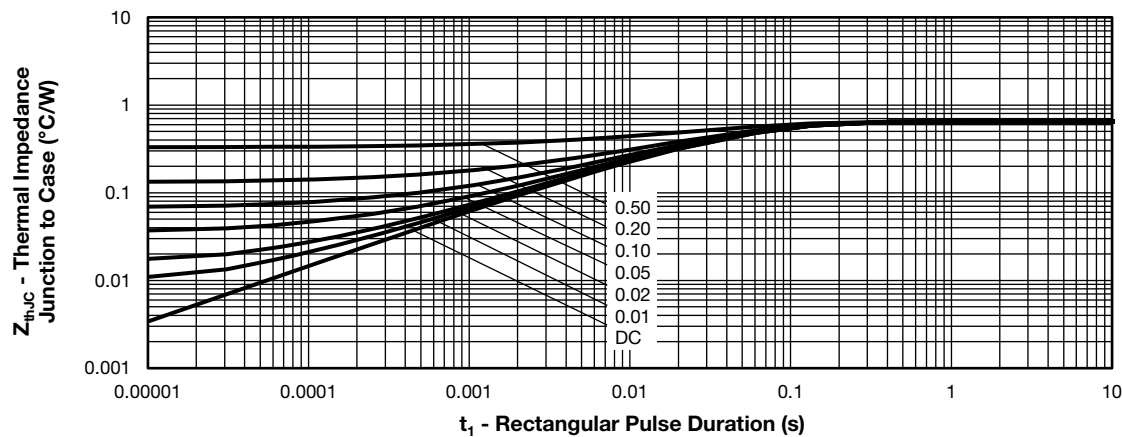
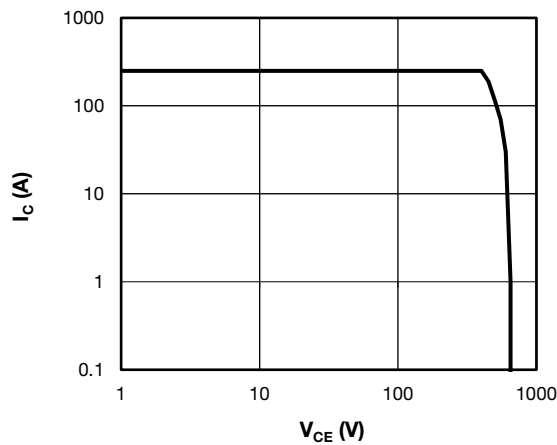


Fig. 19 - Maximum Trench IGBT Thermal Impedance Z_{thJC} Characteristics

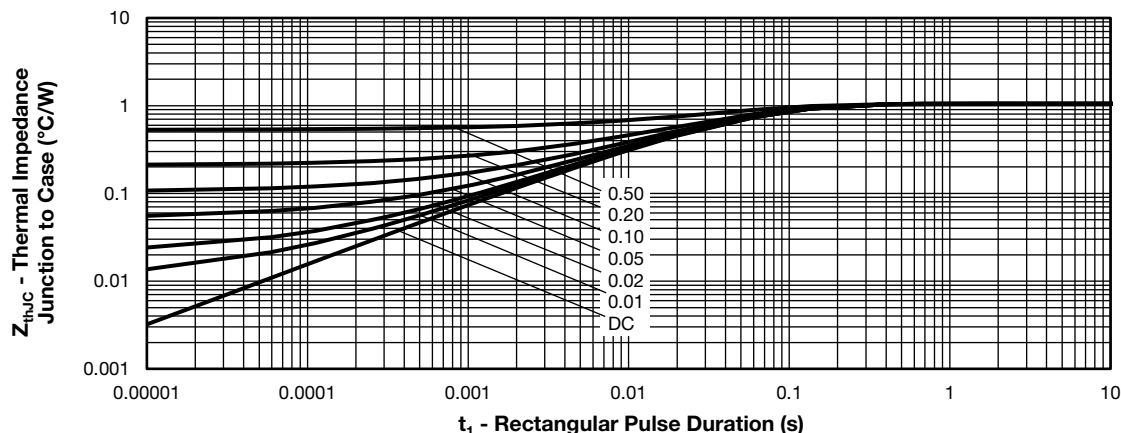


Fig. 20 - Maximum Diode Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

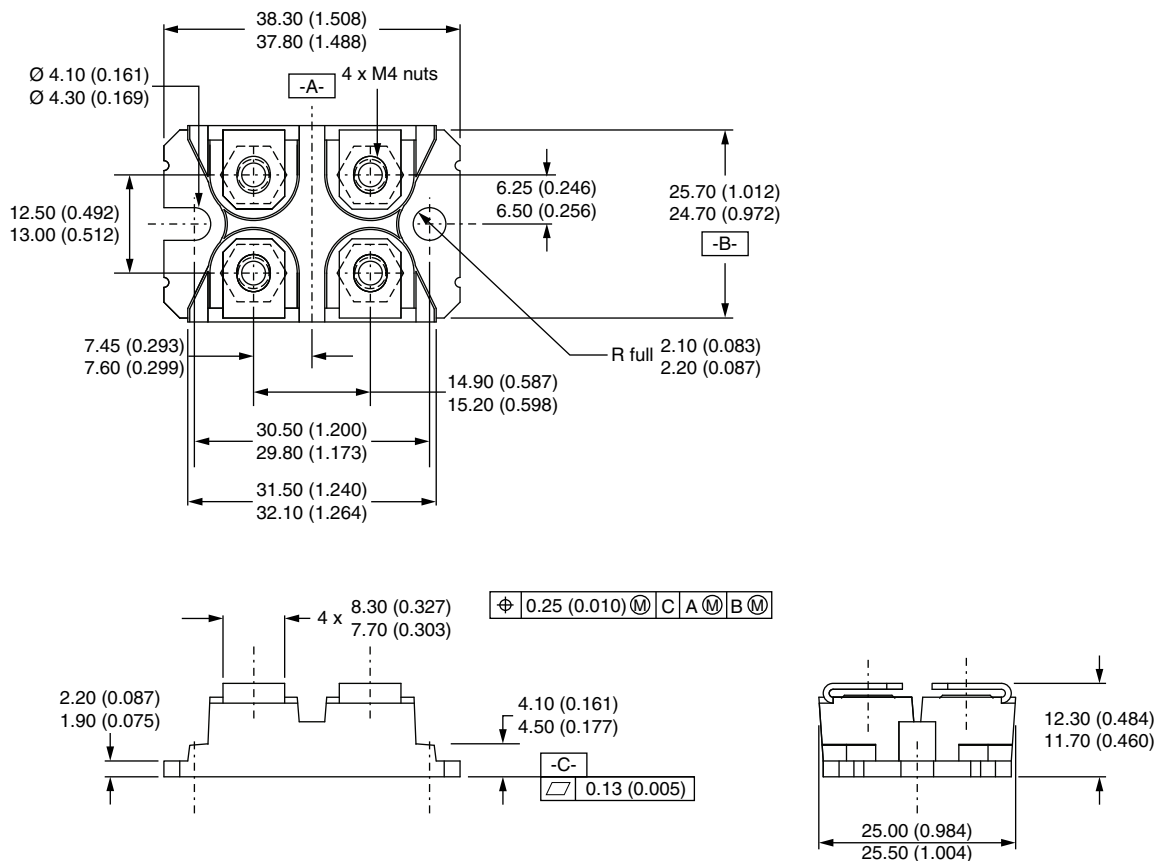
Device code	VS-	G	T	100	L	A	65	U	F
	①	②	③	④	⑤	⑥	⑦	⑧	⑨
①	Vishay Semiconductors product								
②	Insulated gate bipolar transistor (IGBT)								
③	T = Trench IGBT technology								
④	Current rating (100 = 100 A)								
⑤	Circuit configuration (L = low side chopper)								
⑥	Package indicator (A = SOT-227)								
⑦	Voltage rating (65 = 650 V)								
⑧	Speed/type (U = ultrafast IGBT)								
⑨	Diode (F = FRED Pt [®] diode)								

CIRCUIT CONFIGURATION			
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING	
Low side chopper	L		Lead Assignment



LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95423
Packaging information	www.vishay.com/doc?95425
Application note	www.vishay.com/doc?95527

DIMENSIONS in millimeters (inches)



Note

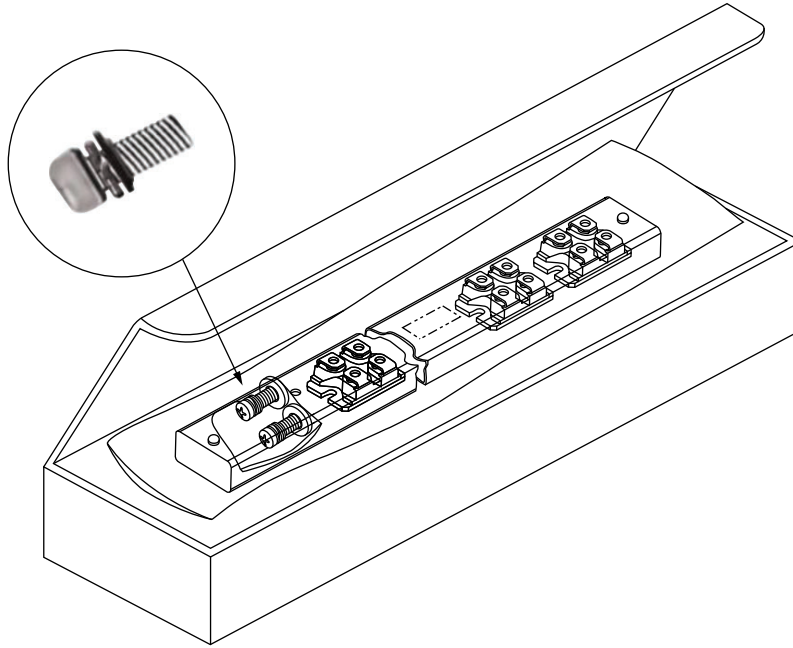
- Controlling

dimension:

millimeter

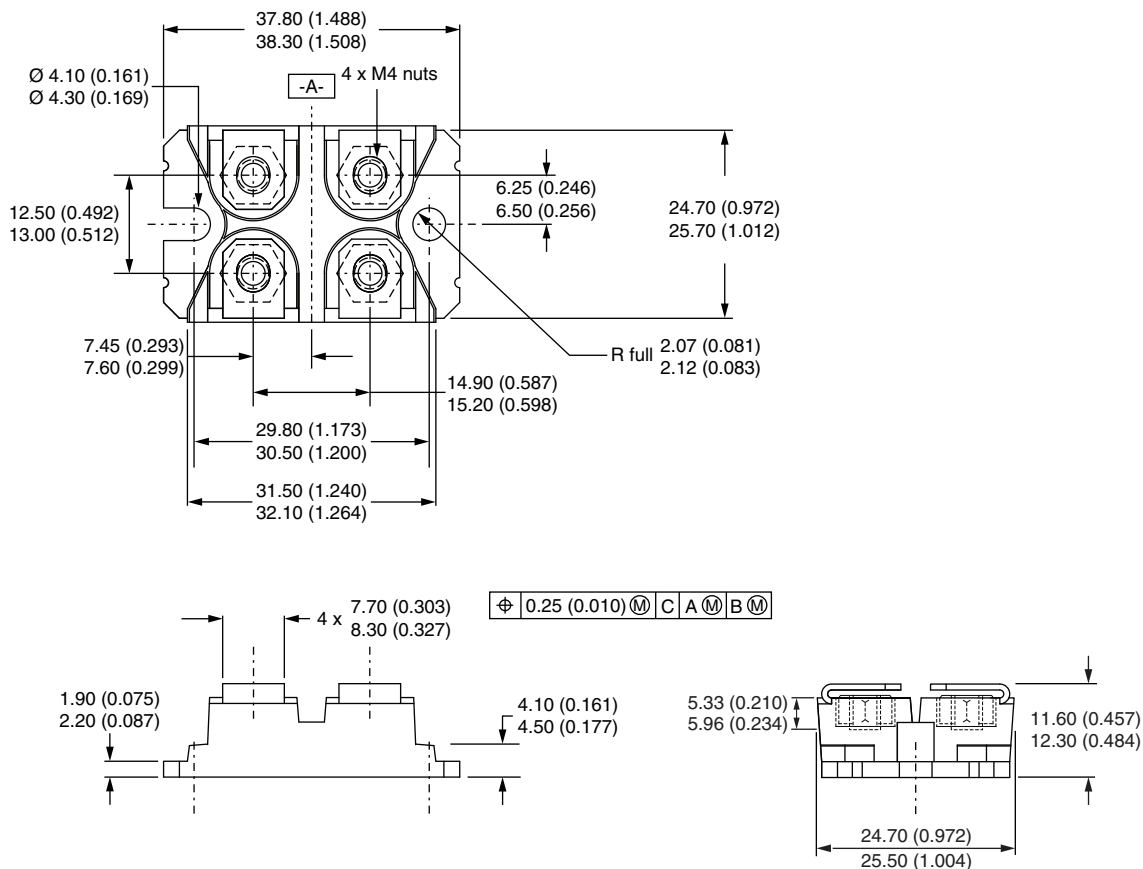


PACKAGING INFORMATION



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

- Controlling dimension: millimeter



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