SE8D20D, SE8D20G, SE8D20J

Vishay General Semiconductor

eSMP[®] Series Top View **Bottom View** SlimSMAW (DO-221AD)

LINKS TO ADDITIONAL RESOURCES

30 3D Models

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 A			
V _{RRM}	200 V to 600 V			
I _{FSM}	35 A			
V _F at I _F = 2 A (T _J = 125 °C)	0.86 V			
T _J max.	175 °C			
Package	SlimSMAW (DO-221AD)			
Circuit configuration	Single			

FEATURES

Surface-Mount Standard Rectifier

- Low-profile package
- Oxide planar chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 gualified available - Automotive ordering code: base P/NHM3
- · Compatible to SOD-128 package case outline
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

General purpose, power line polarity protection and rail-to-rail protection in consumer, industrial. and automotive applications.

MECHANICAL DATA

Case: SlimSMAW (DO-221AD) Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 halogen-free, **RoHS-compliant** Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	SE8D20D	SE8D20G	SE8D20J	UNIT	
Device marking code		SD2D	SD2G	SD2J		
Maximum repetitive peak reverse voltage	V _{RRM}	200	400	600	V	
Maximum average forward rectified current (fig.1)	I _{F(AV)} ⁽¹⁾	2			А	
	I _{F(AV)} ⁽²⁾	1.4				
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	35			А	
Operating junction temperature range	T _J ⁽³⁾	-55 to +175			°C	
Storage temperature range	T _{STG}	-55 to +175				

Notes

(1) Mounted on 30 mm x 30 mm pad areas aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

⁽³⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{eJA}$



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Cathode O Anode

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COMPLIANT HALOGEN FREE



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ELECTRICAL CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 1 A	— T _J = 25 °C	V _F ⁽¹⁾	0.91	-	V	
	I _F = 2 A			0.96	1.10		
	I _F = 1 A	– T _J = 125 °C		0.79	-		
	I _F = 2 A			0.86	0.98		
Reverse current	Rated V _B	T _J = 25 °C	I _R ⁽²⁾	-	5	μA	
	naleu v _R	T _J = 125 °C		8	100		
Typical reverse recovery time	I _F = 0.5 A, I _R = 01 A, I _{rr} = 0.25 A		t _{rr}	1200	-	ns	
Typical junction capacitance	4.0 V, 1 MHz		CJ	12	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Typical thermal resistance	R _{0JA} (1)(2)	120	150	°C/W	
	R _{θJM} ⁽³⁾	12	15	C/W	

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

(2) Thermal resistance junction-to-ambient to follow JEDEC[®] 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint

⁽³⁾ Thermal resistance junction-to-mount to follow JEDEC 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SE8D20J-M3/H	0.033	Н	3500	7" diameter plastic tape and reel		
SE8D20J-M3/I	0.033	I	14 000	13" diameter plastic tape and reel		
SE8D20JHM3/H (1)	0.033	Н	3500	7" diameter plastic tape and reel		
SE8D20JHM3/I ⁽¹⁾	0.033	l	14 000	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

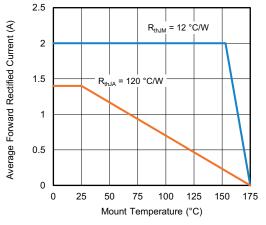


Fig. 1 - Maximum Forward Current Derating Curve

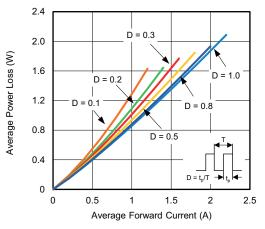


Fig. 2 - Forward Power Loss Characteristics

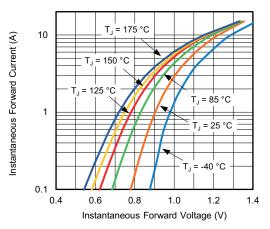


Fig. 3 - Typical Instantaneous Forward Characteristics

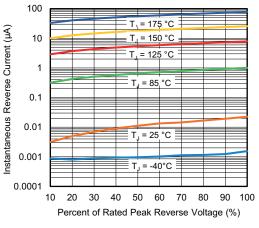


Fig. 4 - Typical Reverse Leakage Characteristics

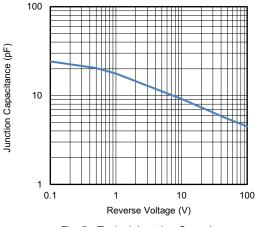


Fig. 5 - Typical Junction Capacitance

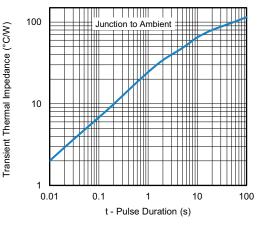


Fig. 6 - Typical Transient Thermal Impedance

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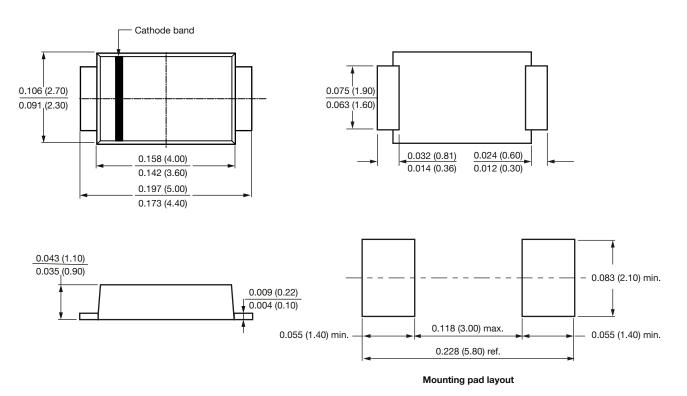
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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