Ceramic Singlelayer DC Disc Capacitors, 8 kV_{DC} General Purpose

QUICK REFERENCE DATA	
DESCRIPTION	**VALUE**
Ceramic Class	2
Ceramic Dielectric	Y5T
Voltage (V_{DC})	8000
Min. Capacitance (pF)	100
Max. Capacitance (pF)	2200
Mounting	Radial

OPERATING TEMPERATURE RANGE

-40 °C to +85 °C (1)

Note

(1) For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see www.vishay.com/doc?48299

TEMPERATURE CHARACTERISTICS

Y5T

SECTIONAL SPECIFICATIONS

Climatic category (according to EN 60068-1):
40 / 085 / 21

FEATURES

- High capacitance in small sizes
- Low losses
- Wide range of different lead styles
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb e3 RoHS COMPLIANT

APPLICATIONS

- Lighting ballasts
- Switching power supplies
- Bypassing, coupling and decoupling
- DC blocking

DESIGN

The capacitors consist of a ceramic disc which is silver plated on both sides. Connection leads are made of tinned copper having diameters of 0.6 mm or 0.8 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 12.5 mm.

Coating is made of blue colored flame retardant epoxy resin in accordance with UL 94 V-0.

CAPACITANCE RANGE

100 pF to 2.2 nF

RATED VOLTAGE

8 kV_{DC}

DIELECTRIC STRENGTH

12 000 V_{DC}, 2 s Component test

INSULATION RESISTANCE AT 500 V_{DC}

≥ 10 000 MΩ (60 s)

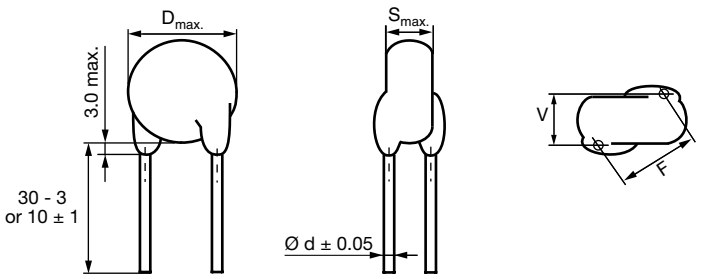
TOLERANCE ON CAPACITANCE

± 20 % (± 10 % available on request)

DISSIPATION FACTOR

Max. 2.0 % (1 kHz)

DIMENSIONS in millimeters



ORDERING INFORMATION

CAPACITANCE (pF)	TOLERANCE (%)	BODY DIAMETER $D_{max.}$ (mm)	BODY THICKNESS $S_{max.}$ (mm)	LEAD SPACING (1) F (mm) ± 1 mm	LEAD DIAMETER (1) d (mm) ± 0.05 mm	WIDTH (1) V (mm) ± 0.5 mm	ORDERING CODE MISSING DIGITS SEE ORDERING CODE BELOW
Y5T							
100	± 20 (2)	9.0	8.3	12.5	0.8	4.0	HGZ101#BP###KR
120							HGZ121#BP###KR
150							HGZ151#BP###KR
180		11.0					HGZ181#BP###KR
220							HGZ221#BP###KR
270							HGZ271#BP###KR
330		13.0					HGZ331#BP###KR
390							HGZ391#BP###KR
470		14.0					HGZ471#BP###KR
560		16.0					HGZ561#BP###KR
680							HGZ681#BP###KR
820		18.0					HGZ821#BP###KR
1000							HGZ102#BP###KR
1200		21.0					HGZ122#BP###KR
1500							HGZ152#BP###KR
1800		24.0					HGZ182#BP###KR
2200							HGZ222#BP###KR

Notes

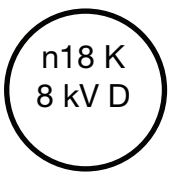
(1) Standard lead configuration, other lead spacing and diameter available on request

(2) ± 10 % available on request

ORDERING CODE

#	7th digit	Capacitance tolerance	± 10 % = K, ± 20 % = M				
###	10th to 12th digit	Lead configuration	See "General Information" www.vishay.com/doc?22001				
Example	**HGZ**	**821**	**M**	**BP**	**ERY**	**K**	**R**
	Series	Capacitance value	Tolerance code	Voltage code	Lead configuration	Internal code	RoHS compliant

MARKING



HGZ 100 pF to 270 pF

HGZ 330 pF to 2.2 nF

STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +35 °C, relative humidity up to 60 %). Class 2 ceramic dielectric capacitors are also subject to aging, see www.vishay.com/doc?22001.

SOLDERING

SOLDERING SPECIFICATIONS		
Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)		
	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT
Soldering temperature	235 °C ± 5 °C	260 °C ± 5 °C
Soldering duration	2 s ± 0.5 s	10 s ± 1 s
Distance from component body	≥ 2 mm	≥ 5 mm

SOLDERING RECOMMENDATIONS

Soldering of the component should be achieved using a Sn60/40 type or a silver-bearing Sn62/36/2Ag type solder. Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see Soldering Specifications table) should not be exceeded. Subjecting the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method: IEC 60068-2-45 (method XA).

MOUNTING

If a defined product stop is required for mounting on a PCB, a mechanically formed product stop (kinked or inline wire) or a mounting tool should be used.

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating.

OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	www.vishay.com/doc?22001

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