

Increasing Accuracy in Feedback Circuits and Voltage Dividers with Thin Film Chip Resistor Arrays

1. INTRODUCTION

When we think about electrical resistors and their properties, the primary characteristics that come to mind are case size and resistance value as well as temperature coefficient (TCR), tolerance, and rated power. For many applications, these characteristics are sufficient to define the resistor that is needed to perform the required tasks. When it comes to analog precision circuits, however, we need to look deeper, and in particular at the behavior of the resistors throughout their intended lifetime. For feedback circuits and voltage dividers, the parameters mentioned above as well as a stable divider ratio have a decisive impact on the accuracy of the circuit. The stability of a voltage divider or a feedback circuit can only be achieved and maintained if the resistance values remain unchanged relative to one another over the lifetime of the circuit.

This application note is intended to show designers how to optimize the behavior and stability of electronic circuits by using thin film chip resistor arrays. The relative properties of the particular resistor elements of a thin film array - tolerance matching ⁽¹⁾, TCR tracking ⁽²⁾, and relative resistance drift - are the key parameters for precision and long-term stability, and are explained in greater detail below.

2. INFLUENCE OF THE RESISTANCE PROPERTIES

Before we take a closer look at the electrical properties of thin film chip resistor arrays, it is important to understand the impact of resistance properties on a comparably simple circuit. For this purpose, let's take the example of the feedback loop of a non-inverting amplifier. We'll compare one configuration that uses two discrete resistors in the feedback loop (section 2.1) and another configuration that uses a resistor array (section 2.2). A worst-case consideration of both scenarios demonstrates that the absolute gain error is drastically reduced by using an array. The application's intended lifetime in this example is assumed to be about 10 years and the resistance layer temperature ⁽³⁾ to be 70 °C. These conditions are typical for today's industrial applications.

Notes

- ⁽¹⁾ Relative tolerances or ratio tolerances.
- ⁽²⁾ Synchronization of the temperature coefficient.
- ⁽³⁾ The resistance layer temperature is the sum of the environmental temperature of the components and the electrical energy converted to heat in the resistance layer.

2.1 Feedback loop consisting of two discrete precision resistors

Technical specification of the resistors:

Temperature coefficient (TCR): ± 25 ppm/K

Tolerance: ± 0.1 %

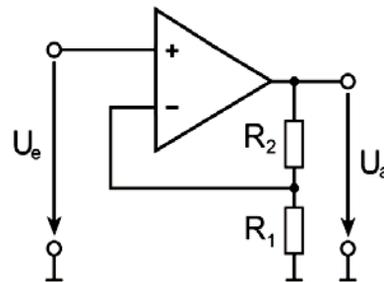


Fig. 1 - Feedback with discrete resistors

The gain factor is calculated as $V = 1 + (R_2/R_1)$

In this example $R_1 = 100 \Omega$ and $R_2 = 900 \Omega$

Consequential: $V = 10$

The overall gain error is the sum of all gain errors, caused by nominal tolerance, temperature coefficient and drift of the resistors ⁽⁴⁾.

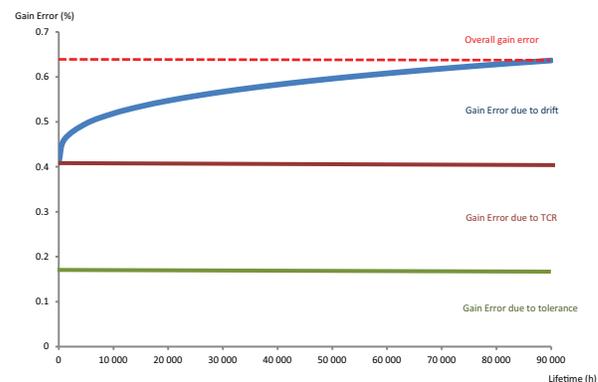


Fig. 2 - Gain error with discrete resistors

Note

- ⁽⁴⁾ Dependent on the application type and operating conditions, further factors must be taken into account in addition to tolerance, temperature coefficient and resistance drift.

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2.2 Feedback loop consisting of a resistor array resistors

Technical specification of the resistor array:

Absolute temperature coefficient: ± 25 ppm/K

Absolute tolerance: ± 0.25 %

Relative TCR (TCR tracking): 10 ppm/K

Relative tolerance (tolerance matching): 0.1 %

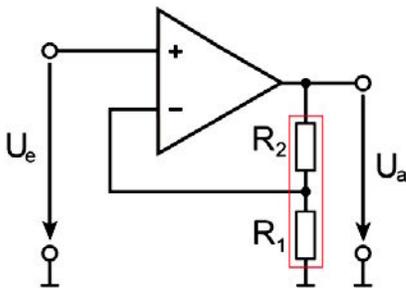


Fig. 3 - Feedback with a resistor array

The gain factor is calculated as $V = 1 + (R_2/R_1)$

In this example $R_1 = 100 \Omega$ and $R_2 = 900 \Omega$

Consequential: $V = 10$

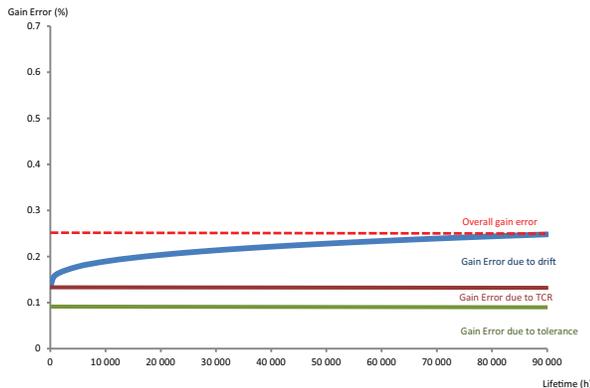


Fig. 4 - Gain error with a resistor array

As we see, simply by using a resistor array with specified relative properties such as tolerance matching and TCR tracking (and relative drift), the possible absolute gain error can be reduced significantly.

3. ADVANTAGES OF RESISTOR ARRAYS

The behavior of thin film chip resistor arrays is determined by the manufacturing process as well as the material properties of the resistive layer. The thermal coupling of the individual resistors and the homogeneous heat distribution arising from this also have a significant impact on the relative resistance drift.

Relative tolerance (tolerance matching)

The nominal value of a chip resistor is achieved by using laser trimming (Fig. 5). During the trimming process, the resistance value is continuously monitored, in order to ensure that the resistance value lies within the specified tolerance.

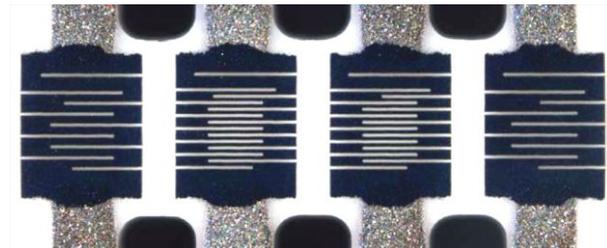


Fig. 5 - Aligned array with two different resistance values (Vishay's ACAS AT series)

Precision laser systems allow different resistance values to be created on the same array. Low energy input during laser processing allows a high degree of structuring and therefore high resistance ratios without influencing other resistor parameters. Sophisticated technical measurement allows precise trimming of the resistors and almost perfect ratio with very tight tolerances.

The variance with respect to the nominal resistance value is defined as absolute tolerance. The difference between the individual resistance values is described as tolerance matching, sometimes also called "relative tolerance" or "ratio tolerance."

Fig. 6 shows an example of a resistor array's tolerances with four integrated resistors. The relative limits for voltage dividers are the decisive parameters, here specified with ± 0.05 %.

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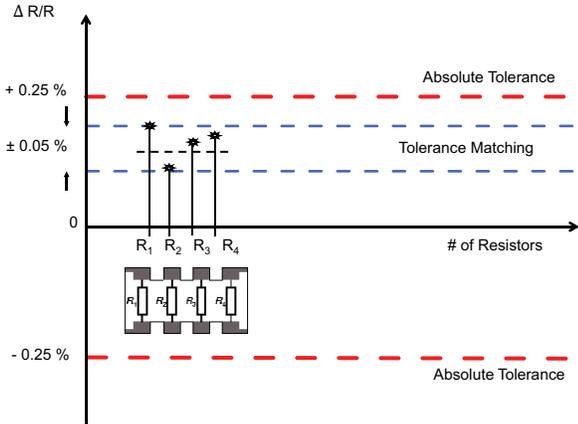


Fig. 6 - Tight relative tolerance: "Tolerance matching"

Relative temperature coefficient (TCR tracking)

Ideally electrical circuits should operate independent from temperature changes. One approach in reducing the temperature dependence is to apply resistors with a low temperature coefficient. The temperature coefficient of thin film resistors (TCR) is influenced by various parameters such as alloy composition of the applied resistive material, sputtering process conditions, and temperature treatments during the manufacturing process. Each of these parameters needs to be controlled very accurately to enable small temperature coefficients.

For voltage divider and feedback circuits, the relative TCR of the individual resistors is even more important than their absolute values. For resistor arrays, this feature is specified by the TCR tracking. The specification of a TCR tracking is possible for resistor arrays, because the individual resistor elements are all influenced by temperature to the same extent. In other words, the temperature coefficient curves of all the resistors in the array follow one another, caused by virtually identical processing conditions during manufacturing and homogeneous heat distribution within the array during operation. The local proximity of the individual resistors on an array ensures a homogeneous layer thickness during the sputtering process. During the subsequent heat treatments, the individual resistors are constantly subjected to identical temperatures. Thus, to the benefit of any electronic design where relative stability of resistors is needed, thin film chip resistor arrays make the circuit almost independent from temperature changes.

Fig. 7 shows schematically the example of an array with a TCR specification of ± 25 ppm/K (red curves) and a TCR tracking specification of ± 5 ppm/K (blue curves).

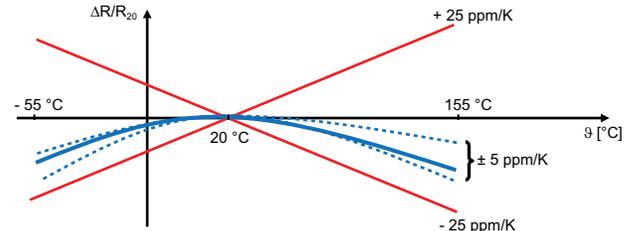


Fig. 7 - Absolute TCR limits (red) and TCR curves with TCR tracking (blue)

Relative resistance drift

All resistors have something in common: their resistance values change under the influence of temperature over time. The extent of the change is highly dependent on the underlying resistor technology, e.g. resistive material composition, process conditions, etc.

The film temperature of the resistor is determined by the applied power, its thermal resistance, and the ambient temperature. The substrate material (AL_2O_3) has a very good heat conductivity, thereby resulting in a good thermal coupling of the individual elements of a resistor array.

This thermal coupling ensures homogeneous heat distribution across the entire array, almost regardless of whether power is applied to one or several resistors and irrespective of where the array is located on the printed circuit board.

This homogeneous heat distribution in turn ensures that all the resistors within the array will age at virtually the same rate. The relative resistance change over time is therefore negligible and the long-term stability of the resistance ratio consequently very high.

Fig. 8 shows the heat distribution of an array with applied power.

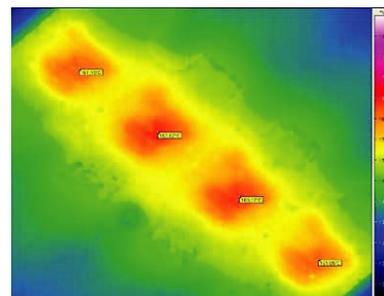


Fig. 8 - Heat distribution of a resistor array



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4. SUMMARY

Thin film chip resistor arrays consist of several resistors of equal or different values combined in one package. During the manufacturing processes and the device's lifecycle, all the particular resistors virtually experience identical conditions, which allow the specification of their relative tolerances, relative temperature coefficients, and even a relative resistance drift. These relative parameters provide precise and stable resistance ratios and far better long-term stability of feedback circuits and voltage dividers compared to discrete resistors.

Vishay's thin film resistor array series, the ACAS AT, has proven its stability and robustness in the field for many years. In addition to technical support, samples are also available for testing and development purposes.

For more technical information please refer to the product data sheet: www.vishay.com/doc?28770.

Your contact for technical support and samples: thinfilmmarray@vishay.com.

REFERENCE

Vishay, Technical Note:
"Drift Calculation for Thin Film Resistors"
www.vishay.com/doc?28809