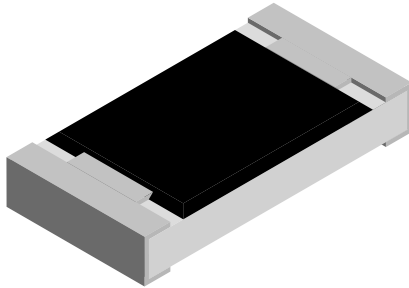


# Thick Film Resistor/Capacitor Chip, Surface Mount


**FEATURES**

- Single component reduces board space and component counts
- Choice of dielectric characteristics X7R or Y5U
- Wrap around termination
- Thick film resistor/capacitor element
- Inner electrode protection
- Flow and reflow solderable
- Automatic placement capability, standard size



STANDARD ELECTRICAL SPECIFICATIONS											
GLOBAL MODEL	SIZE		RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS				
	INCH	METRIC	POWER RATING $P_{70^{\circ}\text{C}}$ W	TEMPERATURE COEFFICIENT $\pm$ ppm/ $^{\circ}\text{C}$	RES. TOL. $\pm$ %	RES. RANGE $\Omega$	DIELECTRIC	TEMPERATURE COEFFICIENT %	CAP. TOL. $\pm$ %	CAP. VOLTAGE $V_{\text{DC}}$	CAP. RANGE pF
CRCC1206	1206	3216	0.125	200	5	10 to 1M	X7R	$\pm 15$	20	50	10 to 270
CRCC1206	1206	3216	0.125	200	5	10 to 1M	Y5U	+ 22, - 56	20	50	270 to 1800
<b>RESISTOR</b>						<b>CAPACITOR</b>					
<ul style="list-style-type: none"> <li>• Operating temperature range: - 55 <math>^{\circ}\text{C}</math> to + 125 <math>^{\circ}\text{C}</math></li> <li>• Technology: Thick film</li> </ul>						<ul style="list-style-type: none"> <li>• Operating temperature range: X7R - 55 <math>^{\circ}\text{C}</math> to + 125 <math>^{\circ}\text{C}</math> Y5U - 30 <math>^{\circ}\text{C}</math> to + 85 <math>^{\circ}\text{C}</math></li> <li>• Maximum dissipation factor: 2.5 %</li> </ul>					

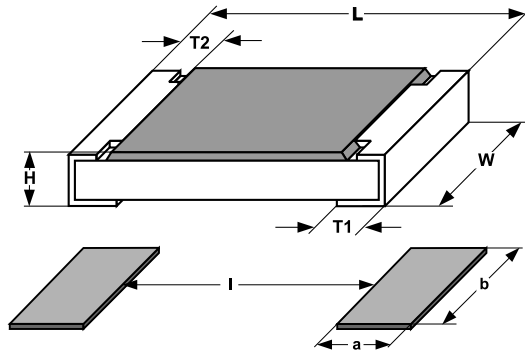
**Notes**

- Packaging: See appropriate catalog or web page.
- Power rating depends on the maximum temperature at the solder point, the component placement density and the substrate material.

TECHNICAL SPECIFICATIONS				
PARAMETER	UNIT	RESISTOR	X7R CAPACITOR	Y5U CAPACITOR
Rated dissipation at 70 $^{\circ}\text{C}$	W	0.125	-	-
Capacitor voltage rating	V	-	50	50
Dielectric withstanding voltage (5 s, 50 mA Charge)	$V_{\text{DC}}$	-	125	125
Category temperature range	$^{\circ}\text{C}$	- 55/+ 125	- 55/+ 125	- 30/+ 85
Insulation resistance	$\Omega$	$> 10^{10}$	$> 10^{10}$	$> 10^{10}$
Weight/1000 pieces	g	0.65	2	5.5

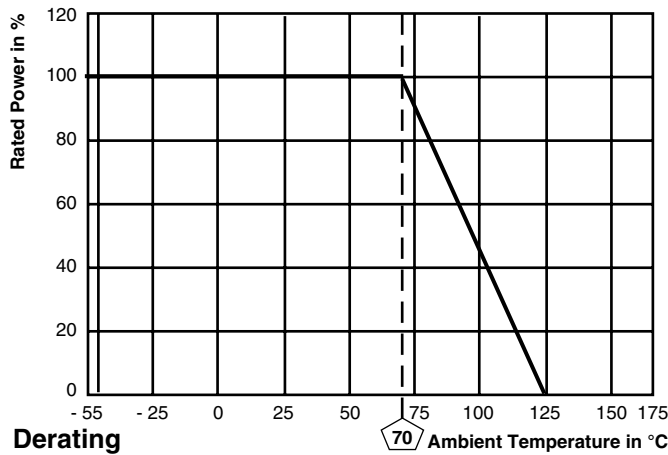
GLOBAL PART NUMBER INFORMATION																	
New Global Part Numbering: CRCC1206472J220MTF (preferred part numbering format)																	
C	R	C	C	1	2	0	6	4	7	2	J	2	2	0	M	T	F
GLOBAL MODEL <b>CRCC1206</b>	RESISTANCE VALUE 2 digit significant figure, followed by a multiplier <b>100</b> = 10 $\Omega$ <b>683</b> = 68 k $\Omega$ <b>105</b> = 1.0 M $\Omega$		RES. TOLERANCE <b>F</b> = $\pm 1$ % <b>G</b> = $\pm 2$ % <b>J</b> = $\pm 5$ %	CAPACITANCE VALUE (pF) 2 digit significant figure, followed by a multiplier <b>100</b> = 10 pF <b>271</b> = 270 pF <b>182</b> = 1800 pF		CAP. TOLERANCE <b>K</b> = $\pm 10$ % <b>M</b> = $\pm 20$ %	PACKAGING <b>EA</b> = Lead (Pb)-free, T/R (4000 pieces) <b>TF</b> = Tin/Lead, T/R (4000 pieces)										
Historical Part Number Example: CRCC1206472J220MR02 (will continue to be accepted)																	
<b>CRCC1206</b>	<b>472</b>	<b>J</b>	<b>220</b>	<b>M</b>	<b>R02</b>												
MODEL	RESISTANCE VALUE	RES. TOLERANCE	CAPACITANCE VALUE	CAP. TOLERANCE	PACKAGING												

**DIMENSIONS**

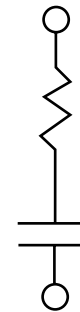


SIZE		DIMENSIONS in millimeters				
INCH	METRIC	L	W	H	T1	T2
1206	3216	3.2 ± 0.15	1.6 ± 0.15	0.55 ± 0.15	0.5 ± 0.25	0.5 ± 0.25

SIZE		SOLDER PAD DIMENSIONS in millimeters					
		REFLOW SOLDERING			WAVE SOLDERING		
INCH	METRIC	a	b	l	a	b	l
1206	3216	0.9	1.7	2.0	1.1	1.7	2.2



**SCHEMATIC**



<b>PERFORMANCE</b>			
TEST	CONDITIONS OF TEST	TEST RESULTS (TYPICAL TEST LOTS)	
		R	C
Endurance test at 70 °C MIL-Std-202 method 108	1000 h at 70 °C, 1.5 h "ON", 0.5 h "OFF"	± (5 % + 2 Ω)	± 20 %
Dielectric withstanding voltage MIL-Std-202 method 301	125 V <sub>DC</sub> , 5 s, 50 mA charge	No physical damage	
Thermal shock MIL-Std-202 method 107	100 cycles, - 55 °C to + 125 °C	± (5 % + 2 Ω)	± 20 %
Moisture MIL-Std-202 method 106	Omit steps 7A and B	± (5 % + 2 Ω)	± 20 %
Resistance to soldering heat EIA 575	10 s at 260 °C solder bath temperature	± (5 % + 2 Ω)	± 20 %
High temperature exposure EIA 575	125 °C for 100 h	± (5 % + 2 Ω)	± 20 %
Low temperature operation EIA 575	1 h at - 55 °C then 45 min at 50 V	± (5 % + 2 Ω)	± 20 %
Solderability and leaching EIA 575 3.12	Condition C	95 % coverage	

<b>APPLICABLE SPECIFICATIONS</b>
<ul style="list-style-type: none"> <li>• IPC standards</li> <li>• EIA 575</li> </ul>



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