

IHLP® Tin / Lead Inductors, High Saturation Series



LINKS TO ADDITIONAL RESOURCES



STANDARD ELECTRICAL SPECIFICATIONS				
L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μH)	DCR NOM. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾
0.56	3.4	3.6	20	12
0.68	4.2	4.5	18	11.5
0.82	4.6	4.9	16.5	13
1.0	5.6	6.5	13	15
1.5	8.6	9.0	12	12
2.2	13.0	13.6	10	10
3.3	19.9	20.9	8	8
4.7	28.9	30.3	6.5	7
5.6	32.7	34.4	6	7
6.8	42.5	44.6	5.5	5.5
8.2	43.5	45.6	5.5	5.5
10.0	67.9	71.3	4.5	4.5

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 75 V
- ⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- ⁽²⁾ DC current (A) that will cause L_0 to drop approximately 20 %

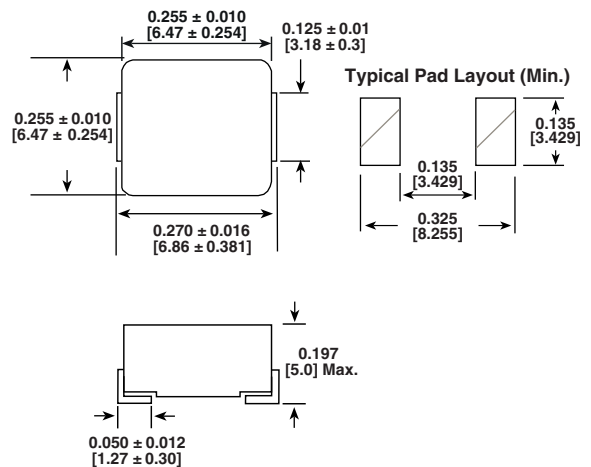
FEATURES

- Shielded construction
- Frequency range below 1.0 MHz
- Lowest DCR/ μH , in this package size
- Powdered iron composition provides soft saturation
- Handles high transient current spikes without saturation
- Saturation and inductance extremely stable over temperature
- Ultra low buzz noise, due to composite construction
- Tin / lead Sn / Pb plated (not dipped) terminals
- IHLP design; PATENT(S): www.vishay.com/patents

APPLICATIONS

- Notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

DIMENSIONS in inches [millimeters]



DESCRIPTION

IHLP-2525EZ-L1	1.0 μH	$\pm 20\%$	RZ
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE

GLOBAL PART NUMBER

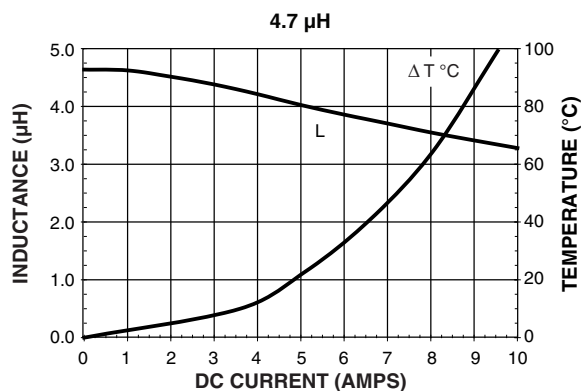
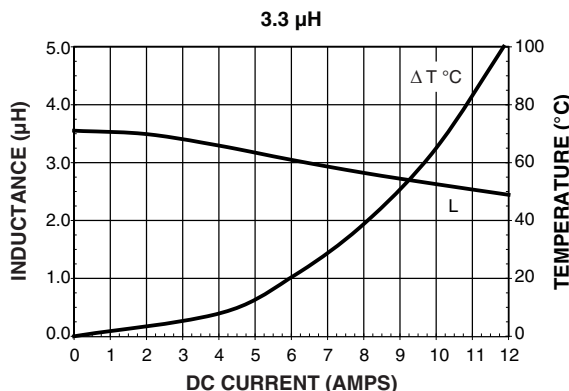
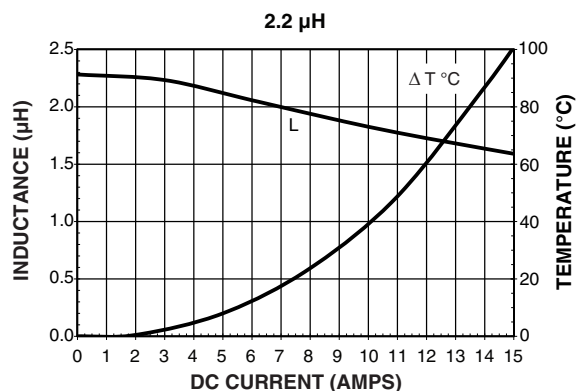
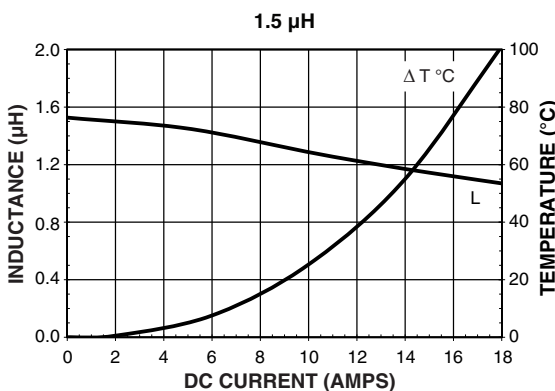
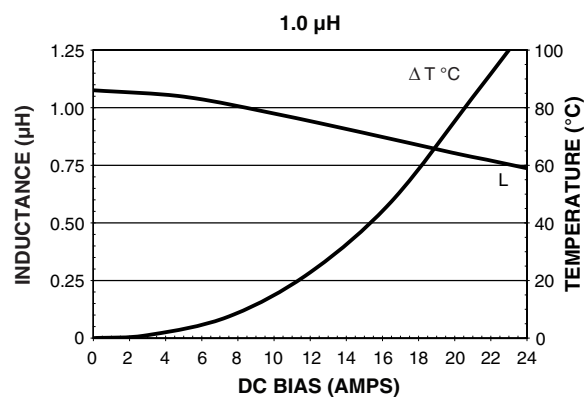
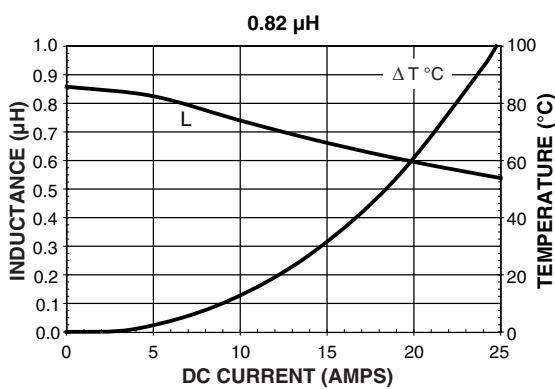
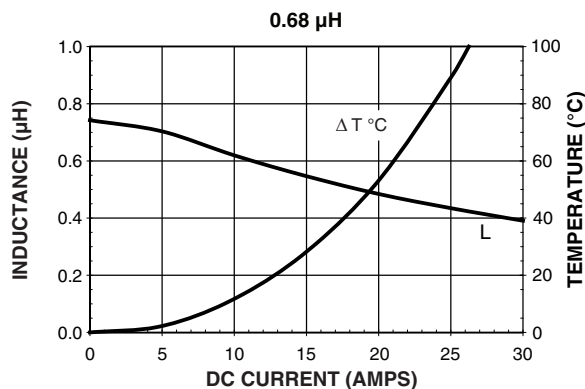
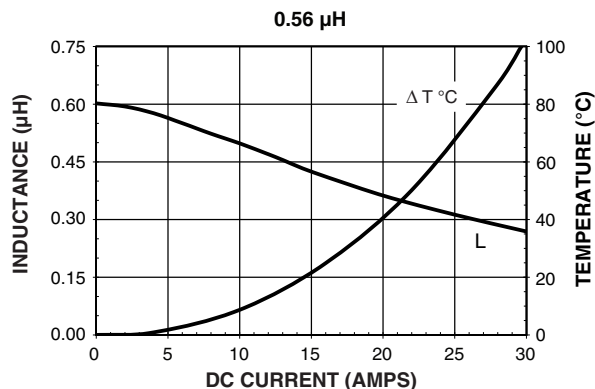
I	H	L	P	2	5	2	5	E	Z	R	Z	1	R	0	M	L	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		TOL	SERIES				

PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.

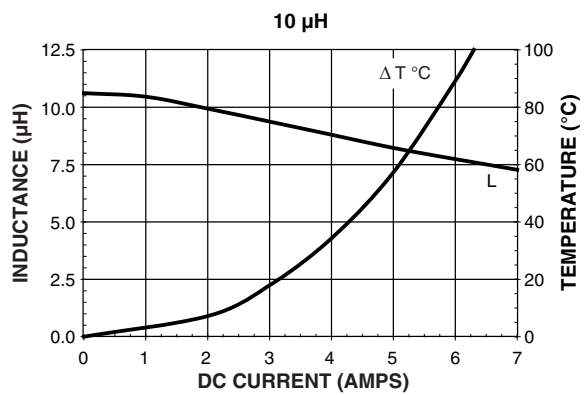
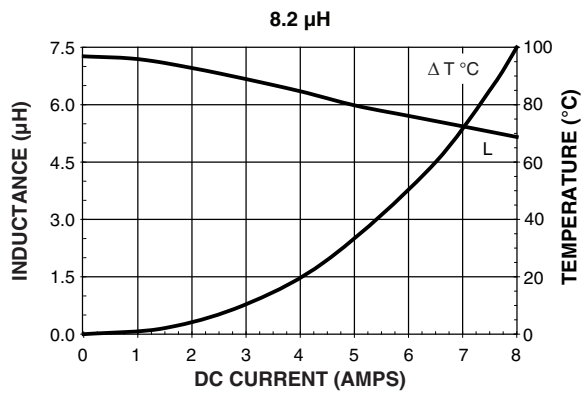
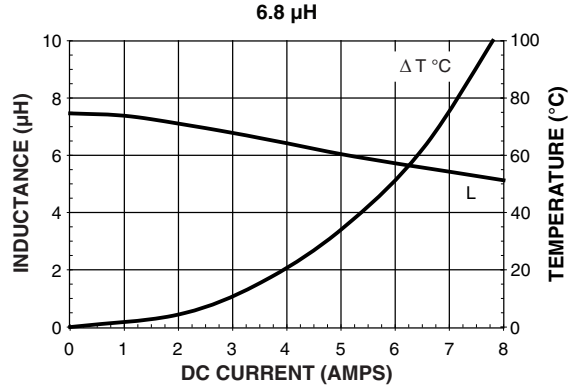
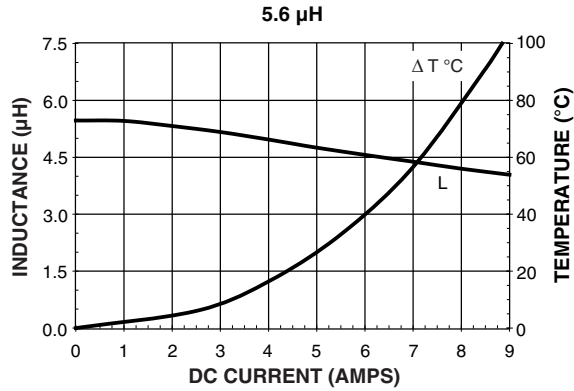


PERFORMANCE GRAPHS





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