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Low Profile, High Current Inductors



STANDARD ELECTRICAL SPECIFICATIONS								
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A	25 °C z, (mΩ)		HEAT RATING CURRENT DC (A) ⁽³⁾		SATURATION CURRENT DC (A) ⁽⁴⁾			
0.23 ν , ο Α (μΗ)	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.		
0.22	9.4	11.5	5.90	5.30	7.00	6.30		
0.24	12	15	5.10	4.60	6.20	5.50		
0.47	26	32	3.95	3.55	4.60	4.15		
1.0	45	55	3.40	3.00	3.70	3.35		
1.5	72	86	2.45	2.25	3.10	2.75		
2.2	94	112	1.90	1.70	2.60	2.35		

Notes

 $^{(1)}\,$ All test data is referenced to 25 °C ambient.

(2) Operating temperature range -55 °C to +125 °C .

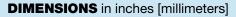
- ⁽³⁾ DC current (A) that will cause an approximate ΔT of 40 °C.
- ⁽⁴⁾ DC current (A) that will cause L_0 to drop approximately 30 %.
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

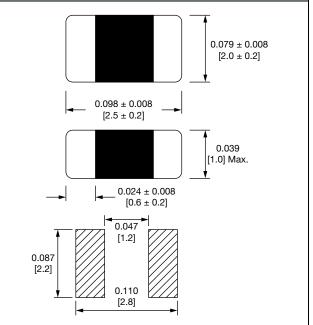
FEATURES

- · Magnetic alloy power choke coil
- Miniature size (2.5 x 2.0) and low profile
- Magnetic shielded
- Low acoustic noise and high efficiency
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)





DESCRIPTION					
IHHP-1008AZ-01	1.0 μH	± 20 %	ER	e3	
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC [®] LEAD (Pb)-FREE STANDARD	
GLOBAL PAR	T NUMBER				
ІНН	P 1 0	0 8 A Z	E R 1	R 0 M 0 1	
PRODUCT FAN	/ILY	SIZE	PACKAGE CODE	INDUCTANCE TOL. SERIES VALUE	
Revision: 21-Oct-16		1		Document Number: 3439	



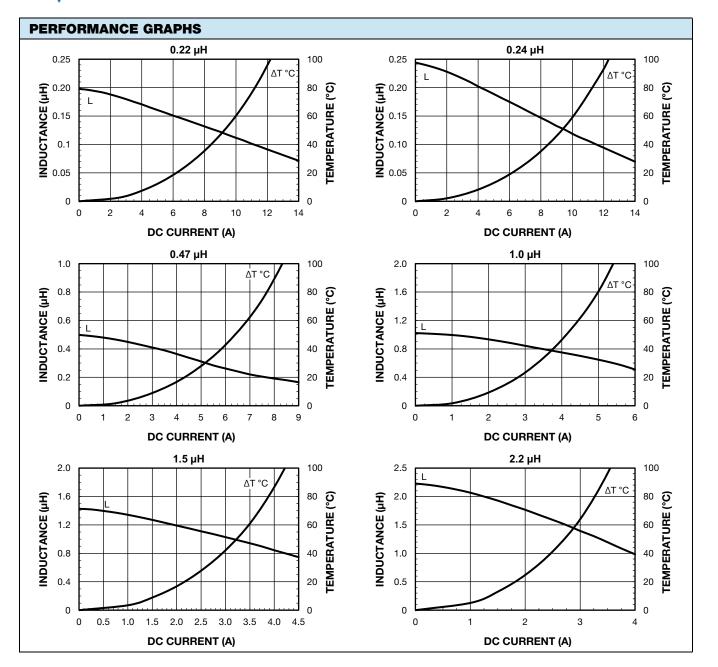
RoHS COMPLIANT HALOGEN FREE

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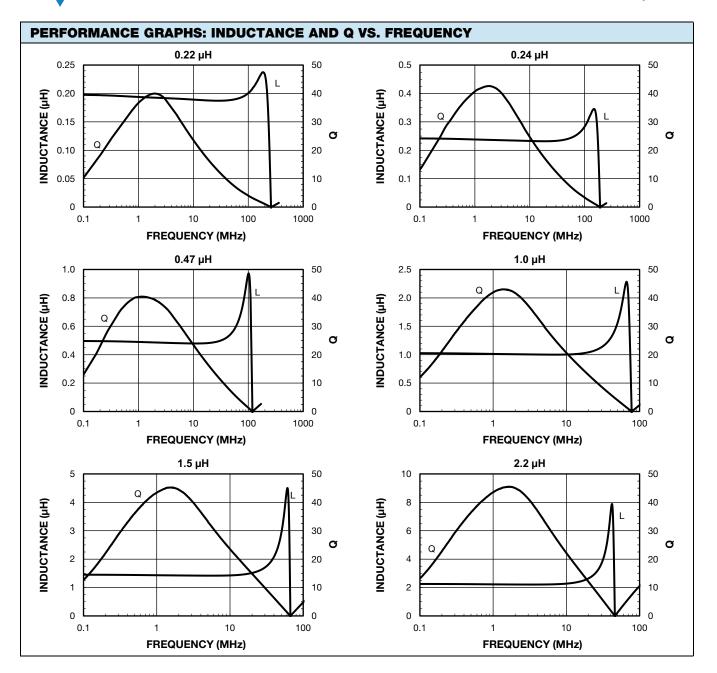
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