

Vishay Dale

Surface-Mount Oscillator



The XOSM-571 series is an ultra miniature package clock oscillator with dimensions 7.0 mm \times 5.0 mm \times 1.9 mm. It is mainly used in portable PC and telecommunication devices and equipment.

FEATURES

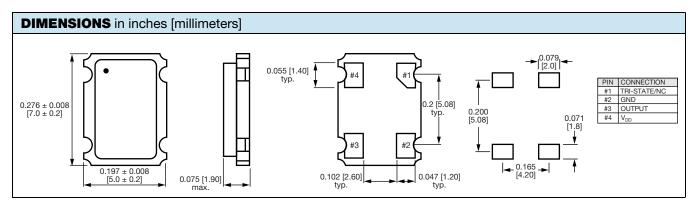
- Size: 7.0 x 5.0 x 1.9 (mm)
- Miniature package
- Tri-state enable / disable
- HCMOS compatible
- Tape and reel
- I_R re-flow
- 1.8 V input voltage
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



STANDARD ELECTRICAL SPECIFICATIONS			
PARAMETER	SYMBOL	CONDITION	VALUE
Frequency range	Fo	-	1.000 MHz to 70.000 MHz
Frequency stability (1)		All conditions	± 25 ppm, ± 50 ppm, ± 100 ppm
Operating temperature range	T _{OPR}	-	0 °C to 70 °C
			-40 °C to +85 °C (option)
Storage temperature range	T _{STG}	-	-55 °C to +125 °C
Power supply voltage	V _{DD}	-	1.8 V ± 10 %
Aging (first year)		25 °C ± 3 °C	± 5 ppm
Supply current	I _{DD}	1.000 MHz to 70.000 MHz	20 mA max.
Output symmetry	Sym	At ¹ / ₂ V _{DD}	40 %/60 % (45 %/55 % option)
Rise/fall time	t _r /t _f	1.000 MHz to 35.328 MHz	10 ns
		35.329 MHz to 70.000 MHz	4 ns
Output voltage	V _{OH}	-	90 % V _{DD} min.
	V _{OL}	-	10 % V _{DD} max.
Output load		-	10 TTL or 30 pF
Start-up time	t _s	-	10 ms max.
Pin 1, tri-state function		-	Pin 1 = H or open (output active at pin 3)
			Pin 1 = L (high impedance at pin 3)

Note

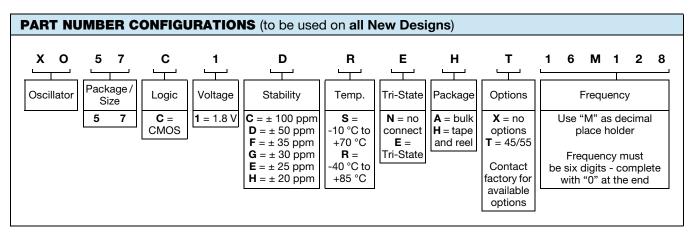
⁽¹⁾ Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock vibration



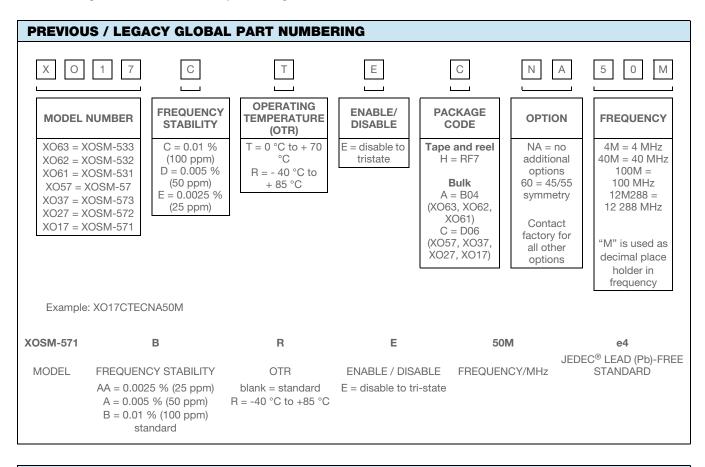
Note

A 0.01 μF bypass capacitor should be placed between V_{DD} (pin 4) and GND (pin 2) to minimize power supply line noise





Previous / legacy part number information: still valid for existing designs; all New Designs should use the new part configuration above



PART MARKING Line 1: M28_XXXXX (part number) Line 2: XX.XXXXM (frequency) Line 3: yywwvv (date/factory code)



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Vishay

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