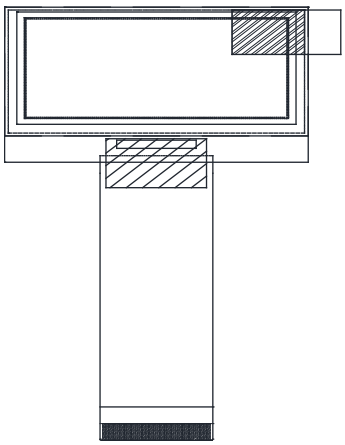
# 128 x 64 Graphic OLED



## FEATURES

- Type: graphic
- Display format: 128 x 64 dots
- Built-in controller: SSD1325Z1
- Duty cycle: 1/64
- +3 V power supply
- Interface: 6800, 8080, serial, and $I^2C$
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

RoHS COMPLIANT

## MECHANICAL DATA

| ITEM | STANDARD VALUE | UNIT |
|---|---|---|
| Module dimension | 41.8 x 27.9 x 1.8 | mm |
| Viewing area | 38.45 x 20.21 | |
| Active area | 36.45 x 18.21 | |
| Dot size | 0.255 x 0.255 | |
| Dot pitch | 0.285 x 0.285 | |
| Mounting hole | n/a | |

## ABSOLUTE MAXIMUM RATINGS

| ITEM | SYMBOL | STANDARD VALUE | | UNIT |
|---|---|---|---|---|
| | | MIN. | MAX. | |
| Supply voltage for logic (1)(2) | $V_{DD}$ | -0.3 | 4 | V |
| Supply voltage for display (1)(2) | $V_{CC}$ | 0 | 15 | |
| Operating temperature | $T_{OP}$ | -40 | +80 | °C |
| Storage temperature | $T_{STG}$ | -40 | +80 | |

**Notes**

(1) All the above voltages are on the basis of "$V_{SS}$ = 0 V".

(2) When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to section 6 "Electrical Characteristics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

## ELECTRICAL CHARACTERISTICS

| ITEM | SYMBOL | CONDITION | STANDARD VALUE | | | UNIT |
|---|---|---|---|---|---|---|
| | | | MIN. | TYP. | MAX. | |
| Supply voltage for logic | $V_{DD}$ | - | 2.8 | 3.0 | 3.3 | V |
| Supply voltage for display | $V_{CC}$ | - | 13.5 | 14 | 14.5 | |
| Input high voltage | $V_{IH}$ | - | 0.8 $V_{DD}$ | - | $V_{DD}$ | |
| Input low voltage | $V_{IL}$ | - | 0 | - | 0.2 $V_{DD}$ | |
| Output high voltage | $V_{OH}$ | - | 0.9 $V_{DD}$ | - | $V_{DD}$ | |
| Output low voltage | $V_{OL}$ | - | 0 | - | 0.1 $V_{DD}$ | |
| 50 % check board operating current | $I_{CC}$ | $V_{CC}$ = 14 V | 12 | 13 | 15 | mA |

## OPTIONS

| EMITTING COLOR | | | | |
|---|---|---|---|---|
| YELLOW | GREEN | RED | BLUE | WHITE |
| - | Yes | - | - | - |



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## INTERFACE PIN FUNCTION

| PIN NO. | SYMBOL | FUNCTION |
|---|---|---|
| 1 | NC (GND) | No connection |
| 2 | $V_{CC}$ | Power supply for OLED panel<br>This is the most positive voltage supply pin of the chip. It must be supplied externally. |
| 3 | $V_{COMH}$ | This pin is the input pin for the voltage output high level for COM signals. It can be supplied externally or internally. When $V_{COMH}$ is generated internally, a capacitor should be connected between this pin and $V_{SS}$. |
| 4 | $I_{REF}$ | This pin is segment current reference pin. A resistor should be connected between this pin and $V_{SS}$. Set the current at 10 µA. |
| 5 | D7 | These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input $S_{CLK}$. |
| 6 | D6 | |
| 7 | D5 | |
| 8 | D4 | |
| 9 | D3 | |
| 10 | D2 | |
| 11 | D1 | |
| 12 | D0 | |
| 13 | E / RD# | This pin is MCU interface input. When interfacing to a 6800-series microprocessor, this pin will be used as the enable (E) signal. Read / write operation is initiated when this pin is pulled high and the CS# is pulled low.<br>When connecting to an 8080-microprocessor, this pin receives the read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low. |
| 14 | R / W# | This pin is MCU interface input. When interfacing to a 6800-series microprocessor, this pin will be used as read / write (R / W#) selection input. Pull this pin to "high" for read mode and pull it to "low" for write mode. When 8080 interface mode is selected, this pin will be the write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low. |
| 15 | D / C# | This pin is data / command control pin. When the pin is pulled high, the input at D7to D0 is treated as display data. When the pin is pulled low, the input at D7 to D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the timing characteristics diagrams. |
| 16 | RES# | This pin is reset signal input. When the pin is low, initialization of the chip is executed. |
| 17 | CS# | This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low. |
| 18 | NC | No connection |
| 19 | BS2 | Communicating protocol select<br>These pins are MCU interface selection input. See the following table: |
| 20 | BS1 | |
| 21 | $V_{DD}$ | This is a voltage supply pin. It must be connected to external source. |
| 22 | NC | No connection |
| 23 | NC | No connection |
| 24 | NC | No connection |
| 25 | NC | No connection |
| 26 | NC | No connection |
| 27 | NC | No connection |
| 28 | NC | No connection |
| 29 | $V_{SS}$ | This is a ground pin. It also acts as a reference for the logic pins and the OLED driving voltages. It must be connected to external ground. |
| 30 | $V_{SL}$ | This pin is the output pin for the voltage output low level for SEG signals. A capacitor should be connected between this pin and $V_{SS}$. |

| | 68XX-parallel | 80XX-parallel | Serial | $I^2C$ |
|---|---|---|---|---|
| BS1 | 0 | 1 | 0 | 1 |
| BS2 | 1 | 1 | 0 | 0 |


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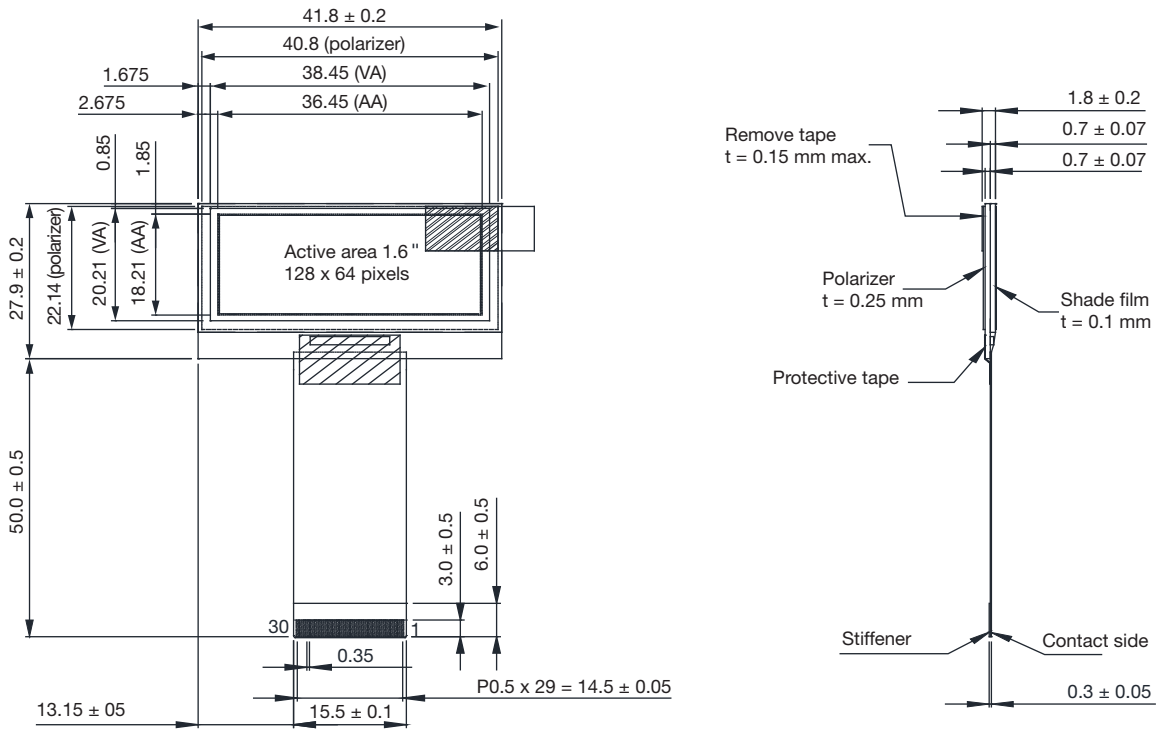
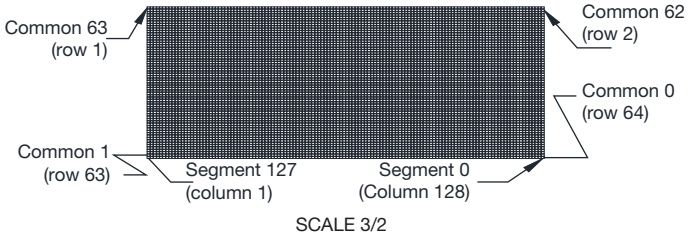
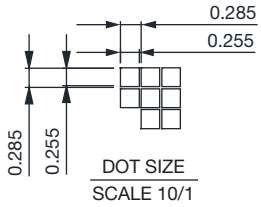
## **DIMENSIONS** in millimeters




SCALE 3/2




DOT SIZE
SCALE 10/1

The non-specific tolerance of dimensions is ± 0.3 mm.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# 1.Module Classification Information

| OLED | 128 | O | 064 | F | G | P | P | 3 | N | 0 | 0 | 000 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| ① | ② | ③ | ④ | ⑤ | ⑥ | ⑦ | ⑧ | ⑨ | ⑩ | ⑪ | ⑫ | ⑬ |

| | | | |
|---|---|---|---|
| 1 | Brand: Vishay Intertechnology, Inc. | | |
| 2 | Horizontal Format: 128 Columns | | |
| 3 | Display Type: N→Character Type, H→Graphic Type, Y→Tab Type, O→Cog | | |
| 4 | Vertical Format: 64 Lines | | |
| 5 | Serials code | | |
| 6 | Emitting Color | A：Amber | R：RED |
| | | B：Blue | W：White |
| | | G：Green | L：Yellow |
| 7 | Polarizer | P：With Polarizer; N: Without Polarizer | |
| 8 | Display Mode | P：Passive Matrix ; A: Active Matrix | |
| 9 | Driver Voltage | 3: 3.0 V; 5: 5.0V | |
| 10 | Touch Panel | N：Without touch panel; T: With touch panel | |
| 11 | Products type | 0：Standard type<br>1. Sunlight Readable type<br>2. Transparent OLED (TOLED)<br>3. Flexible OLED<br>4. OLED for Lighting | |
| 12 | Product grades | Product grades:<br>0：Standard(A-level)<br>2：B-level<br>3：C-level<br>4：high class(AA-level)<br>5：Customer offerings | |
| 13 | Serial No. | Application serial number(000~ZZZ) | |


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# 2.General Specification

| Item | Dimension | Unit |
|---|---|---|
| Dot Matrix | 128 x 64 | — |
| Module dimension | 41.8× 27.9 × 1.8 | mm |
| Active Area | 36.45 × 18.21 | mm |
| Pixel Size | 0.255 × 0.255 | mm |
| Pixel Pitch | 0.285 × 0.285 | mm |
| Display Mode | Passive Matrix | |
| Display Color | Green | |
| Drive Duty | 1/64 Duty | |
| IC | SSD1325 | |


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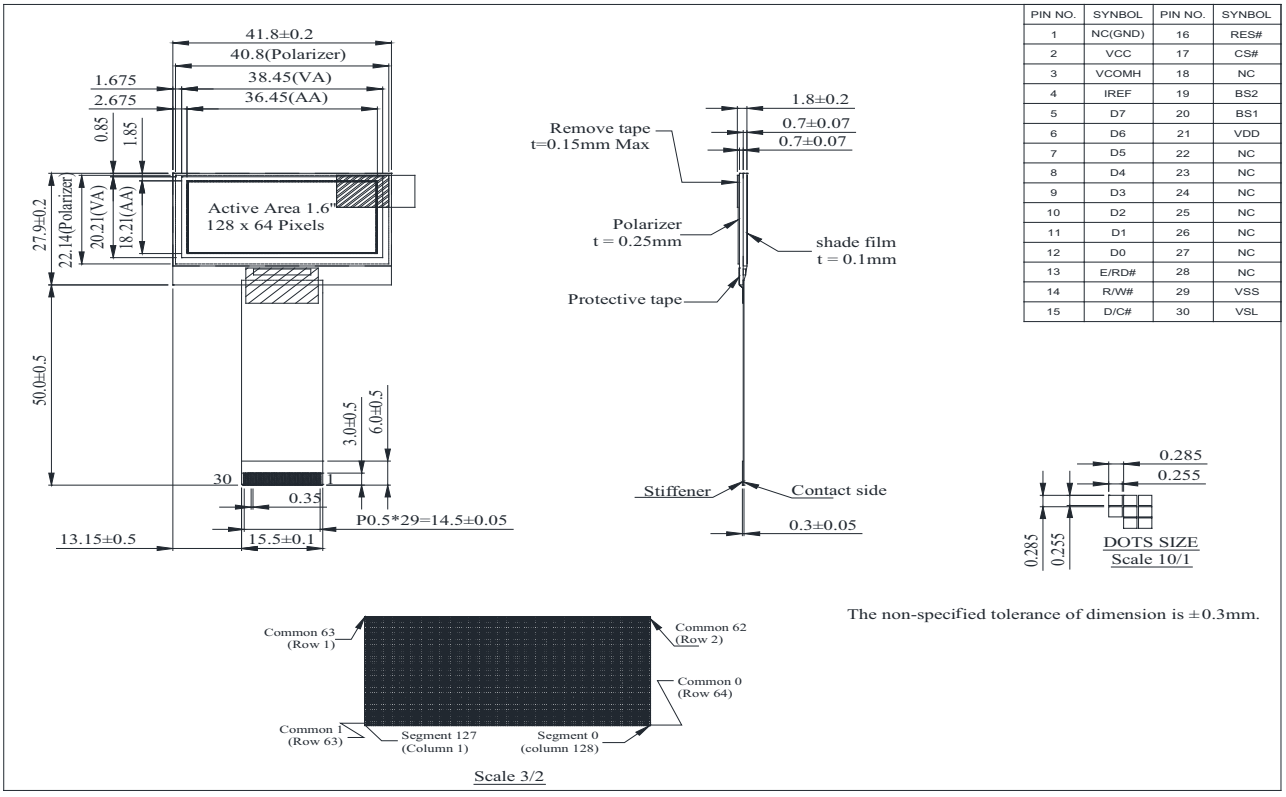
# 3. Contour Drawing & Block Diagram




| PIN NO. | SYNBOL | PIN NO. | SYNBOL |
|---|---|---|---|
| 1 | NC(GND) | 16 | RES# |
| 2 | VCC | 17 | CS# |
| 3 | VCOMH | 18 | NC |
| 4 | IREF | 19 | BS2 |
| 5 | D7 | 20 | BS1 |
| 6 | D6 | 21 | VDD |
| 7 | D5 | 22 | NC |
| 8 | D4 | 23 | NC |
| 9 | D3 | 24 | NC |
| 10 | D2 | 25 | NC |
| 11 | D1 | 26 | NC |
| 12 | D0 | 27 | NC |
| 13 | E/RD# | 28 | NC |
| 14 | R/W# | 29 | VSS |
| 15 | D/C# | 30 | VSL |

The non-specified tolerance of dimension is ±0.3mm.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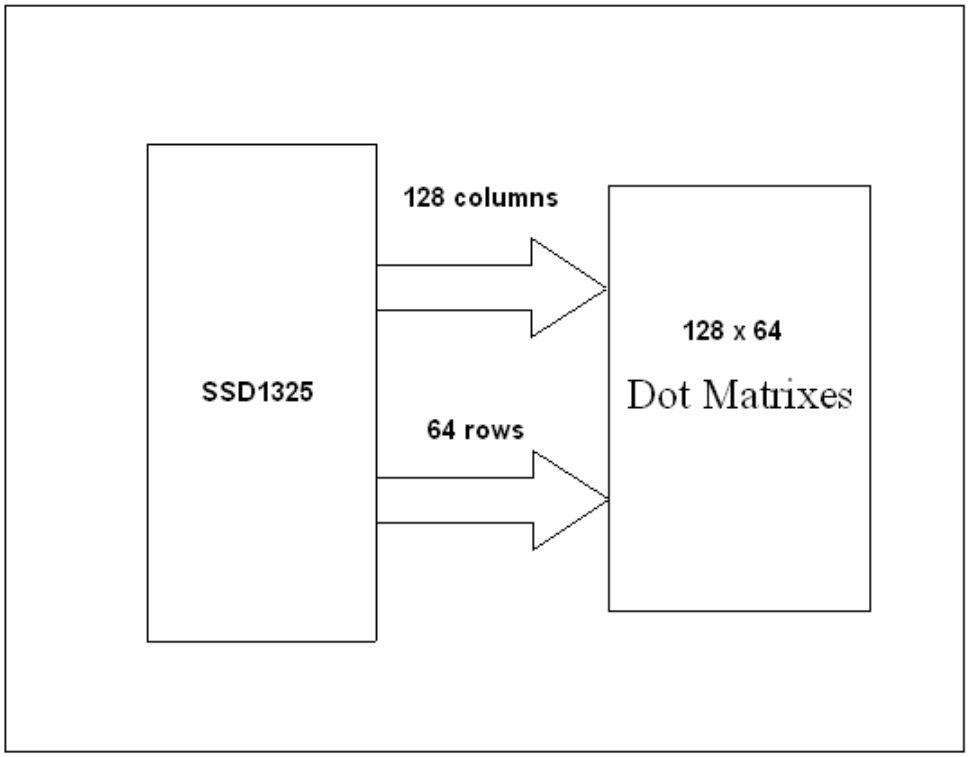
## FUNCTION BLOCK DIAGRAM




*For more information, please refer to Application Note provided by Vishay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# 4. Interface Pin Function

| Pin No. | Symbol | Level | Description |
|---|---|---|---|
| 1 | NC(GND) | | No Connection |
| 2 | VCC | | Power Supply for OLED Panel<br>This is the most positive voltage supply pin of the chip.<br>It must be supplied externally. |
| 3 | VCOMH | | This pin is the input pin for the voltage output high level for COM signals. It can be supplied externally or internally. When VCOMH is generated internally, a capacitor should be connected between this pin and VSS. |
| 4 | IREF | | This pin is segment current reference pin. A resistor should be connected between this pin and VSS. Set the current at 10uA. |
| 5 | D7 | | These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK. |
| 6 | D6 | | |
| 7 | D5 | | |
| 8 | D4 | | |
| 9 | D3 | | |
| 10 | D2 | | |
| 11 | D1 | | |
| 12 | D0 | | |
| 13 | E/RD# | | This pin is MCU interface input. When interfacing to a 6800-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 8080-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low. |
| 14 | R/W# | | This pin is MCU interface input. When interfacing to a 6800-series microprocessor, this pin will be used as<br>Read/Write (R/W#) selection input. Pull this pin to "High" for read mode and pull it to "LOW" for write mode. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low. |
| 15 | D/C# | | This pin is Data/Command control pin. When the pin is pulled high, the input at D7-D0 is treated as display data. When the pin is pulled low, the input at D7-D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams. |
| 16 | RES# | | This pin is reset signal input. When the pin is low, initialization of the chip is executed. |
| 17 | CS# | | This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low. |


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<table>
<tr><td>18</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>19<br>20</td><td>BS2<br>BS1</td><td></td><td>Communicating Protocol Select<br>These pins are MCU interface selection input. See the following table:
<table>
<tr><td></td><td>68XX-parallel</td><td>80XX-parallel</td><td>Serial</td><td>I2C</td></tr>
<tr><td>BS1</td><td>0</td><td>1</td><td>0</td><td>1</td></tr>
<tr><td>BS2</td><td>1</td><td>1</td><td>0</td><td>0</td></tr>
</table></td></tr>
<tr><td>21</td><td>VDD</td><td></td><td>This is a voltage supply pin. It must be connected to external source.</td></tr>
<tr><td>22</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>23</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>24</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>25</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>26</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>27</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>28</td><td>NC</td><td></td><td>No Connection</td></tr>
<tr><td>29</td><td>VSS</td><td></td><td>This is a ground pin. It also acts as a reference for the logic pins and the OLED driving voltages. It must be connected to external ground.</td></tr>
<tr><td>30</td><td>VSL</td><td></td><td>This pin is the output pin for the voltage output low level for SEG signals. A capacitor should be connected between this pin and VSS</td></tr>
</table>


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# 5.Absolute Maximum Ratings

| Parameter | Symbol | Min | Max | Unit | Notes |
|---|---|---|---|---|---|
| Supply Voltage for Logic | VDD | -0.3 | 4 | V | 1, 2 |
| Supply Voltage for Display | VCC | 0 | 15 | V | 1, 2 |
| Operating Temperature | TOP | -40 | +80 | °C | - |
| Storage Temperature | TSTG | -40 | +80 | °C | - |

Note 1: All the above voltages are on the basis of “VSS = 0V”.
Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 6 ‘’Electrical Characteristics’’. If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate


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# 6.Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Supply Voltage for Logic | VDD | — | 2.8 | 3.0 | 3.3 | V |
| Supply Voltage for Display | VCC | — | 13.5 | 14 | 14.5 | V |
| High Level Input | VIH | — | $0.8\times V_{DD}$ | — | $V_{DD}$ | V |
| Low Level Input | VIL | — | 0 | — | $0.2\times V_{DD}$ | V |
| High Level Output | VOH | — | $0.9\times V_{DD}$ | — | $V_{DD}$ | V |
| Low Level Output | VOL | — | 0 | — | $0.1\times V_{DD}$ | V |
| 50% Check Board operating Current | ICC | VCC =14 V | 12 | 13 | 15 | mA |


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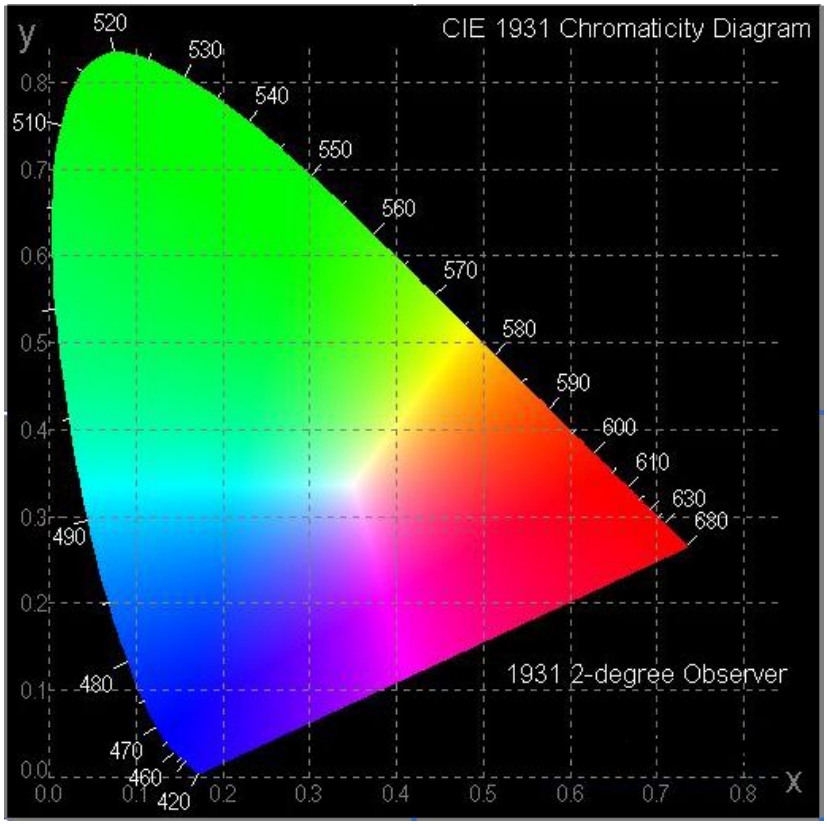
# 7.Optical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| View Angle | (V)θ | | 160 | | | deg |
| | (H)φ | | 160 | | | deg |
| Contrast Ratio | CR | Dark | 2000:1 | | — | — |
| Response Time | T rise | — | | 10 | | μs |
| | T fall | — | | 10 | | μs |
| Display with 50% check Board Brightness | | | 60 | 80 | | cd/m2 |
| CIEx(Green) | | (CIE1931) | 0.24 | 0.28 | 0.32 | |
| CIEy(Green) | | (CIE1931) | 0.59 | 0.63 | 0.67 | |





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# 8.OLED Lifetime

| ITEM | Conditions | Min | Typ | Remark |
|---|---|---|---|---|
| Operating Life Time | Ta=25℃ / Initial 50% check board brightness Typical Value | 40,000 Hrs | — | Note |

Notes:
1. Life time is defined the amount of time when the luminance has decayed to <50% of the initial value.
2. This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (*pdf*) for the product under normal use conditions.
3. Screen saving mode will extend OLED lifetime.


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# 9.Reliability

## Content of Reliability Test

| Environmental Test | | | |
|---|---|---|---|
| **Test Item** | **Content of Test** | **Test Condition** | **Applicable Standard** |
| High Temperature storage | Endurance test applying the high storage temperature for a long time. | 80 ℃<br>240hrs | —— |
| Low Temperature storage | Endurance test applying the low storage temperature for a long time. | -40℃<br>240hrs | —— |
| High Temperature Operation | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time. | 80 ℃<br>240hrs | —— |
| Low Temperature Operation | Endurance test applying the electric stress under low temperature for a long time. | -40 ℃<br>240hrs | —— |
| High Temperature/ Humidity Storage | Endurance test applying the high temperature and high humidity storage for a long time. | 60℃,90% RH<br>240hrs | —— |
| Temperature Cycle | Endurance test applying the low and high temperature cycle.<br>-40 ℃ 25℃<br>30min 5min 30min<br>1 cycle | -40 ℃/80℃<br>100 cycles | —— |
| Mechanical Test | | | |
| Vibration test | Endurance test applying the vibration during transportation and using. | 10~22Hz→1.5mmp-p<br>22~500Hz→1.5G<br>Total 0.5hr | —— |
| Shock test | Constructional and mechanical endurance test applying the shock during transportation. | 50G Half sin wave 11 ms<br>3 times of each direction | —— |
| Atmospheric pressure test | Endurance test applying the atmospheric pressure during transportation by air. | 115mbar<br>40hrs | —— |
| Others | | | |
| Static electricity test | Endurance test applying the electric stress to the terminal. | VS=±600V(contact),<br>±800v(air),<br>RS=330Ω<br>CS=150pF<br>10 times | —— |

*** Supply voltage for OLED system =Operating voltage at 25℃


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**Test and measurement conditions**

1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
2. All-pixels-on is used as operation test pattern.
3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/ Humidity Storage, Temperature Cycle

**Evaluation criteria**

1. The function test is OK.
2. No observable defects.
3. Luminance: > 50% of initial value.
4. Current consumption: within ± 50% of initial value.

**APPENDIX:**

**RESIDUE IMAGE**

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.


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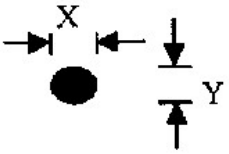
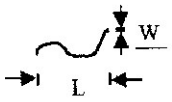
# 10.Inspection Specification

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>01</td><td>Electrical Testing</td><td>1.1 Missing vertical, horizontal segment, segment contrast defect.<br>1.2 Missing character , dot or icon.<br>1.3 Display malfunction.<br>1.4 No function or no display.<br>1.5 Current consumption exceeds product specifications.<br>1.6 OLED viewing angle defect.<br>1.7 Mixed product types.<br>1.8 Contrast defect.</td><td>0.65</td></tr>
<tr><td>02</td><td>Black or white spots on OLED (display only)</td><td>2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.<br>2.2 Densely spaced: No more than two spots or lines within 3mm.</td><td>2.5</td></tr>
<tr><td>03</td><td>OLED black spots, white spots, contamination (non-display)</td><td>3.1 Round type : As following drawing<br>Φ=( x + y ) / 2<br> 

<table>
<tr><th>SIZE</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.10</td><td>Accept no dense</td></tr>
<tr><td>0.10＜ Φ≦0.20</td><td>2</td></tr>
<tr><td>0.20＜ Φ≦0.25</td><td>1</td></tr>
<tr><td>0.25＜Φ</td><td>0</td></tr>
</table></td><td>2.5</td></tr>
<tr><td></td><td></td><td>3.2 Line type : (As following drawing)<br> 

<table>
<tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr>
<tr><td>---</td><td>W≦0.02</td><td>Accept no dense</td></tr>
<tr><td>L≦3.0</td><td>0.02＜W≦0.03</td><td rowspan="2">2</td></tr>
<tr><td>L≦2.5</td><td>0.03＜W≦0.05</td></tr>
<tr><td>---</td><td>0.05＜W</td><td>As round type</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.
<table>
<tr><th>Size Φ</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.20</td><td>Accept no dense</td></tr>
<tr><td>0.20＜Φ≦0.50</td><td>3</td></tr>
<tr><td>0.50＜Φ≦1.00</td><td>2</td></tr>
<tr><td>1.00＜Φ</td><td>0</td></tr>
<tr><td>Total Q TY</td><td>3</td></tr>
</table></td><td>2.5</td></tr>
</table>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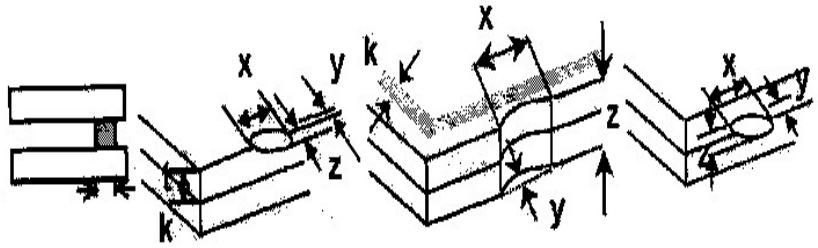
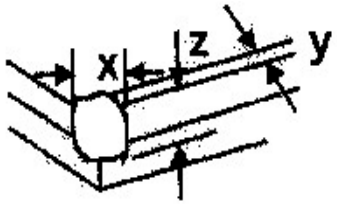
<table>
<tr><td>NO</td><td>Item</td><td>Criterion</td><td>AQL</td></tr>
<tr><td>05</td><td>Scratches</td><td>Follow NO.3 OLED black spots, white spots, contamination</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td>Symbols Define:<br>x: Chip length y: Chip width z: Chip thickness<br>k: Seal width t: Glass thickness a: OLED side length<br>L: Electrode pad length:<br><br>6.1 General glass chip :<br>6.1.1 Chip on panel surface and crack between panels:<br>  <br><br><table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙If there are 2 or more chips, x is total length of each chip.<br><br>6.1.2 Corner crack:<br>  <br><br><table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙If there are 2 or more chips, x is the total length of each chip.</td><td>2.5</td></tr>
</table>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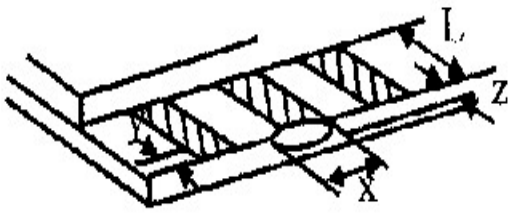
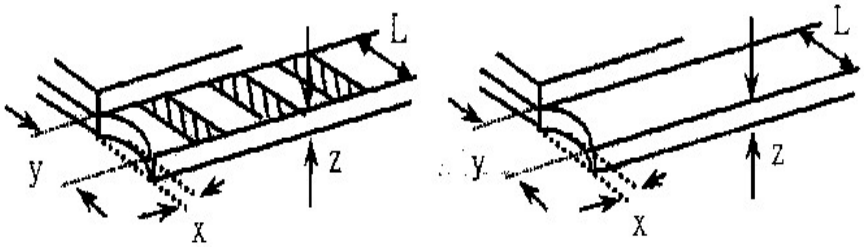
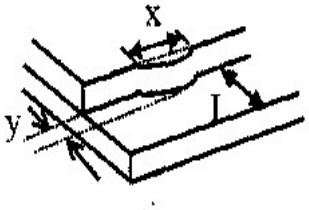
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>06</td><td>Glass crack</td><td>
Symbols :<br>
x: Chip length y: Chip width z: Chip thickness<br>
k: Seal width t: Glass thickness a: OLED side length<br>
L: Electrode pad length<br>
6.2 Protrusion over terminal :<br>
6.2.1 Chip on electrode pad :<br>

<table>
<tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
6.2.2 Non-conductive portion:<br>

<table>
<tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr>
<tr><td>y≦ L</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.<br>
⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.<br>
6.2.3 Substrate protuberance and internal crack.<br>

<table>
<tr><td>y: width</td><td>x: length</td></tr>
<tr><td>y≦1/3L</td><td>x ≦ a</td></tr>
</table>
</td><td>2.5</td></tr>
</table>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| NO | Item | Criterion | AQL |
|---|---|---|---|
| 07 | Cracked glass | The OLED with extensive crack is not acceptable. | 2.5 |
| 08 | Backlight elements | 8.1 Illumination source flickers when lit.<br>8.2 Spots or scratched that appear when lit must be judged. Using OLED spot, lines and contamination standards.<br>8.3 Backlight doesn't light or color wrong. | 0.65<br>2.5<br>0.65 |
| 09 | Bezel | 9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.<br>9.2 Bezel must comply with job specifications. | 2.5<br>0.65 |
| 10 | PCB、COB | 10.1 COB seal may not have pinholes larger than 0.2mm or contamination.<br>10.2 COB seal surface may not have pinholes through to the IC.<br>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.<br>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.<br>10.5 No oxidation or contamination PCB terminals.<br>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.<br>10.7 The jumper on the PCB should conform to the product characteristic chart.<br>10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down. | 2.5<br>2.5<br>0.65<br>2.5<br>2.5<br>0.65<br>0.65<br>2.5 |
| 11 | Soldering | 11.1 No un-melted solder paste may be present on the PCB.<br>11.2 No cold solder joints, missing solder connections, oxidation or icicle.<br>11.3 No residue or solder balls on PCB.<br>11.4 No short circuits in components on PCB. | 2.5<br>2.5<br>2.5<br>0.65 |


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| NO | Item | Criterion | AQL |
|---|---|---|---|
| 12 | General appearance | 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. | 2.5 |
| | | 12.2 No cracks on interface pin (OLB) of TCP. | 0.65 |
| | | 12.3 No contamination, solder residue or solder balls on product. | 2.5 |
| | | | 2.5 |
| | | 12.4 The IC on the TCP may not be damaged, circuits. | 2.5 |
| | | 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. | 2.5 |
| | | 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. | 2.5 |
| | | | 0.65 |
| | | 12.7 Sealant on top of the ITO circuit has not hardened. | 0.65 |
| | | 12.8 Pin type must match type in specification sheet. | 0.65 |
| | | 12.9 OLED pin loose or missing pins. | |
| | | 12.10 Product packaging must the same as specified on packaging specification sheet. | 0.65 |
| | | 12.11 Product dimension and structure must conform to product specification sheet. | |


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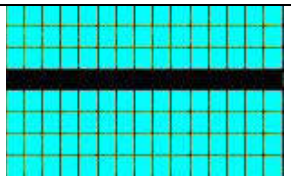
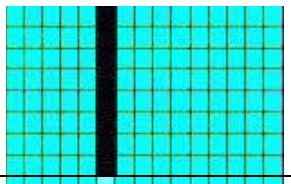
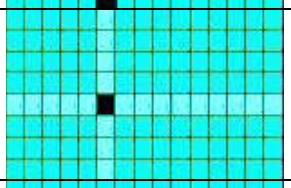
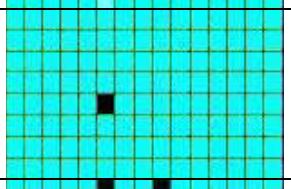
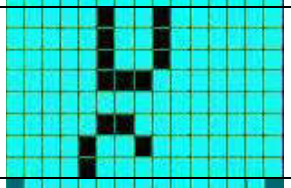
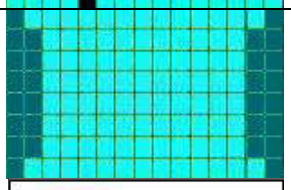
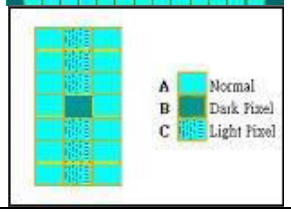
| Check Item | Classification | Criteria |
|---|---|---|
| No Display | Major | |
| Missing Line | Major |   |
| Pixel Short | Major |  |
| Darker Short | Major |  |
| Wrong Display | Major |  |
| Un-uniform<br>B/A x 100% < 70%<br>A/C x 100% < 70% | Major |    |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# 11.Precautions in use of OLED Modules

## Modules

(1)Avoid applying excessive shocks to module or making any alterations or modifications to it.
(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED display module.
(3)Don't disassemble the OLED display module.
(4)Don't operate it above the absolute maximum rating.
(5)Don't drop, bend or twist OLED display module.
(6)Soldering: only to the I/O terminals.
(7)Storage: please storage in anti-static electricity container and clean environment.
(8)It's pretty common to use "Screen Saver" to extend the lifetime and Don't use fix informatior for long time in real application.
(9)Don't use fixed information in OLED panel for long time, that will extend "screen burn" effect time..
(10) Vishay has the right to change the passive components, including R2and R3 adjust resisto (Resistors, capacitors and other passive components will have different appearance and col caused by the different supplier.)
(11) Vishay have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Vishay have the right to modi the version.)

### 11.1. Handling Precautions

(1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
(2) If the display panel is broken by some accident and the internal organic substance leaks ou be careful not to inhale nor lick the organic substance.
(3) If pressure is applied to the display surface or its neighborhood of the OLED display module the cell structure may be damaged and be careful not to apply pressure to these sections.
(4) The polarizer covering the surface of the OLED display module is soft and easily scratched Please be careful when handling the OLED display module.
(5) When the surface of the polarizer of the OLED display module has soil, clean the surface. I takes advantage of by using following adhesion tape.
* Scotch Mending Tape No. 810 or an equivalent
Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent
such as ethyl alcohol, since the surface of the polarizer will become cloudy.
Also, pay attention that the following liquid and solvent may spoil the polarizer:
* Water
* Ketone
* Aromatic Solvents
(6) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, d not over bend the film with electrode pattern layouts.


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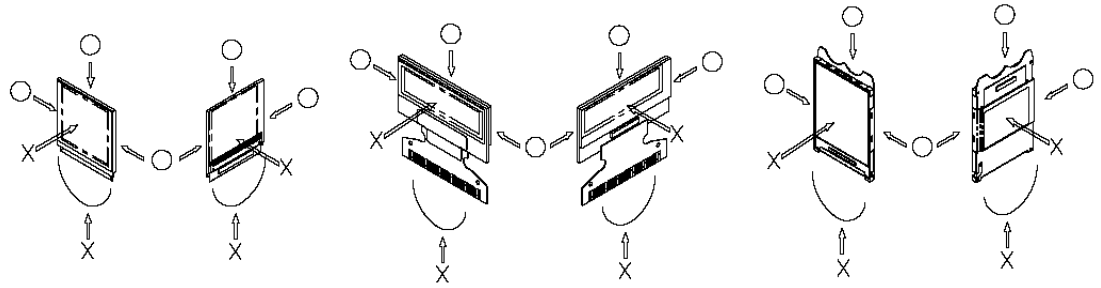

(7) Do not apply stress to the LSI chips and the surrounding molded sections.
(8) Do not disassemble nor modify the OLED display module.
(9) Do not apply input signals while the logic power is off.
(10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
* Be sure to make human body grounding when handling OLED display modules.
* Be sure to ground tools to use or assembly such as soldering irons.
* To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
* Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
(11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
(12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

**11.2. Storage Precautions**

(1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. and, also, avoiding high temperature and high humidity environment or low temperature (less than 0°C) environments.
(We recommend you to store these modules in the packaged state when they were shipped from Vishay.
At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
(2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

**11.3. Designing Precautions**

(1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
(2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
(3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)
(4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
(5) As for EMI, take necessary measures on the equipment side basically.


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(6) When fastening the OLED display module, fasten the external plastic housing section.
(7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module.
* Connection (contact) to any other potential than the above may lead to rupture of the IC.

**11.4. Precautions when disposing of the OLED display modules**

1) Request the qualified companies to handle industrial wastes when disposing of the OLED display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

**11.5. Other Precautions**

(1) When an OLED display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.
Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module.
(2) To protect OLED display modules from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the OLED display modules.
* Pins and electrodes
* Pattern layouts such as the TCP & FPC
(3) With this OLED display module, the OLED driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this OLED driver is exposed to light, malfunctioning may occur.
* Design the product and installation method so that the OLED driver may be shielded from light in actual usage.
* Design the product and installation method so that the OLED driver may be shielded from light during the inspection processes.
(4) Although this OLED display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. It therefore is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
(5) We recommend you to construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise.
(6)Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.
(7)Our company will has the right to upgrade and modify the product function.


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