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Wet Tantalum SMD Capacitors, Tantalum Metal Case With Glass-to-Tantalum Hermetic Seal





LINKS TO ADDITIONAL RESOURCES



PERFORMANCE CHARACTERISTICS

Operating Temperature: -55 °C to +85 °C (to +125 °C with voltage derating)

DC Leakage Current (DCL Max.): at +25 °C and above: leakage current shall not exceed the values listed in the Standard Ratings table.

Capacitance Range: 10 µF to 110 µF

Capacitance Tolerance: ± 10 %, ± 20 % standard

Voltage Rating: 50 V_{DC} to 125 V_{DC}

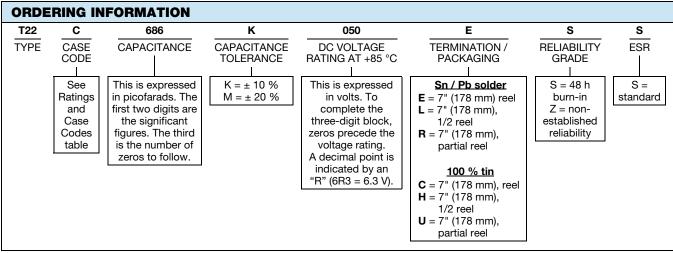
FEATURES

- Advanced SMD packaging with high volumetric efficiency, patents pending
- Pb-free
 Available
- Enhanced performance, high reliability design
- SMD, standard tin / lead (Sn / Pb), 100 % tin (RoHS-compliant) available
- Available
 HALOGEN
 FREE
 GREEN
 (5-2008)

- Mounting: surface-mount
- Increased thermal shock capability of 300 cycles
- Designed for the avionics and aerospace applications
- PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details



Note

· We reserve the right to supply better series with more extensive screening

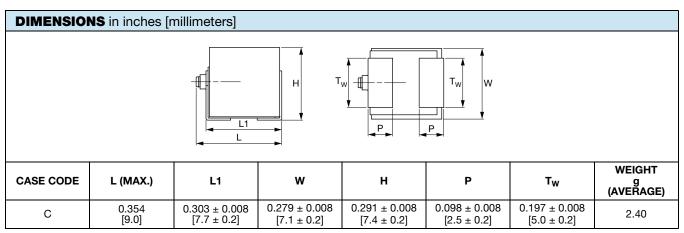
PATENT(S): www.vishay.com/patents

Revision: 13-Aug-2024

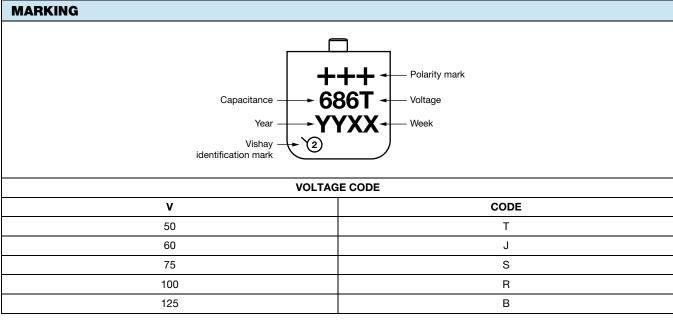
This Vishay product is protected by one or more United States and international patents.



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| RATINGS AND | RATINGS AND CASE CODES | | | | | | | | | | | | | |
|-------------|------------------------|------|------|-------|-------|--|--|--|--|--|--|--|--|--|
| μF | 50 V | 60 V | 75 V | 100 V | 125 V | | | | | | | | | |
| 10 | | | | | С | | | | | | | | | |
| 15 | | | | С | | | | | | | | | | |
| 33 | | | С | | | | | | | | | | | |
| 47 | | С | | | | | | | | | | | | |
| 68 | С | С | | | | | | | | | | | | |
| 110 | С | | | | | | | | | | | | | |



Note

Capacitors may bear T24 and DLA 20012 marking scheme if parts are substituted with high performance grade T24 and DLA 20012 products. This includes letter "H" or "A" instead of central (+) in polarity mark. Call the factory for further explanation





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| STANDARD | RATIN | GS | | | | | | | | |
|--------------------------|-------|------------------------|--------------------------|--------------------------|-----------|--------------------------|-----------|--------------|---------|--|
| CAPACITANCE AT +25 °C | CASE | | MAX. ESR AT +25 °C | MAX. IMP. AT -55 °C | | . DCL) AT | MAX. C | AC RIPPLE | | |
| 120 Hz (μF) | CODE | PART NUMBER | 120 Hz (Ω) | 120 Hz (Ω) | +25 °C | +85 °C AND +125 °C | -55 °C | +85 °C | +125 °C | +85 °C 40 kHz (mA _{RMS}) |
| | | 50 | V _{DC} AT +85 | °C; 30 V _{DC} A | Γ+125 °C | | | | | |
| 68 | С | T22C686(1)050(2)(3)(4) | 1.50 | 35 | 1 | 5 | -25 | 8 | 15 | 1650 |
| 110 | С | T22C117(1)050(2)(3)(4) | 1.00 | 40 | 1 | 10 | -40 | 14 | 16 | 1590 |
| | | 60 | V _{DC} AT +85 | °C; 40 V _{DC} A | Γ+125 °C | | | | | |
| 47 | С | T22C476(1)060(2)(3)(4) | 1.50 | 40 | 1 | 10 | -40 | 14 | 16 | 1590 |
| 68 | С | T22C686(1)060(2)(3)(4) | 1.50 | 40 | 1 | 10 | -40 | 14 | 16 | 1590 |
| | | 75 | V _{DC} AT +85 | °C; 50 V _{DC} A | Γ+125 °C | | | | | |
| 33 | С | T22C336(1)075(2)(3)(4) | 2.50 | 66 | 1 | 5 | -25 | 5 | 9 | 1310 |
| | | 100 | V _{DC} AT +85 | °C; 65 V _{DC} A | T +125 °C | ; | | | | |
| 15 | С | T22C156(1)100(2)(3)(4) | 3.50 | 125 | 1 | 5 | -18 | 3 | 10 | 1030 |
| | | 125 | 5 V _{DC} AT +85 | °C; 85 V _{DC} A | T +125 °C | | | | | |
| 10 | С | T22C106(1)125(2)(3)(4) | 5.50 | 175 | 1 | 5 | -15 | 3 | 10 | 832 |

Note

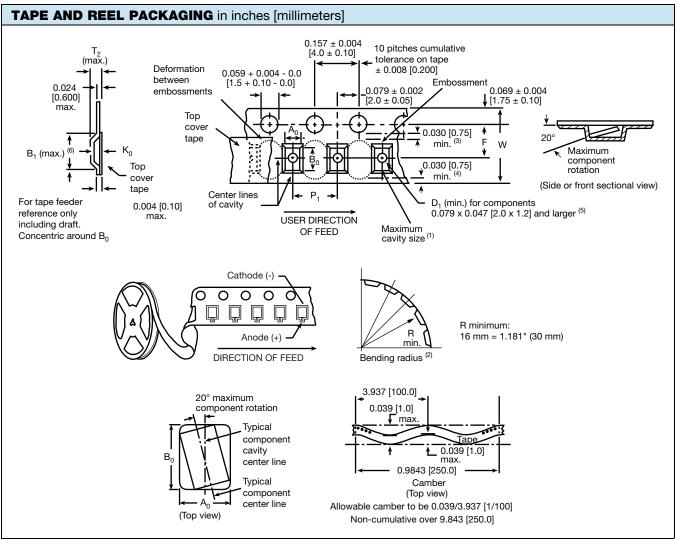
- Part number definitions:
 - (1) Capacitance tolerance: K, M
 - (2) Termination and packaging: C, H, E, L, R, U
 - (3) Reliability level: Z, S
 - (4) ESR: S

| POWER DISSIPATION | |
|-------------------|---|
| CASE CODE | MAXIMUM PERMISSIBLE POWER DISSIPATION AT +25 °C (W) IN FREE AIR |
| С | 0.9 |

| STANDARD PACKAGING QUANTITY | | | | | | | | | |
|-----------------------------|--------------|----------------|-----------------|--|--|--|--|--|--|
| CASE CODE | | UNITS PER REEL | | | | | | | |
| CASE CODE | 7" FULL REEL | 7" HALF REEL | 7" PARTIAL REEL | | | | | | |
| С | 100 | 50 | 25 | | | | | | |



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Notes

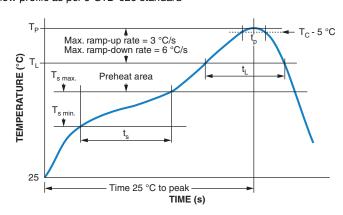
- Metric dimensions will govern. Dimensions in inches are rounded and for reference only
- (1) A₀, B₀, K₀, are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀, K₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°
- (2) Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide "R" minimum for 12 mm embossed tape for reels with hub diameters approaching N minimum
- (3) This dimension is the flat area from the edge of the sprocket hole to either outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less
- (4) This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less
- (5) The embossed hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location shall be applied independent of each other
- (6) B₁ dimension is a reference dimension tape feeder clearance only

| CARRIER TAPE | CARRIER TAPE DIMENSIONS in inches [millimeters] | | | | | | | | | | | | | |
|----------------|---|---|-----------------------|------------------------------|-----------------------|-------------------------------|--|--|--|--|--|--|--|--|
| CAPACITOR TYPE | CASE CODE | TAPE WIDTH W | B ₁ (max.) | F | K ₀ (max.) | P ₁ | | | | | | | | |
| T22 | С | 0.630 + 0.012 / - 0.004 [16.0 + 0.3 / - 0.1] | 0.45 [11.3] | 0.295 ± 0.004 [7.5 ± 0.1] | 0.31 [7.9] | 0.476 ± 0.004 [12.0 ± 0.1] | | | | | | | | |



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RECOMMENDED REFLOW PROFILES Capacitors should withstand reflow profile as per J-STD-020 standard



| PROFILE FEATURE | SnPb EUTECTIC ASSEMBLY | LEAD (Pb)-FREE ASSEMBLY |
|--|------------------------|-------------------------|
| Preheat / soak | | |
| Temperature min. (T _{s min.}) | 100 °C | 150 °C |
| Temperature max. (T _{s max.}) | 150 °C | 200 °C |
| Time (t_s) from ($T_{s\ min.}$ to $T_{s\ max.}$) | 60 s to 120 s | 60 s to 120 s |
| Ramp-up | | |
| Ramp-up rate (T _L to T _P) | 3 °C/s max. | 3 °C/s max. |
| Liquidus temperature (T _L) | 183 °C | 217 °C |
| Time (t _L) maintained above T _L | 60 s to 150 s | 60 s to 150 s |
| Peak package body temperature (T _p) | 220 | 245 |
| Time (t_p) within 5 °C of the specified classification temperature (T_C) | 20 s | 30 s |
| Time 25 °C to peak temperature | 6 min max. | 8 min max. |
| Ramp-down | | |
| Ramp-down rate (T _P to T _L) | 6 °C/s max. | 6 °C/s max. |
| Time 25 °C to peak temperature | 6 min max. | 8 min max. |

PAD DIMENSIONS in inches [millimeters] CASE CODE A (MIN.) B (NOM.) C (NOM.) D (NOM.) C 0.295 [7.50] 0.138 [3.50] 0.100 [2.50] 0.374 [9.50]

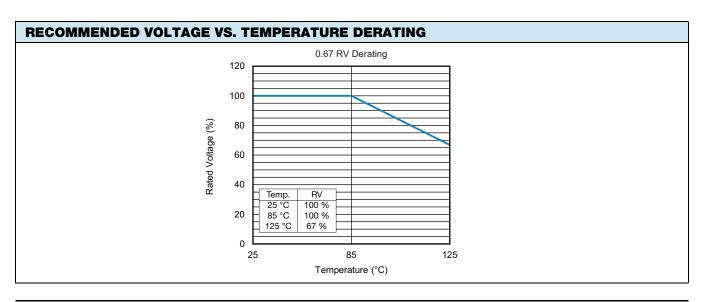


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TYPICAL PERFORMANCE CHARACTERISTICS OF T22 CAPACITORS

| ITEM | PERFORMANCE CHARACTERISTICS | | | | | | | | | |
|-----------------------------------|---|---|---------------------------------------|--|--|--|--|--|--|--|
| Category temperature range | -55 °C to +85 °C (to +125 °C with voltage derating) | | | | | | | | | |
| Capacitance tolerance | ± 20 %, ± 10 % at +25 °C, 120 I | ± 20 %, ± 10 % at +25 °C, 120 Hz | | | | | | | | |
| Capacitance change by temperature | Limit per Standard Ratings table | • | | | | | | | | |
| ESR | Limit per Standard Ratings table | e, at +25 °C, 120 Hz | | | | | | | | |
| Impedance | Limit per Standard Ratings table | e, at -55 °C, 120 Hz | | | | | | | | |
| DCL (leakage current) | Limit per Standard Ratings table | • | | | | | | | | |
| AC ripple current | Limit per Standard Ratings table | e, at +85 °C and 40 kHz | | | | | | | | |
| Reverse voltage | | Reverse voltage shall be in accordance with MIL-PRF-39006, paragraphs 3.23 and 4.8.19, except DC potential will be maximum of 3 V | | | | | | | | |
| Maximum operating voltage | OPERATING TEMPERATURE | | | | | | | | | |
| | +85 | +125 °C | | | | | | | | |
| | RATED VOLTAGE (V _{DC}) | SURGE VOLTAGE (V _{DC}) | DERATED VOLTAGE (V _{DC}) | | | | | | | |
| | 50 | 57.5 | 30 | | | | | | | |
| | 75 | 86.2 | 50 | | | | | | | |
| | 100 115.0 65 | | | | | | | | | |
| | 125 144.0 85 | | | | | | | | | |
| Surge voltage | The DC surge voltage is the maximum voltage to which the capacitor can be subjected under any conditions including transients and peak ripple at the highest line voltage. The DC surge voltage is 115 % of rated DC voltage | | | | | | | | | |

| PERFORM | ANCE CHARACTERISTICS | | |
|----------------|--|--|--|
| ITEM | CONDITION | POST TEST PERFORMANCE | |
| Surge voltage | In accordance with MIL-PRF-39006: 85 °C 1000 successive test cycles at the applicable DC surge voltage specified in series with a 1 k Ω resistor at the rate of 30 s ON, 5.5 min OFF | Capacitance change Leakage current | Within ± 10 % of initial measured value Not to exceed specified value |
| Life testing | In accordance with MIL-PRF-39006: capacitors shall be capable of withstanding a 2000 h life test at a temperature +85 °C at rated voltage, or a 2000 h life test at a temperature +125 °C at derated voltage | Capacitance change Leakage current at 85 °C / 125 °C Leakage current at 25 °C ESR | +10 % / -20 % of initial measured value Not to exceed 125 % of initial specified value Not to exceed specified value Not to exceed 200 % of specified value |
| AC ripple life | In accordance with MIL-PRF-39006: 20 | 000 h, +85 °C | |





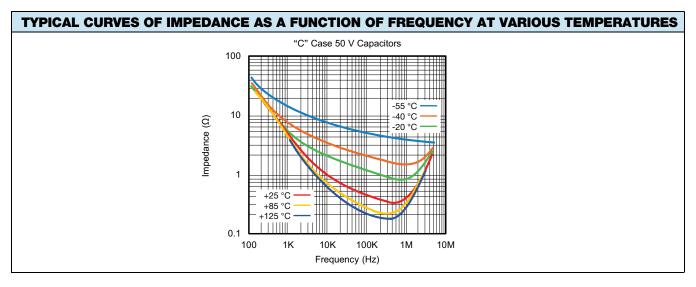
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| ENVIRONMENTAL CHARACTERISTICS | | | | | | | | | | |
|--|---|--|--|--|--|--|--|--|--|--|
| ITEM | CONDITION | POST TEST PERFORMANCE | | | | | | | | |
| Stability at low and high temperatures | As specified in MIL-PRF-39006 | The capacitors shall meet the requirements of MIL-PRF-39006 | | | | | | | | |
| Seal | MIL-PRF-39006 Method 112 of MIL-STD-202, conditions A and C When the capacitors are tested as specified in MIL there shall be no evidence of leakage. | | | | | | | | | |
| Moisture resistance | MIL-PRF-55365 Method 106 of MIL-STD-202, number of cycles: 10 continuous cycles except that steps 7a and 7b shall be omitted. | DC leakage Capacitance change ESR Not exceed 125 % of the specified value Within ±10 % of the initial measured value Not exceed the specified value | | | | | | | | |
| Barometric pressure (reduced) | Method 105 of MIL-STD-202, condition E (150 000 feet) (45,720.1 m). | There shall be no mechanical or visual damage to capacitors post-conditioning. | | | | | | | | |
| Low temperature storage | MIL-PRF-39006 Method 502 of MIL-STD-810, Storage temperature: - 62 °C + 0 °C, - 3 °C Exposure time: 72 h followed by a 1 h exposure at + 125 °C + 7 °C, - 0 °C within 24 h after low temperature storage. | DC leakage Capacitance change ESR Not to exceed 125 % of the specified value Within ± 10 % of the initial measured value Not exceed the specified value | | | | | | | | |
| Salt atmosphere (corrosion) | MIL-PRF-39006 Method 101 of MIL-STD-202, condition B (48 h), applicable salt solution: 5 % | There shall be no harmful corrosion. Marking shall remain legible. | | | | | | | | |

| MECHANICAL | PERFORMANCE CHARACTERISTIC | cs |
|------------------------------|---|--|
| ITEM | TEST METHOD | CONDITION |
| Shear test | AEC-Q200-006 Apply a pressure load of 5 N for 10 s ± 1 s horizontally to the center of capacitor side body. | DC leakage Capacitance change ESR Not to exceed 125 % of the specified value Within ± 10 % of the initial measured value Not exceed the specified value There shall be no mechanical or visual damage to capacitors post-conditioning. |
| Solderability | MIL-STD-202, method 208 | Test B ANSI/J-STD-002: SnPb solder - test B Pb-free solder - test B1 All terminations shall exhibit a continuous solder coating free from defects for a minimum of 95 % of the critical area of any individual lead. |
| Resistance to solvent | MIL-STD-202, method 215 | There shall be no mechanical or visual damage to capacitors post-conditioning. Marking shall remain legible, no degradation of the can material. |
| Insulation resistance | MIL-STD-202, method 302 | Test condition B (500 V _{DC} \pm 10 %) The insulation resistance shall be not less than 100 M Ω . The capacitors shall meet the requirements of MIL-PRF-39006. |
| Shock (specified pulse) | MIL-STD-202, method 213 | Test condition D (500 g) |
| Vibration, high frequency | MIL-STD-202, method 204 | Test condition H (80 g peak) |
| Random vibration | MIL-STD-202, method 214 | Test condition II-K (53.79 g RMS) |
| Thermal shock | MIL-STD-202, method 107 | Test condition A, 300 cycles |
| Resistance to soldering heat | MIL-STD-202, method 210 | Test condition J, except with only one heat cycle Capacitance change Within ± 10 % of initial ESR Initial specified value or less Leakage current Initial specified value or less There shall be no mechanical or visual damage to capacitors post-conditioning. |



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PERFORMANCE CHARACTERISTICS

 Operating Temperature: capacitors are designed to operate over a temperature range of -55 °C to +125 °C.

| UP TO +85 °C (V) | AT +125 °C (V) |
|---------------------|-------------------|
| 50 | 30 |
| 75 | 50 |
| 100 | 65 |
| 125 | 85 |

- DC Working Voltage: the DC working voltage is the maximum operating voltage for continuous duty at the rated temperature.
- Surge Voltage: the surge voltage rating is the maximum voltage to which the capacitors should be subjected under any conditions. This includes transients and peak ripple at the highest line voltage.
- 3.1 The surge voltage of capacitors is 115 % of rated DC working voltage.
- 3.2 **Surge Voltage Test:** capacitors shall withstand the surge voltage applied through a $1000~\Omega \pm 10~\%$ resistor in series with the capacitor and voltage source at the rate of one-half minute on, five and one-half minutes off, for 1000~successive test cycles at +85~°C.
- 3.3 Following the surge voltage test, the capacitance at $+25\,^{\circ}\text{C}$ shall not have changed by more than $\pm\,10\,\%$ and the equivalent series resistance and DC leakage current will not exceed the values shown in the Standard Ratings table for each capacitor.
- Capacitance Tolerance: the capacitance of all capacitors shall be within the specified tolerance limits of the nominal rating.
- 4.1 Measurements shall be made by the bridge method at or referred to a frequency of 120 Hz at a temperature of +25 °C. The maximum voltage applied to the capacitors during measurement shall be 1 V_{RMS}. Measurement accuracy of the bridge shall be within \pm 2 %.
- Capacitance Change With Temperature: the capacitance change with temperature shall not exceed the values given in the Standard Ratings table for each capacitor.

- Equivalent Series Resistance: measurements shall be made by the bridge method at, or referred to, a frequency of 120 Hz at a temperature of +25 °C. A maximum of 1 V_{RMS} shall be applied during measurement.
- 6.1 The equivalent series resistance shall not exceed the maximum value in ohms listed in the Standard Ratings table for each capacitor.
- 6.2 The dissipation factor may be calculated from the equivalent series resistance and capacitance values as shown:

$$\mathsf{DF} \,=\, \frac{2\pi \mathsf{fRC}}{\mathsf{10}^4}$$

where:

DF = dissipation factor in %

 $R = ESR in \Omega$

 $C = capacitance in \mu F$

f = frequency in Hz

At 120 Hz, the above equation becomes:

$$DF = \frac{R \times C}{13.26}$$

For example, percent dissipation factor of a 30 μ F, 6 V capacitor, which has a maximum ESR of 4.0 Ω at +25 °C and 120 Hz, would be calculated as shown:

$$DF = \frac{2\pi \times 120 \times 4 \times 30}{10^4} = \frac{4 \times 30}{13.26} = 9.05 \%$$

7. **Leakage Current:** measurements shall be made at the applicable rated working voltage at +25 °C \pm 5 °C through application of a steady source of power, such as a regulated power supply. A 1000 Ω resistor to limit the charging current shall be connected in series with each capacitor under test. Rated working voltage shall be applied to capacitors for 5 minutes before making leakage current measurements.

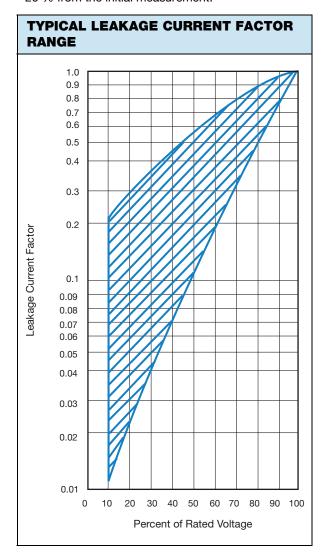


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7.1 The maximum leakage current for any capacitor shall not exceed the maximum value in µA listed in the Standard Ratings table for each capacitor.

Note

- Leakage current varies with applied voltage. See graph next column for the appropriate adjustment factor
- 8. **Low Temperature Impedance:** the impedance of any capacitor at -55 °C at 120 Hz, shall not exceed the values given in the Standard Ratings table.
- 9. **Life Test:** capacitors are capable of withstanding a 2000 h life test at a temperature of +85 °C or +125 °C at the applicable rated DC working voltage.
- 9.1 Following the life test, the capacitors shall be returned to 25 °C \pm 5 °C. The leakage current, measured at the \pm 85 °C rated voltage, shall not be in excess of the original requirement; the capacitance value shall not exceed 150 % of the initial requirement; the capacitance value shall not change more than \pm 10 % / \pm 20 % from the initial measurement.



- Ripple Life Test at +85 °C: capacitors shall be tested in accordance with military specification MIL-PRF-39006 except that:
 - a) Operation conditions: this test shall be run at a frequency of 40 kHz ± 2 kHz sinusoidal and at the RMS ripple current levels specified in the Standard Ratings table.
 - b) Applied DC voltage shall be reduced so that the peak AC voltage plus DC voltage shall not exceed the rated voltage of the capacitor in either the forward or reverse direction.
- 10.1 When tested as specified above, capacitors shall meet the following requirements:
 - a) The DC leakage current at +25 °C and at +85 °C shall not exceed the original requirements.
 - b) The capacitance shall not change more than \pm 15 % from the initial measured value.
 - The dissipation factor shall not exceed the original requirements.
 - d) Visual examination: There shall be no damage, obliteration of marking or leakage of electrolyte.

GUIDE TO APPLICATION

 AC Ripple Current: subjecting a capacitor to an AC voltage causes an AC current to flow through it. The amplitude of the current is dependent on the impedance of the capacitor at the frequency of the applied signal:

$$I = \frac{V}{Z}$$

where:

I = ripple current

V = applied AC voltage

Z = impedance of capacitor (frequency dependent)

This current causes heating in the capacitor because of I^2R losses (R is the equivalent series resistance at the applied frequency). This heating or power dissipation, is one of the limiting factors of the capacitor's ripple current rating.

These power dissipation ratings are based on a calculated +50 °C internal temperature rise in still air. The maximum allowable ripple currents given in the Standard Ratings table are based on these ratings and the maximum equivalent series resistance at that frequency.

The relationship is written as follows:

$$P = I^2R$$

where:

P = maximum power

I = maximum ripple current

R = equivalent series resistance

Therefore:

$$I = \sqrt{\frac{P}{R}}$$

where:

R is in Ω

P is in W

I is in A_{RMS}



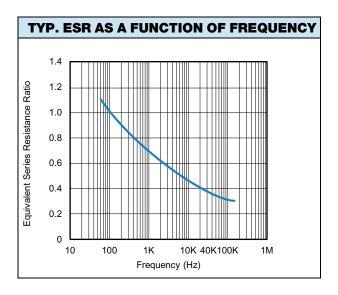
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 AC Ripple Voltage: in operation, the peak voltage across the capacitor (DC working voltage plus peak ripple voltage) must not exceed the rated working voltage of the capacitor. The DC component of the applied voltage should be sufficiently large to prevent polarity reversal in excess of 3 V at +85 °C or 2 V at 125 °C.

There will be a point at the lower frequency and capacitance values when the peak AC voltage will be the limiting factor on the ripple current - not its heating effects.

Ripple Current Multipliers: the Standard Ratings table
list the maximum permissible RMS ripple current at
40 kHz for each rating. These values are based on the
maximum power dissipation allowed at that frequency.
 This ripple current, will cause heating, which adds to the

This ripple current, will cause heating, which adds to the ambient temperature. The higher ambient temperatures, voltage derating or current derating is required (see "Ripple Current Multipliers" table). Also shown are the multipliers for ripple currents at various frequencies, caused by the frequency dependence of the (ESR) equivalent series resistance. (see "Typical ESR as a Function of Frequency" chart)



| RIPE | PLE CUI | RRE | NT | MU | LTIF | PLIE | RS | VS. | FRE | EQU | ENG | CY, | TEN | IPE | RAT | URI | E Al | ND A | APP | LIE | S PI | EAK | V VO | LTA | IGE |
|--|-----------------------|------|--------|------|------|------|-------|------|------|------|--------|------|------|--------|------|------|------|---------|------|------|------|------|------|------|------------|
| FREQUENCY OF APPLIED RIPPLE CURRENT 120 Hz | | | 800 Hz | | | | 1 kHz | | | | 10 kHz | | | 40 kHz | | | | 100 kHz | | | | | | | |
| | NT STILL MP. IN °C | ≤ 55 | 85 | 105 | 125 | ≤ 55 | 85 | 105 | 125 | ≤ 55 | 85 | 105 | 125 | ≤ 55 | 85 | 105 | 125 | ≤ 55 | 85 | 105 | 125 | ≤ 55 | 85 | 105 | 125 |
| | 100 % | 0.60 | 0.39 | - | - | 0.71 | 0.43 | 1 | 1 | 0.72 | 0.46 | - | 1 | 0.88 | 0.55 | 1 | 1 | 1.0 | 0.63 | 1 | 1 | 1.1 | 0.69 | - | - |
| % of 85 °C | 90 % | 0.60 | 0.46 | - | - | 0.71 | 0.55 | 1 | 1 | 0.72 | 0.55 | - | 1 | 0.88 | 0.67 | 1 | 1 | 1.0 | 0.77 | 1 | ı | 1.1 | 0.85 | - | - |
| rated | 80 % | 0.60 | 0.52 | 0.35 | 1 | 0.71 | 0.62 | 0.42 | ı | 0.72 | 0.62 | 0.42 | 1 | 0.88 | 0.76 | 0.52 | 1 | 1.0 | 0.87 | 0.59 | ı | 1.1 | 0.96 | 0.65 | - |
| peak voltage | 70 % | 0.60 | 0.58 | 0.44 | - | 0.71 | 0.69 | 0.52 | - | 0.72 | 0.70 | 0.52 | - | 0.88 | 0.85 | 0.64 | 1 | 1.0 | 0.97 | 0.73 | 1 | 1.1 | 1.07 | 0.80 | - |
| | 66 2/3 % | 0.60 | 0.60 | 0.46 | 0.27 | 0.71 | 0.71 | 0.55 | 0.32 | 0.72 | 0.72 | 0.55 | 0.32 | 0.88 | 0.88 | 0.68 | 0.40 | 1.0 | 1.0 | 0.77 | 0.45 | 1.1 | 1.1 | 0.85 | 0.50 |



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