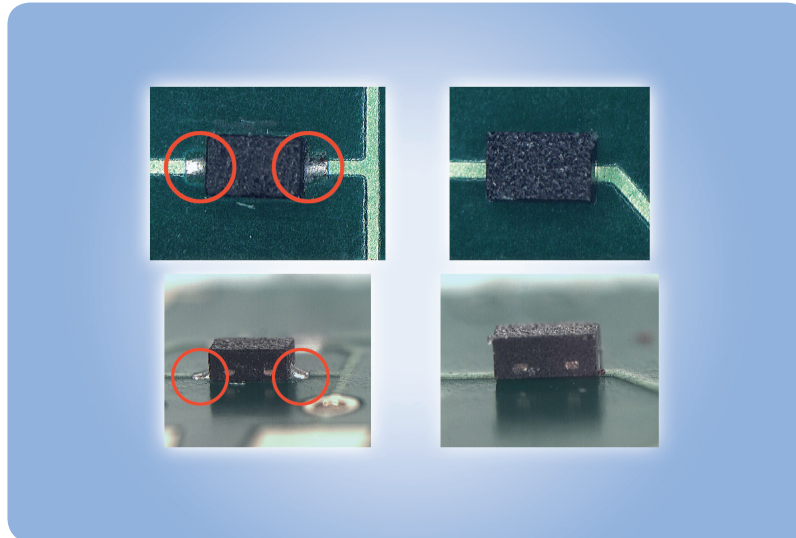




DIODES

Advantages of the LLP Package

LLP Series Eliminates Need for X-Ray Inspection by Allowing Side Soldering, Even for Very Low Profile (0.38 mm) LLP1006 Devices



KEY BENEFITS

- Only visual inspection is needed: side solderability eliminates the need for X-ray inspection
- Enables more cost-effective production
- Compatible with most common footprints; no design-in restrictions
- Wide variety of product types and case sizes
- Offers more space inside of package, allowing larger die area or more chips to be used
 - Hosts up to eight arrayed diodes
- ESD protection diodes and EMI filter devices are available in LLP packages
 - V_{ESD} up to 30 kV
 - C_D of less than 1 pF
- EMI filter portfolio
 - Typical cut-off frequency of 130 MHz
 - Input loss down to 1 dB

RESOURCES

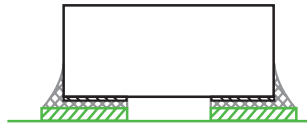
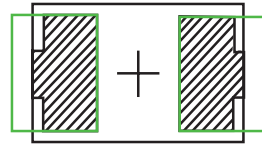
- Datasheet: ESD Protection Diodes - www.vishay.com/diodes/protection-tvs-esd/esd-protection/
- Selector Guide: ESD Protection Diodes and EMI Filters - www.vishay.com/doc?49673
- For technical questions contact ESDProtection@vishay.com
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



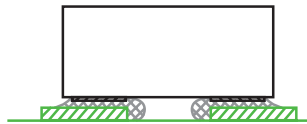
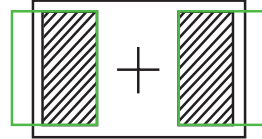
Advantages of the LLP Package

FOOTPRINT **SOLDERING PROFILE**

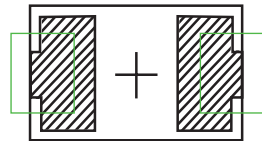
Vishay LLP1006 on Varistor Footprint



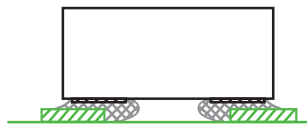
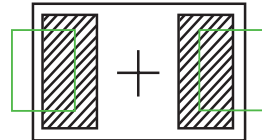
Competitor LLP1006 on Varistor Footprint



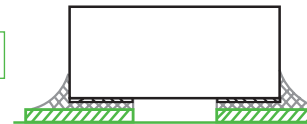
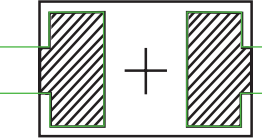
Vishay LLP1006 on Footprint SOD-923



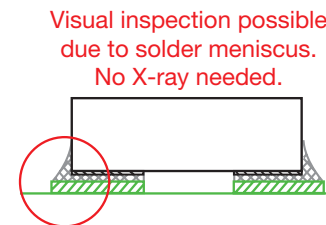
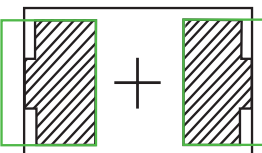
Competitor LLP1006 on Footprint SOD-923



Vishay LLP1006 on Footprint LLP1006



Vishay LLP1006



Competition 1006

