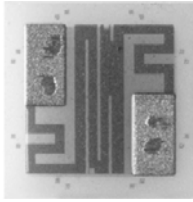


Thin Film 0202 Size Resistor on Alumina



Product may not be to scale

FEATURES

- Wire bondable
- Chip size: 0.020 inches square
- Resistance range: 20 Ω to 20 kΩ
- Alumina substrate
- Low stray capacitance: < 0.2 pF
- Resistor material: Nichrome with passivation coat
- Tolerances to 0.5 %

The CC8 series resistor chips offer a combination of low shunt capacitance, small size and excellent stability. The CC8s are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The CC8s are 100 % electrically tested and visually inspected to MIL-STD-883.

APPLICATIONS

Vishay EFI CC8 chip resistors provide excellent high-frequency response and are ideally suited for prototyping.

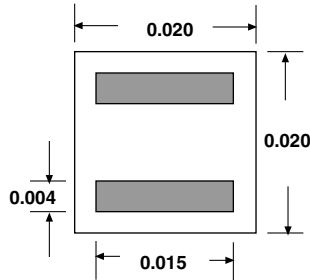
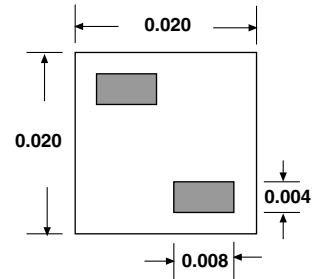
Typical application areas are:

- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

Recommended for hermetic environment where die is not exposed to moisture.

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES AND TOLERANCES											
Tightest Standard Tolerance Available											
0.5 %											
± 25 ppm/°C	<table border="1"> <thead> <tr> <th colspan="2">PROCESS CODE</th> </tr> <tr> <th>CLASS H*</th> <th>CLASS K*</th> </tr> </thead> <tbody> <tr> <td>303</td> <td>308</td> </tr> <tr> <td>302</td> <td>307</td> </tr> <tr> <td>301</td> <td>306</td> </tr> </tbody> </table>	PROCESS CODE		CLASS H*	CLASS K*	303	308	302	307	301	306
PROCESS CODE											
CLASS H*		CLASS K*									
303	308										
302	307										
301	306										
± 50 ppm/°C											
± 100 ppm/°C											
20 Ω 50 Ω 5.1 kΩ 20 kΩ	* MIL-PRF-38534 inspection criteria										

STANDARD ELECTRICAL SPECIFICATIONS	
PARAMETER	
Noise, MIL-STD-202, Method 308	- 20 dB typ.
Moisture Resistance, MIL-STD-202 Method 106 (Passivated Film)	± 0.5 % max. ΔR/R
Stability, 1000 h, + 125 °C, 25 mW	± 0.2 % max. ΔR/R
Operating Temperature Range	- 55 °C to + 125 °C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	± 0.25 % max. ΔR/R
High Temperature Exposure, + 150 °C, 1000 h	± 0.5 % max. ΔR/R
Dielectric Voltage Breakdown	400 V
Insulation Resistance	10 ¹² min.
Operating Voltage	100 V max.
DC Power Rating at + 70 °C (Derated to Zero at + 150 °C)	35 mW max.
5 x Rated Power Short-Time Overload, + 25 °C, 5 s	± 0.25 % max. ΔR/R

DIMENSIONS in inches

TYPICAL RANGE
 20 Ω - 100 Ω

TYPICAL RANGE
 110 Ω - 20 kΩ

SCHEMATIC


MECHANICAL SPECIFICATIONS in inches	
PARAMETER	
Chip Size	0.020 x 0.020 ± 0.003 (0.5 x 0.5 ± 0.08 mm)
Chip Thickness	0.010 ± 0.002 (0.25 ± 0.05 mm)
Chip Substrate Material	99.6 % alumina, 2 - 4 microinch finish
Resistor Material	Nichrome
Bonding Pad Size	0.004 x 0.008 (0.10 x 0.20 mm) minimum
Number of Pads	2
Pad Material	25 kΩ minimum gold standard
Passivation	Thermalset plastic
Backing	None

Options: Gold back for solder die attach
 Contact Applications Engineer

ORDERING INFORMATION					
Example: 100 % visual, 50 Ω, ± 10 %, ± 50 ppm/°C TCR, gold pads, class H visual inspection					
W INSPECTION/ PACKAGING	CC8 PRODUCT FAMILY	302 PROCESS CODE	5000 RESISTANCE VALUE	B MULTIPLIER CODE	K TOLERANCE CODE
W = 100 % visually inspected parts in matrix tray per MIL-STD-883 X = Sample, commercial visually inspected parts loaded in matrix trays (4 % AQL)		See Process Code table	Use first 4 significant digits of resistance	B = 0.01 A = 0.1 0 = 1 1 = 10	D = 0.5 % F = 1.0 % G = 2.0 % H = 2.5 % J = 5.0 % K = 10 % *Coating standard



Disclaimer

All product specifications and data are subject to change without notice.

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