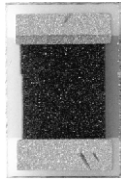


## Thin Film Microwave Resistor



Product may not  
be to scale

The MIF resistor chips on alumina are designed with low shunt capacitance. Resistor geometrics are compatible with strip lines, making them ideally suited for microwave circuits. These chips are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The MIFs are 100 % electrically tested and visually inspected to MIL-STD-883.

### FEATURES

- Wire bondable
- Small single chip size: 0.016 x 0.020 inches
- Microwave resistance range: 20  $\Omega$  to 100  $\Omega$
- Overall resistance range: 20  $\Omega$  to 2 k $\Omega$
- Alumina substrate
- Low stray capacitance: < 0.2 pF
- Resistor material: Tantalum nitride, self passivating
- Moisture resistant
- Power: 50 mW
- High frequency

### APPLICATIONS

Vishay EFI MIF chip resistors provide excellent high frequency response and are ideally suited for prototyping. Typical application areas are:

- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

### TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES AND TOLERANCES

Resistance Range	20 $\Omega$ to 100 $\Omega$
Tolerance	$\pm 1\%$ , $\pm 5\%$ , $\pm 10\%$ , $\pm 20\%$ standard
TCR	$\pm 100$ ppm/ $^{\circ}$ C

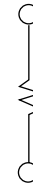
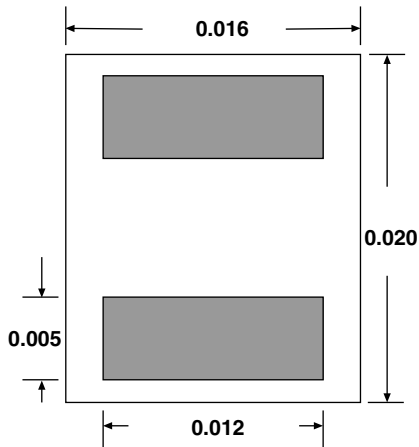
### STANDARD ELECTRICAL SPECIFICATIONS

PARAMETER	
Noise, MIL-STD-202, Method 308	- 20 dB typ.
Moisture Resistance, MIL-STD-202, Method 106	$\pm 0.5\%$ max. $\Delta R/R$
Stability, 1000 h, + 125 $^{\circ}$ C, 25 mW	$\pm 0.5\%$ max. $\Delta R/R$
Operating Temperature Range	- 55 $^{\circ}$ C to + 125 $^{\circ}$ C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	$\pm 0.25\%$ max. $\Delta R/R$
High Temperature Exposure, + 150 $^{\circ}$ C, 1000 h	$\pm 0.5\%$ max. $\Delta R/R$
Dielectric Voltage Breakdown	400 V
Insulation Resistance	$10^{12}$ min.
Operating Voltage	100 V max.
DC Power Rating at + 70 $^{\circ}$ C (Derated to Zero at 150 $^{\circ}$ C)	50 mW max.
5 x Rated Power Short-Time Overload, + 25 $^{\circ}$ C, 5 s	$\pm 0.25\%$ max. $\Delta R/R$



**DIMENSIONS** in inches

**SCHEMATIC**



MICROWAVE RESISTORS

<b>MECHANICAL SPECIFICATIONS</b> in inches	
PARAMETER	
Chip Size	0.016 x 0.020 ± 0.003 (0.40 x 0.5 ± 0.076 mm)
Chip Thickness	0.010 ± 0.001 (0.25 ± 0.025 mm)
Chip Substrate Material	99.6 % alumina, 2 - 4 microinch finish
Resistor Material	Tantalum nitride, self passivating
Bonding Pad Size	0.005 x 0.012 (0.125 x 0.30 mm)
Number of Pads	2
Pad Material	25 kÅ minimum gold standard
Backing	None

**Options:** Terminations: Aluminum/solder  
 Gold back for solder die attach  
 5 mil chip thickness  
 Contact Applications Engineer

<b>ORDERING INFORMATION</b>					
Example: 100 % visualled, 50 Ω, ± 10 %, ± 100 ppm/°C TCR, gold pads, class H visual inspection					
<b>W</b>	<b>MIF</b>	<b>002</b>	<b>5000</b>	<b>B</b>	<b>K</b>
INSPECTION/ PACKAGING	PRODUCT FAMILY	PROCESS CODE	RESISTANCE VALUE	MULTIPLIER CODE	TOLERANCE CODE
W = 100 % visually inspected parts in matrix trays per MIL-STD-883			Use first 4 digits significant digits of the resistance	<b>B</b> = 0.01 <b>A</b> = 0.1 <b>0</b> = 1	<b>F</b> = 1.0 % <b>G</b> = 2.0 % <b>H</b> = 2.5 % <b>J</b> = 5.0 % <b>K</b> = 10 % <b>M</b> = 20 %
X = Sample, visually inspected parts loaded in matrix trays (4 % AQL)					



## Disclaimer

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