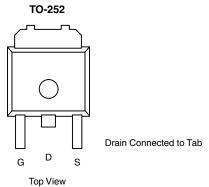


Vishay Siliconix

## N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$ Max.	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)	
100	0.066 at V <sub>GS</sub> = 10 V	18.2	19.8	
100	0.080 at V <sub>GS</sub> = 4.5 V	13.2	19.0	



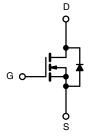
#### **FEATURES**

- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



#### **APPLICATIONS**

- DC/DC Converters
- DC/AC Inverters
- **Motor Drives**



N-Channel MOSFET

#### **Ordering Information:**

SUD20N10-66L-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
0 5 . 0	T <sub>C</sub> = 25 °C	,	16.9	
Continuous Drain Current	T <sub>C</sub> = 70 °C	I <sub>D</sub>	13.6	Ī ,
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	25	_ A
Avalanche Current		I <sub>AS</sub>	15	
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	11.25	mJ
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	В	41.7 <sup>b</sup>	w
	T <sub>A</sub> = 25 °C <sup>c</sup>	P <sub>D</sub>	2.1	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	60	°C/W	
Junction-to-Case (Drain)	$R_{thJC}$	3	- C/VV	

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Base on  $T_C$  = 25 °C.

## SUD20N10-66L

## Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	Symbol	rest conditions	IVIIII.	тур.	IVIAA.	Oilit
		V 0.V.I 050A	100			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	100		3	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1.2		_	A
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA
7 0		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ
0. 0. 1. 5		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 6.6 \text{ A}$		0.055	0.066	Ω
	20(011)	$V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$		0.066	0.080	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 6.6 \text{ A}$		25		S
Dynamic <sup>b</sup>					1	
Input Capacitance	C <sub>iss</sub>			860		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		85		
Reverse Transfer Capacitance	C <sub>rss</sub>			40		
Total Gate Charge <sup>c</sup>	$Q_g$			19.8	30	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6.6 \text{ A}$		3.6		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			4.1		
Gate Resistance	$R_{g}$	f = 1 MHz	0.4	2	4	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			8	16	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 9.6 \Omega$		11	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 5.2 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		18	27	
Fall Time <sup>c</sup>	t <sub>f</sub>			5	10	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			38	57	ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V, R}_1 = 9.6 \Omega$		58	87	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 5.2 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		18	27	
Fall Time <sup>c</sup>	t <sub>f</sub>			8	16	
Drain-Source Body Diode Ratings a	nd Characteri	stics <sup>b</sup> T <sub>C</sub> = 25 °C				
Continuous Current	Is				16.9	
Pulsed Current	I <sub>SM</sub>				25	Α
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 5.2 A, V <sub>GS</sub> = 0 V		0.8	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			34	51	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 5.2 A, dl/dt = 100 A/μs		3	5	Α
Reverse Recovery Charge	Q <sub>rr</sub>			50	75	nC

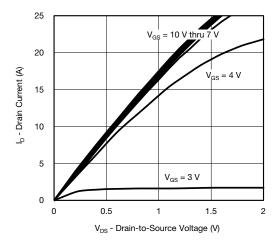
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

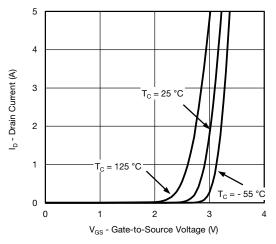
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



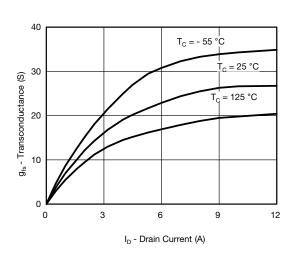
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



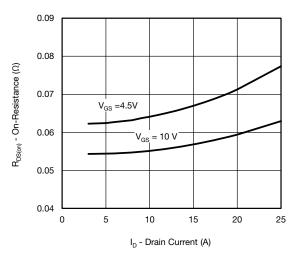
#### **Output Characteristics**



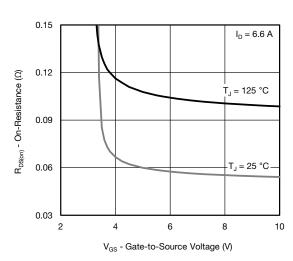
#### **Transfer Characteristics**



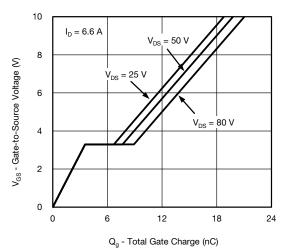
**Transconductance** 



On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage

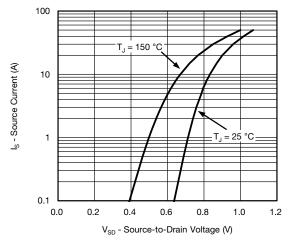


**Gate Charge** 

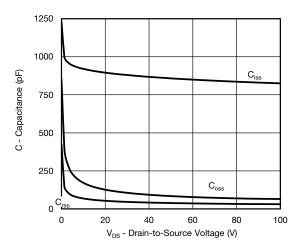
## Vishay Siliconix

# VISHAY.

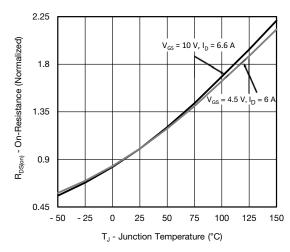
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



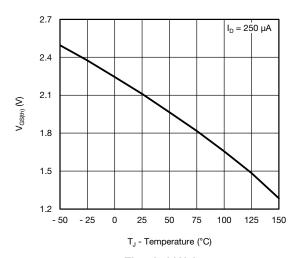
#### Source-Drain Diode Forward Voltage



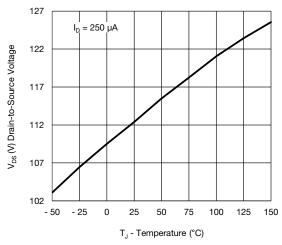
#### Capacitance



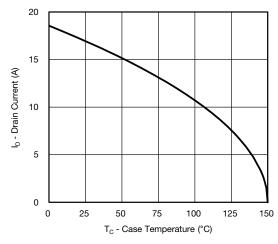
On-Resistance vs. Junction Temperature



#### **Threshold Voltage**



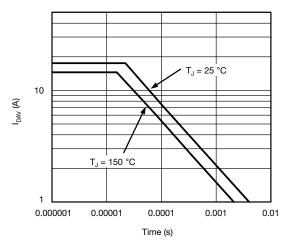
#### Drain Source Breakdown vs. Junction Temperature

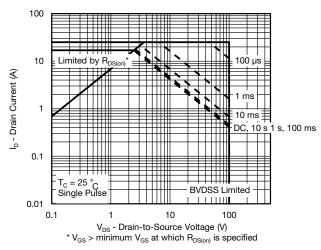


**Current Derating** 



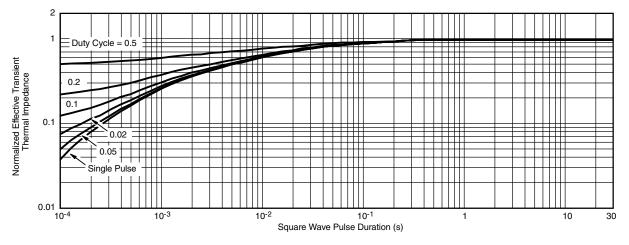
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time





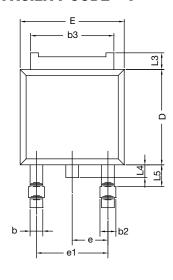
Normalized Thermal Transient Impedance, Junction-to-Case

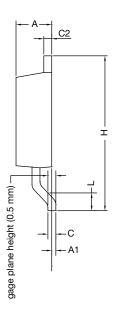
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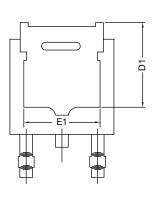


## **TO-252AA Case Outline**

#### **VERSION 1: FACILITY CODE = Y**







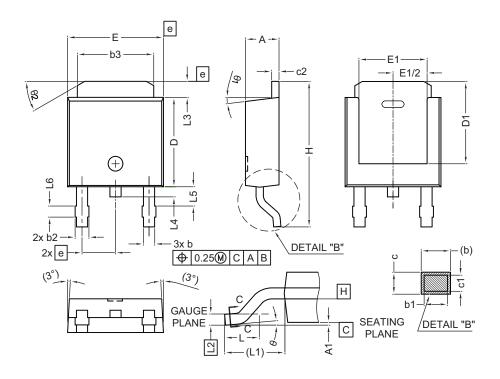
	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	2.18 2.38		
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
E	6.35	6.73	
E1	4.32	=	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

#### Note

• Dimension L3 is for reference only



#### **VERSION 2: FACILITY CODE = N**



	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
Е	6.35	6.73	
E1	4.32	-	
е	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74 ref.		
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

#### Notes

- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022

DWG: 5347



### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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