## SiUD402ED

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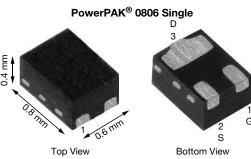
**Vishay Siliconix** 

RoHS

COMPLIANT

HALOGEN FREE

# N-Channel 20 V (D-S) MOSFET



Marking Code: C

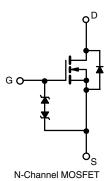
PRODUCT SUMMARY						
V <sub>DS</sub> (V)	20					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 4.5 V	0.73					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 2.5 V	0.87					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 1.8 V	1.10					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 1.5 V	1.80					
Q <sub>g</sub> typ. (nC)	0.5					
I <sub>D</sub> (A) <sup>a</sup>	1					
Configuration	Single					

### **FEATURES**

- TrenchFET<sup>®</sup> power MOSFET
- Ultra small 0.8 mm x 0.6 mm outline
- Ultra thin 0.4 mm max. height
- 100 % R<sub>q</sub> tested
- Typical ESD protection 2000 V (HBM)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **APPLICATIONS**

- Load switch
- · High speed switching
- DC/DC converters
- · For smart phones, tablet PCs and mobile computing
- Small signal switching



ORDERING INFORMATION

Package	PowerPAK 0806
Lead (Pb)-free and halogen-free	SiUD402ED-T1-GE3

#### Note

The lead finish is NiPdAu and classed as E4 finish

Parameter		Symbol	Limit	Unit	
Drain-source voltage		V <sub>DS</sub>	20	V	
Gate-source voltage		V <sub>GS</sub>	± 8	V	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C		<b>1</b> a		
	T <sub>A</sub> = 70 °C		0.8 ª		
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	0.35 <sup>b</sup>		
	T <sub>A</sub> = 70 °C	1	0.28 <sup>b</sup>	A	
Pulsed drain current (t = 100 µs)		I <sub>DM</sub>	1.4		
	T <sub>A</sub> = 25 °C		1 <sup>a</sup>		
Continuous source-drain diode current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	0.37 <sup>b</sup>		
	T <sub>A</sub> = 25 °C		1.25 <sup>a</sup>		
Maximum power dissipation	T <sub>A</sub> = 70 °C		0.8 <sup>a</sup>	W	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.37 <sup>b</sup>		
	T <sub>A</sub> = 70 °C	1	0.24 <sup>b</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub> -55 to +150		°C	
Soldering recommendations (peak temperature) <sup>c</sup>			260		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum junction-to-ambient a, d	t≤5s	P	80	100	°C/W	
Maximum junction-to-ambient b, e	1235	≤5s R <sub>thJA</sub>	265	335	0/11	

#### Notes

a. Surface mounted on 1" x 1" FR4 board with full copper, t = 5 s

a. Surface mounted on 1" x 1" FR4 board with minimum copper, t = 5 s
b. Surface mounted on 1" x 1" FR4 board with minimum copper, t = 5 s
c. Refer to IPC/JEDEC<sup>®</sup> (J-STD-020), no manual or hand soldering
d. Maximum under steady state conditions is 135 °C/W

Maximum under steady state conditions is 400 °C/W e.

S20-0847-Rev. B, 26-Oct-2020

1 For technical questions, contact: pmostechsupport@vishay.com

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 250 \mu A$	20	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	L 050 A	-	18	-	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-1.9	-	mV/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.4	-	0.9	V
	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$	-	-	± 0.5	-μΑ
Gate-source leakage		$V_{DS} = 0 V, V_{GS} = \pm 8 V$	-	-	± 10	
Zero gate voltage drain current		$V_{DS} = 20 V, V_{GS} = 0 V$	-	-	1	
	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	10	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	1	-	-	Α
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.2 A	-	0.57	0.73	
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 0.1 A	-	0.67	0.87	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 0.02 A	-	0.80	1.10	Ω
		V <sub>GS</sub> = 1.5 V, I <sub>D</sub> = 0.01 A	-	0.90	1.80	_
Forward transconductance a	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.2 \text{ A}$	-	1.2	-	S
Dynamic <sup>b</sup>				1	1	
Input capacitance	C <sub>iss</sub>		-	16	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	7.5	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	3.5	-	
	Qg	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 8 V, I <sub>D</sub> = 0.2 A	-	0.75	1.20	
Total gate charge			-	0.50	0.75	nC
Gate-source charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.2 \text{ A}$	-	0.09	-	
Gate-drain charge	Q <sub>gd</sub>		-	0.09	-	
Gate resistance	R <sub>q</sub>	f = 1 MHz	3	24	50	Ω
Turn-on delay time	t <sub>d(on)</sub>		-	7	15	1
Rise time	t <sub>r</sub>	f = 1 MHz V <sub>DD</sub> = 10 V, R <sub>L</sub> = 50 Ω		10	20	_
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 0.2 \text{ A}, V_{\text{GEN}} = 4.5 \text{ V}, \text{Rg} = 1 \Omega$	-	23	50	
Fall time	t <sub>f</sub>		-	7	15	-
Turn-on delay time	t <sub>d(on)</sub>		-	5	10	ns
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}. \text{ R}_{1} = 15 \Omega$	-	5	10	-
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 0.2 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$	-	11	25	
Fall time	t <sub>f</sub>		-	5	10	
Drain-Source Body Diode Characteristic	1					
Continuous source-drain diode current	IS	T <sub>C</sub> = 25 °C	-	-	1 °	
Pulse diode forward current	I <sub>SM</sub>	-	-	-	1.4	A
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 0.2 A, V <sub>GS</sub> = 0 V	-	0.8	1.2	V
Body diode reverse recovery time	t <sub>rr</sub>		-	11	25	ns
Body diode reverse recovery charge	Q <sub>rr</sub>		-	3.5	7	nC
Reverse recovery fall time	t <sub>a</sub>	$I_F = 0.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{T}_J = 25 ^\circ\text{C}$	-	5.3	-	
Reverse recovery rise time	t <sub>b</sub>		_	5.7	-	ns

#### Note

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing

c. Surface mounted on 1" x 1" FR4 board with full copper, t = 5 s

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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T<sub>.1</sub> = 25 °C

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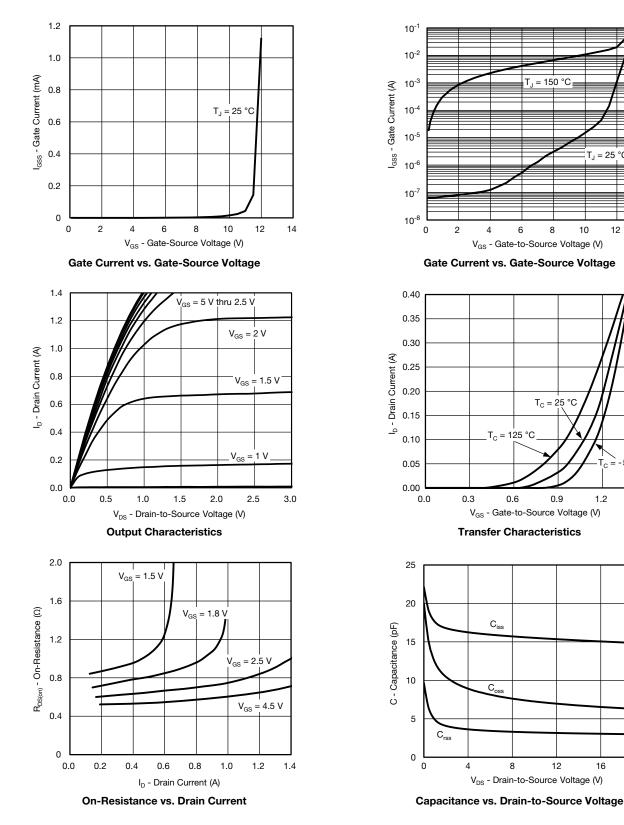
-55 °C

1.5

1.2

10

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

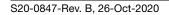


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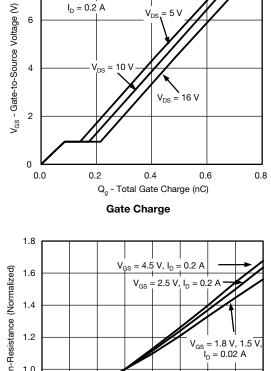
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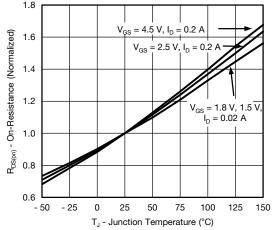
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

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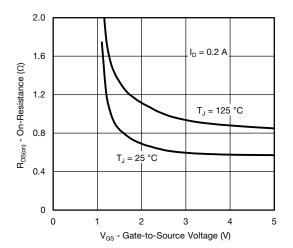
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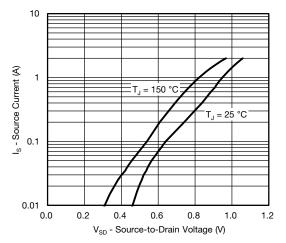




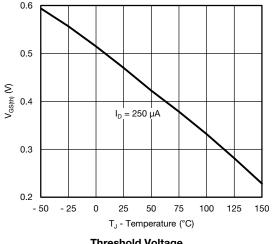
**On-Resistance vs. Junction Temperature** 



**On-Resistance vs. Gate-to-Source Voltage** 



Source-Drain Diode Forward Voltage





Document Number: 62968

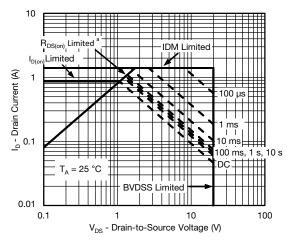
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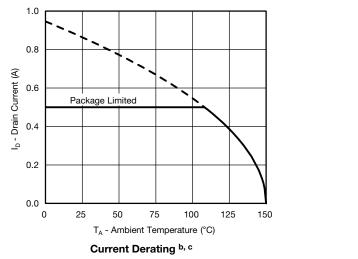


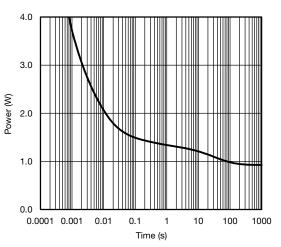
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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

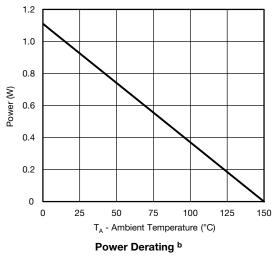


Safe Operating Area (Junction-to-Ambient) b





Single Pulse Power, Junction-to-Ambient b

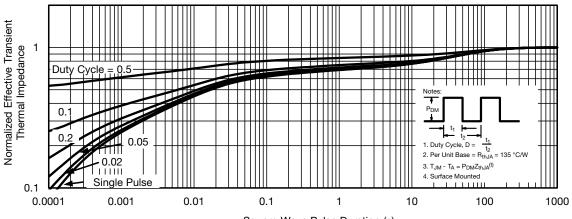


#### Note

- a.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified
- b. When mounted on 1" x 1" FR4 with full copper
- c. The power dissipation  $P_D$  is based on  $T_J$  (max.) = 150 °C, using junction-to-ambient thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

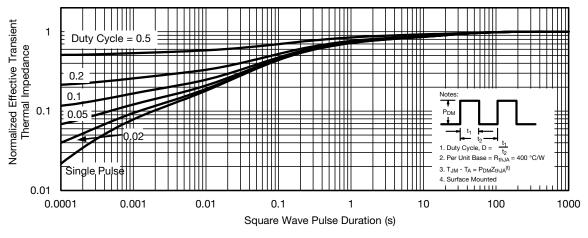


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Ambient <sup>a</sup>



Normalized Thermal Transient Impedance, Junction-to-Ambient <sup>a</sup>

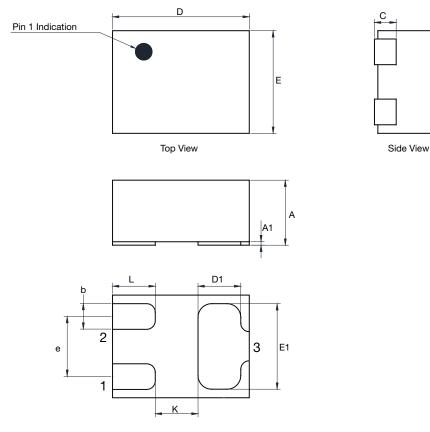
Note

a. When mounted on 1" x 1" FR4 with minimum copper

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## Case Outline for PowerPAK 0.8 mm x 0.6 mm



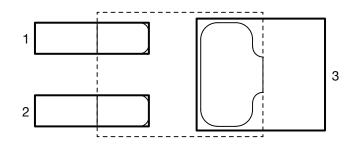
Bottom View

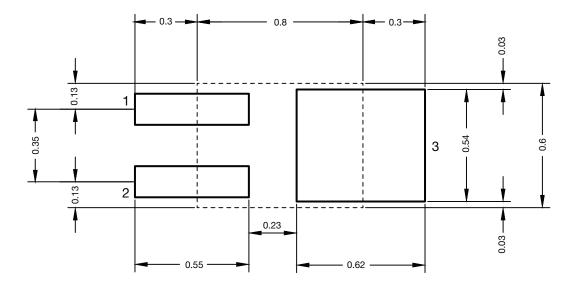
	MILLIMETERS					
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
А	0.350	0.380	0.400	0.0138	0.0150	0.0157
A1	0	-	0.020	0	-	0.0008
b	0.120	0.150	0.180	0.0047	0.0059	0.0071
С	0.119	0.127	0.135	0.0047	0.0050	0.0053
D	0.750	0.800	0.850	0.0295	0.0315	0.0335
D1	0.200	0.250	0.300	0.0078	0.0098	0.0118
E	0.550	0.600	0.650	0.0217	0.0236	0.0256
E1	0.450	0.500	0.550	0.0177	0.0197	0.0217
е	0.300	0.350	0.400	0.0118	0.0138	0.0158
К	0.150	0.250	0.350	0.0058	0.0098	0.0138
L	0.200	0.250	0.300	0.0078	0.0098	0.0118
ECN: C13-1574-R DWG: 6020	ev. A, 23-Dec-13	•			•	·





## **Recommended Land Pattern PowerPAK® 0806**







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