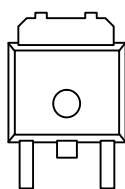


P-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω) Max.	I_D (A)	Q_g (Typ.)
- 40	0.0162 at $V_{GS} = - 10$ V	- 36	67
	0.0230 at $V_{GS} = - 4.5$ V	- 24	

TO-252



G D S

Top View

Drain Connected to Tab

Ordering Information:

SUD45P04-16P-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

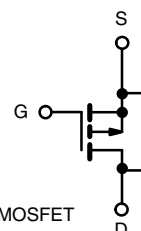
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Power Switch
- Load Switch in High Current Applications
- DC/DC Converters



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	$T_C = 25$ °C	A
		$T_C = 70$ °C	
Pulsed Drain Current ($t = 300$ μ s)	I_{DM}	- 100	
Avalanche Current	I_{AS}	- 32	
Single Avalanche Energy ^a	E_{AS}	51	mJ
Maximum Power Dissipation ^a	P_D	$T_C = 25$ °C	W
		$T_A = 25$ °C ^c	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	60	°C/W
Junction-to-Case (Drain)	R_{thJC}	3	

Notes:

a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

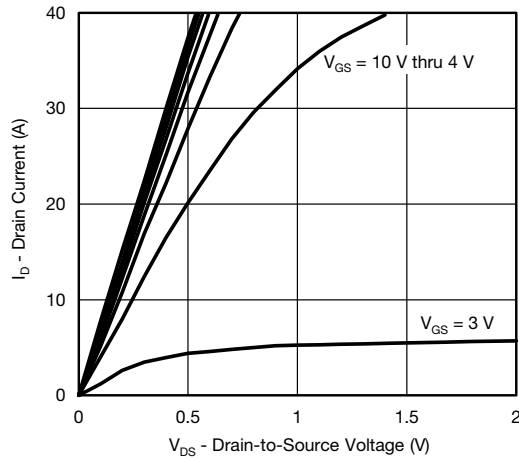
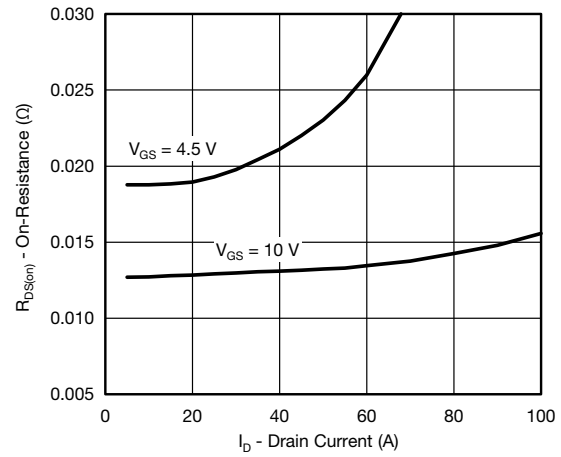
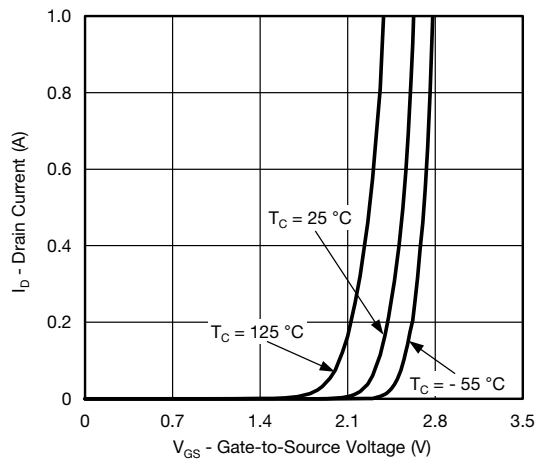
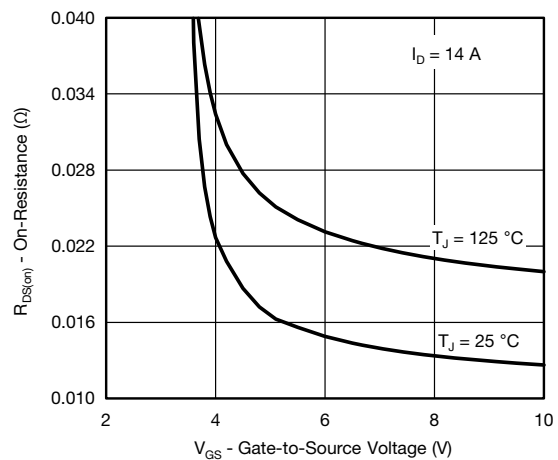
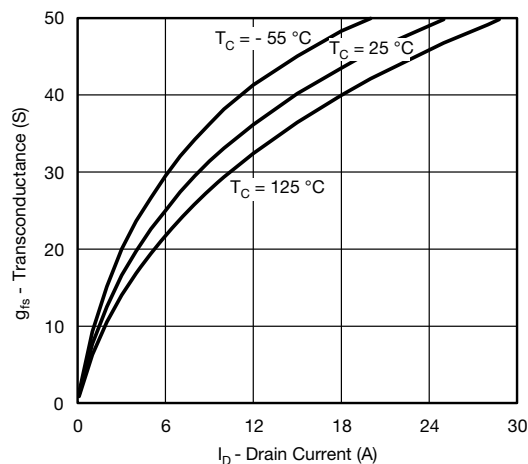
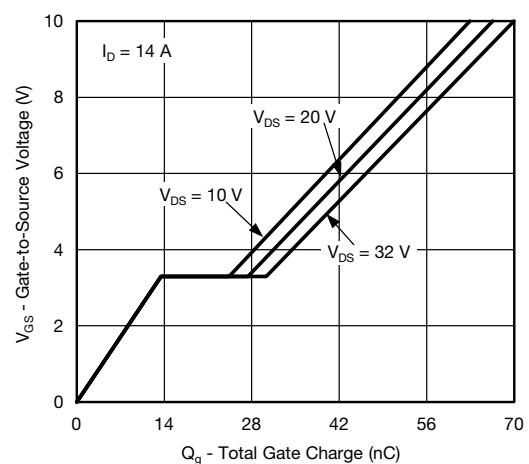
c. When mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{DS} = 0 V, I _D = - 250 μA	- 40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1		- 2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 40 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 125 °C			- 50	
		V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 150 °C			- 250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ - 10 V, V _{GS} = - 10 V	- 50			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 14 A		0.0135	0.0162	Ω
		V _{GS} = - 4.5 V, I _D = - 12 A		0.0190	0.0230	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 20 V, I _D = - 14 A		40		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 20 V, f = 1 MHz		2765		pF
Output Capacitance	C _{oss}			330		
Reverse Transfer Capacitance	C _{rss}			280		
Total Gate Charge ^c	Q _g	V _{DS} = - 20 V, V _{GS} = - 10 V, I _D = - 14 A		67	100	nC
Gate-Source Charge ^c	Q _{gs}			13.5		
Gate-Drain Charge ^c	Q _{gd}			14		
Gate Resistance	R _g	f = 1 MHz	0.5	2.5	5	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = - 20 V, R _L = 2 Ω I _D ≅ - 10 A, V _{GEN} = - 10 V, R _g = 1 Ω		10	20	ns
Rise Time ^c	t _r			11	20	
Turn-Off Delay Time ^c	t _{d(off)}			42	63	
Fall Time ^c	t _f			12	20	
Drain-Source Body Diode Ratings and Characteristics T _C = 25 °C ^b						
Continuous Current	I _S				- 36	A
Pulsed Current	I _{SM}				- 100	
Forward Voltage ^a	V _{SD}	I _F = - 10 A, V _{GS} = 0 V		- 0.8	- 1.5	V
Reverse Recovery Time	t _{rr}	I _F = - 10 A, dI/dt = 100 A/μs		38	57	ns
Peak Reverse Recovery Current	I _{RM(REC)}			2.3	3.5	A
Reverse Recovery Charge	Q _{rr}			40	60	nC

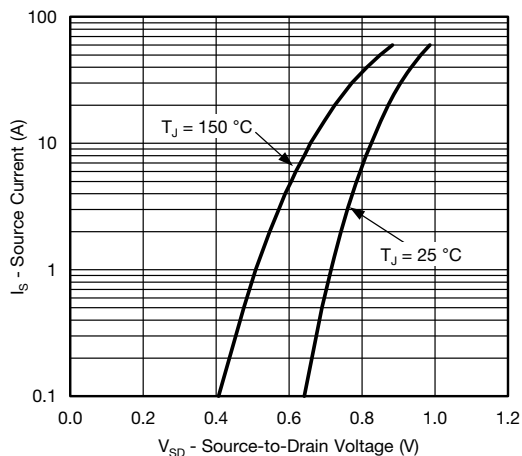
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

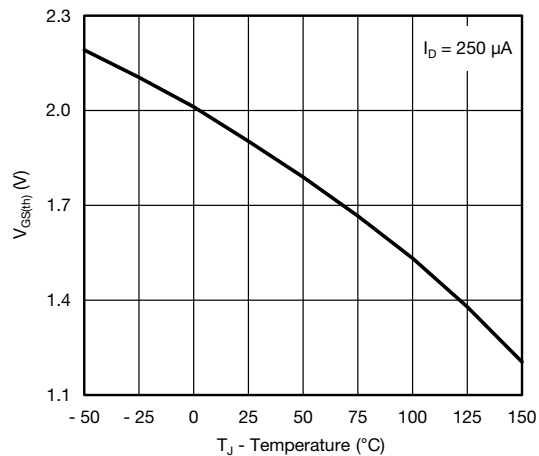
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Output Characteristics

On-Resistance vs. Drain Current

Transfer Characteristics

On-Resistance vs. Gate-to-Source Voltage

Transconductance

Gate Charge

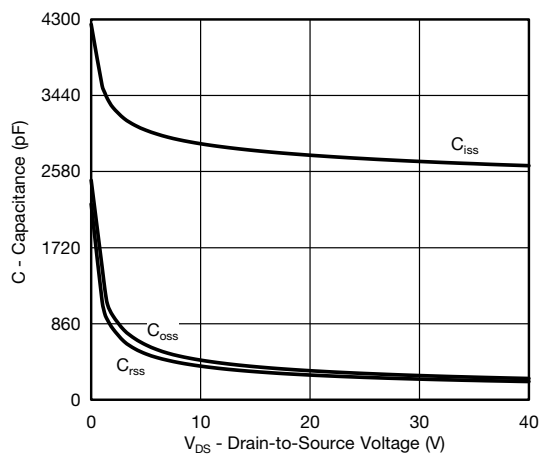
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



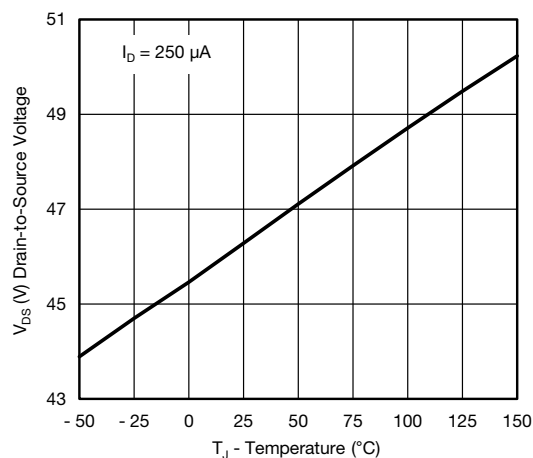
Source-Drain Diode Forward Voltage



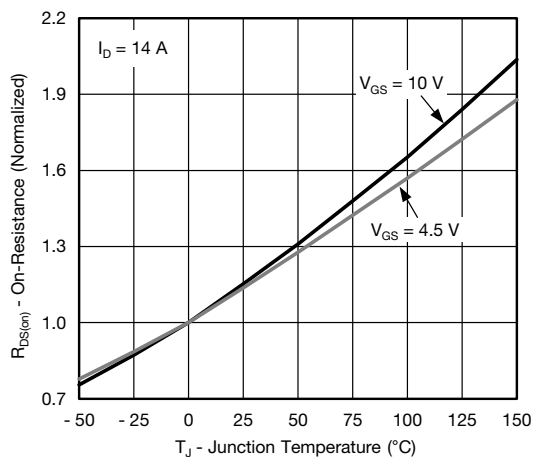
Threshold Voltage



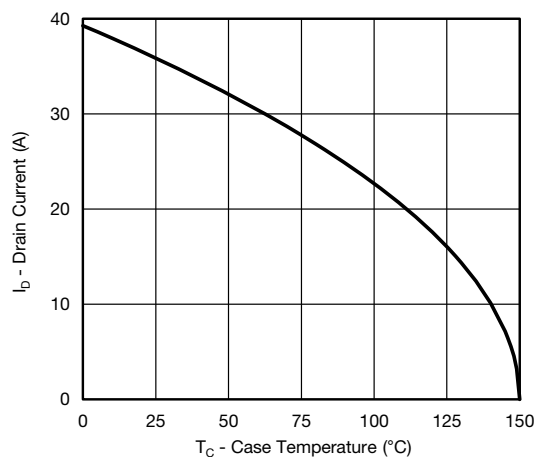
Capacitance



Drain Source Breakdown vs. Junction Temperature

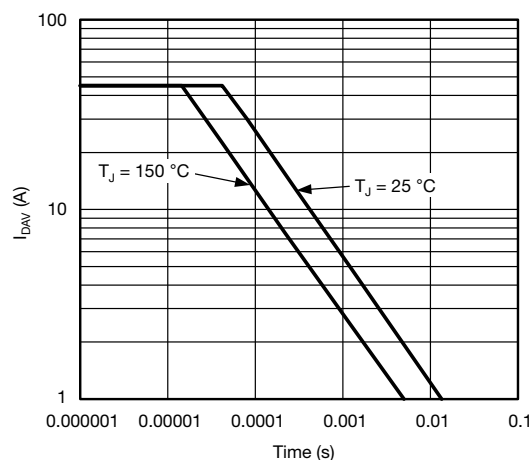


On-Resistance vs. Junction Temperature

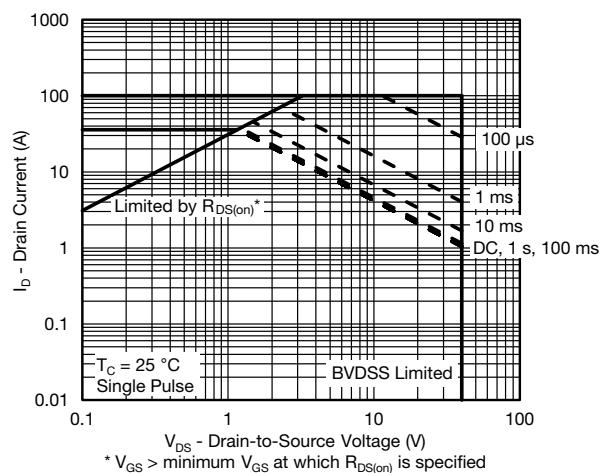


Current Derating

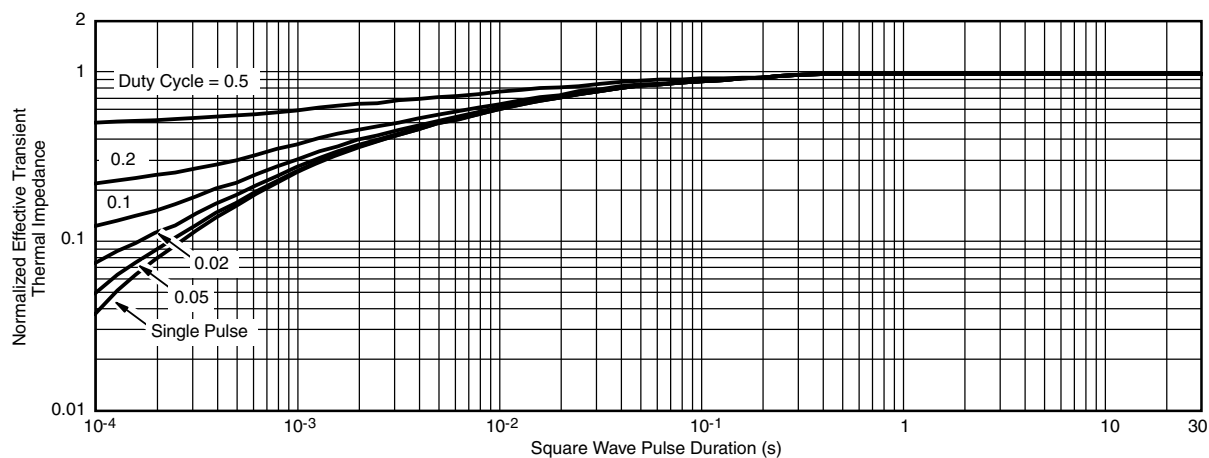
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area

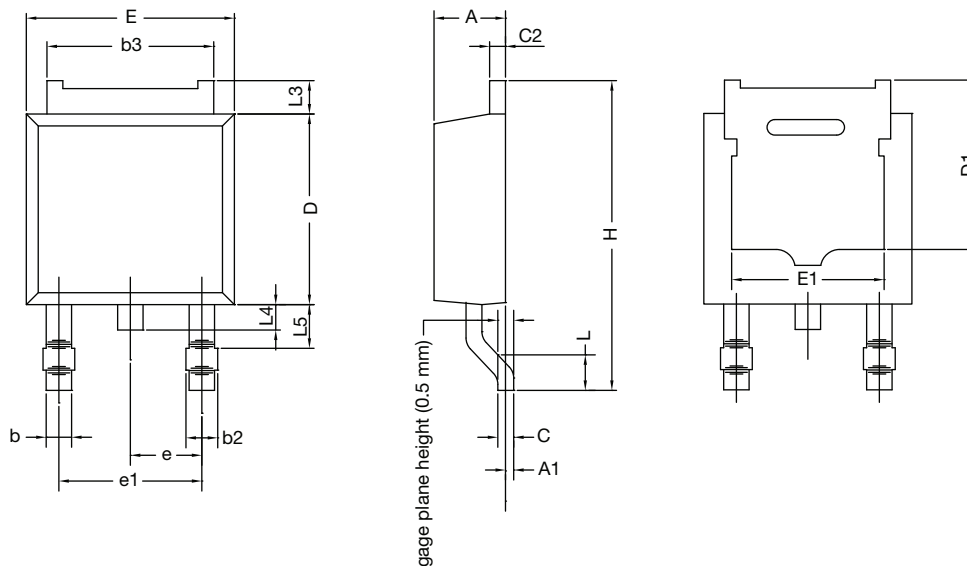


Normalized Thermal Transient Impedance, Junction-to-Case

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TO-252AA Case Outline

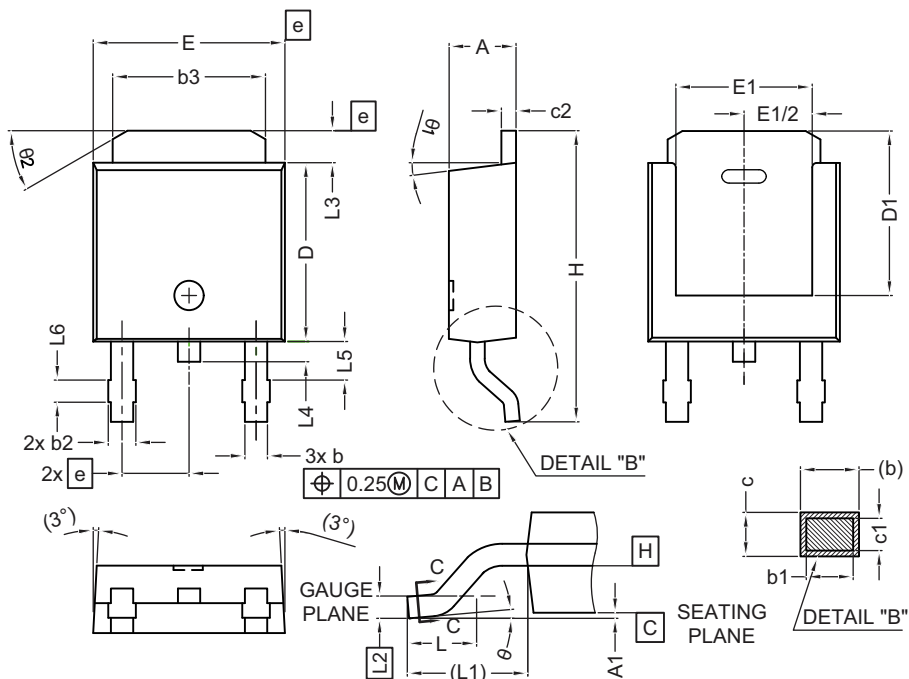
VERSION 1: FACILITY CODE = Y



MILLIMETERS		
DIM.	MIN.	MAX.
A	2.18	2.38
A1	-	0.127
b	0.64	0.88
b2	0.76	1.14
b3	4.95	5.46
C	0.46	0.61
C2	0.46	0.89
D	5.97	6.22
D1	4.10	-
E	6.35	6.73
E1	4.32	-
H	9.40	10.41
e	2.28 BSC	
e1	4.56 BSC	
L	1.40	1.78
L3	0.89	1.27
L4	-	1.02
L5	1.01	1.52

Note

- Dimension L3 is for reference only


VERSION 2: FACILITY CODE = N


	MILLIMETERS	
DIM.	MIN.	MAX.
A	2.18	2.39
A1	-	0.13
b	0.65	0.89
b1	0.64	0.79
b2	0.76	1.13
b3	4.95	5.46
c	0.46	0.61
c1	0.41	0.56
c2	0.46	0.60
D	5.97	6.22
D1	5.21	-
E	6.35	6.73
E1	4.32	-
e	2.29 BSC	
H	9.94	10.34

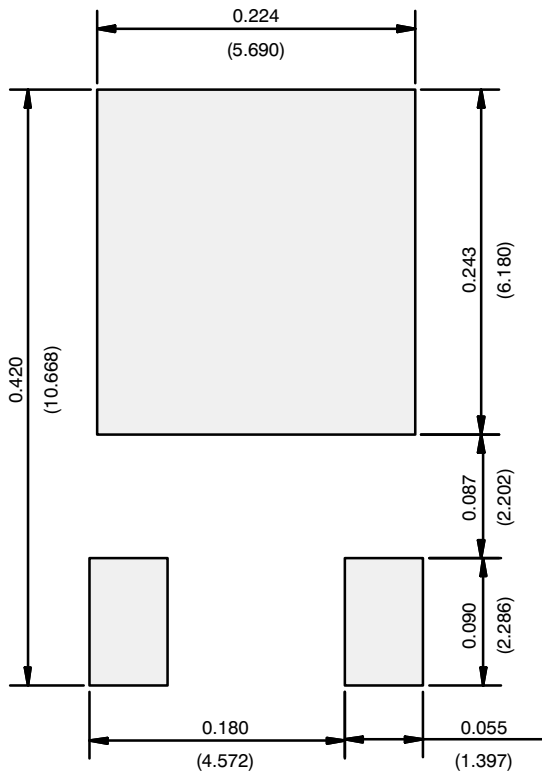
	MILLIMETERS	
DIM.	MIN.	MAX.
L	1.50	1.78
L1	2.74 ref.	
L2	0.51 BSC	
L3	0.89	1.27
L4	-	1.02
L5	1.14	1.49
L6	0.65	0.85
theta	0°	10°
theta1	0°	15°
theta2	25°	35°

Notes

- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022
DWG: 5347

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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