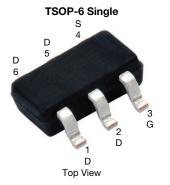
# Si3127DV



**Vishay Siliconix** 

# P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (Ω) MAX.</b>	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (TYP.)			
-60	0.089 at V <sub>GS</sub> = -10 V	-5.1	10.1 nC			
-00	0.146 at V <sub>GS</sub> = -4.5 V	-4	10.1110			



#### FEATURES

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> and UIS tested
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- Load switches
- DC/DC converter



P-Channel MOSFET

Marking Code: BL

#### Ordering Information:

Si3127DV-T1-GE3 (lead (Pb)-free and halogen-free)

PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	-60		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
	T <sub>C</sub> = 25 °C		-5.1	
	T <sub>C</sub> = 70 °C	1 . 🗖	-4.1	
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-3.5 <sup>a,b</sup>	
	T <sub>A</sub> = 70 °C	1	-2.8 <sup>a,b</sup>	
Pulsed Drain Current (t = 100 µs)	I <sub>DM</sub>	-20	— A	
Continuous Course Durin Dia da Courset	T <sub>C</sub> = 25 °C		-3.5	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-1.7 <sup>a,b</sup>	
Avalanche Current		I <sub>AS</sub>	-15	
Single-Pulse Avalanche Energy L = 0.1 mH		E <sub>AS</sub>	11.25	mJ
	T <sub>C</sub> = 25 °C		4.2	
Maximum Davies Dissis ation	T <sub>C</sub> = 70 °C		2.7	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2 <sup>a,b</sup>	
	T <sub>A</sub> = 70 °C	1	1.3 <sup>a,b</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient a,c	t ≤ 10 s	R <sub>thJA</sub>	40	62.5	°C/W	
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	25	30	0/00	

#### Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 110 °C/W.
- d. Based on  $T_C = 25$  °C.

Si3127DV

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			1	<u> </u>	1	<b>I</b>	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	-60	-	-	V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_J$			-6.7	-	m\//º(	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I <sub>D</sub> = -250 μΑ	-	4.3	-	mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	-1	-	-3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA	
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	1		<u> </u>		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$	-	-	-5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	-30	-	-	А	
		V <sub>GS</sub> = -10 V, I <sub>D</sub> = -3.5 A	-	0.074	0.089		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2.8 A	-	- 0.095 0.146		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$	-	11	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		-	832	-		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	88	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	63	-		
Total Gate Charge	Qg	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$	-	20	30	1	
			-	10.1	15.2		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -3.5 \text{ A}$		3.3	-	- nC	
Gate-Drain Charge	Q <sub>gd</sub>			3.9	-		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.8	9	18	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>		-	8	16		
Rise Time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{L}} = 10.7 \Omega$	-	6	12		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ -2.8 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$	-	35	53		
Fall Time	t <sub>f</sub>		-	16	24		
Turn-On Delay Time	t <sub>d(on)</sub>		-	40	60	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{L}} = 10.7 \Omega$	-	28	42	-	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ -2.8 A, $V_{GEN}$ = -4.5 V, $R_g$ = 1 $\Omega$	-	31	47		
Fall Time	t <sub>f</sub>		-	15	23		
Drain-Source Body Diode Characterist	ics	· · · · · · · · · · · · · · · · · · ·					
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-3.5	_	
Pulse Diode Forward Current (t = $100 \mu s$ )	I <sub>SM</sub>			-	-20	A	
Body Diode Voltage	V <sub>SD</sub>	$I_{\rm S}$ = -2.8 A, $V_{\rm GS}$ = 0 V	-	-0.85	-1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	32	48	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = -2.8 A, dl/dt = 100 A/μs,	-	45	68	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$T_J = 25 \ ^{\circ}C$	-	24	-		
Reverse Recovery Rise Time	t <sub>b</sub>	1	-	8	-	ns	

#### Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

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Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

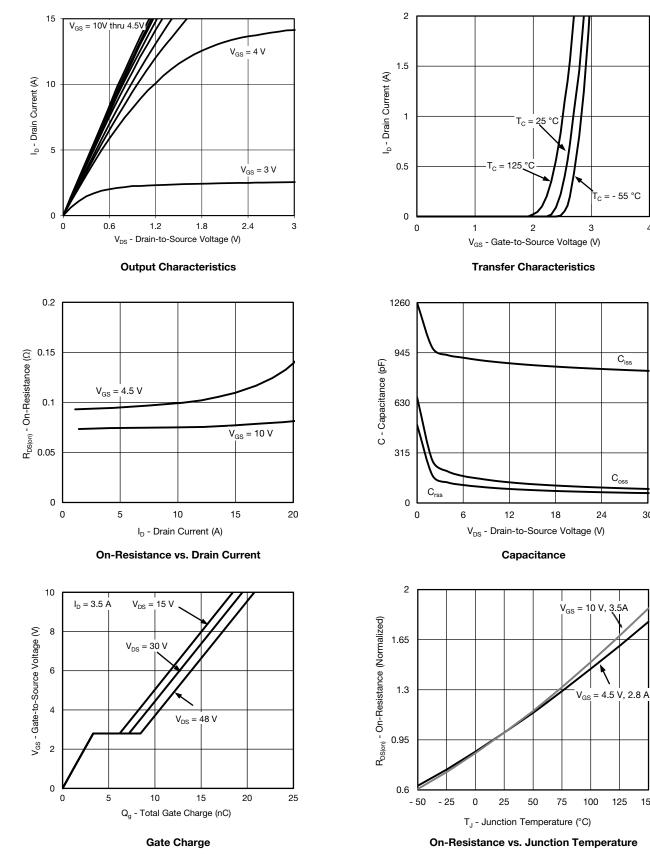
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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



S14-0767-Rev. A, 14-Apr-14

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Document Number: 64282

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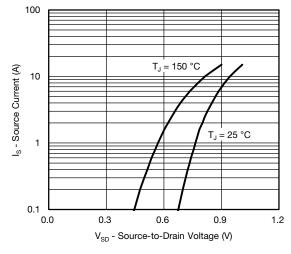
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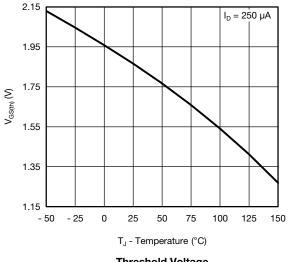


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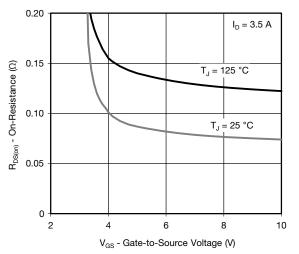
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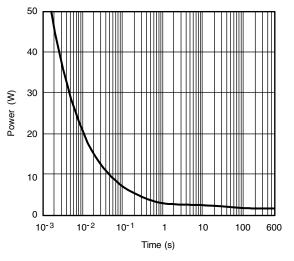
Source-Drain Diode Forward Voltage



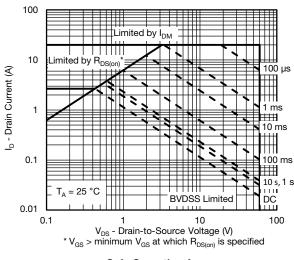
**Threshold Voltage** 



**On-Resistance vs. Gate-to-Source Voltage** 



Single Pulse Power, Junction-to-Ambient



Safe Operating Area

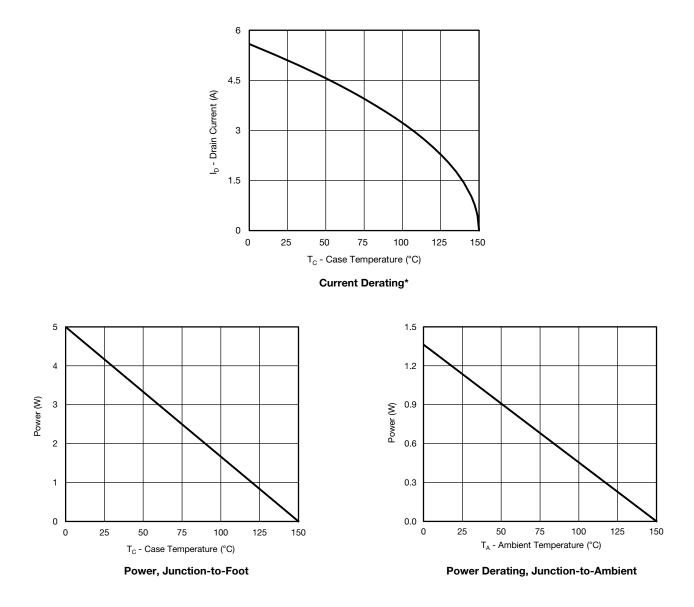
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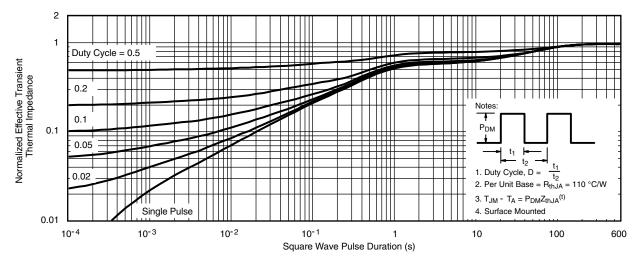
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



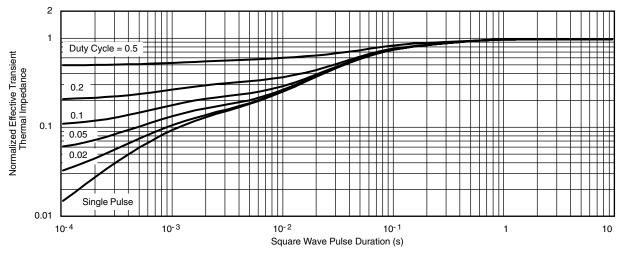
\* The power dissipation  $P_D$  is based on  $T_{J (max.)} = 150 \text{ °C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?64282">www.vishay.com/ppg?64282</a>.



Package Information

Vishay Siliconix

TSOP: 5/6-LEAD JEDEC Part Number: MO-193C









6-LEAD TSOP



	MIL	LIMETER	RS	INCHES					
Dim	Min	Nom	Max	Min	Nom	Max			
Α	0.91	-	1.10	0.036	-	0.043			
<b>A</b> <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004			
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039			
b	0.30	0.32	0.45	0.012	0.013	0.018			
С	0.10	0.15	0.20	0.004	0.006	0.008			
D	2.95	3.05	3.10	0.116	0.120	0.122			
Е	2.70	2.85	2.98	0.106	0.112	0.117			
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067			
е	0.95 BSC			0.0374 BSC					
<b>e</b> <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079			
L	0.32	-	0.50	0.012	-	0.020			
L <sub>1</sub>	0.60 Ref				0.024 Ref				
L <sub>2</sub>	0.25 BSC				0.010 BSC				
R	0.10	-	-	0.004	-	-			
θ	0°	4°	8°	0°	4°	8°			
$\theta_1$	7° Nom				7° Nom				
		ev. I, 18-Dec	c-06		ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540				

## **PAD** Pattern



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# **Recommended Land Pattern For TSOP-5L / TSOP-6L**





TSOP 5L





#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	



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