

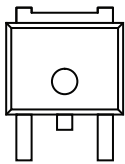
## Automotive N-Channel 20 V (D-S) 175 °C MOSFET



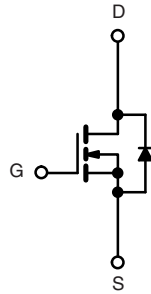
PRODUCT SUMMARY	
$V_{DS}$ (V)	20
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 10$ V	0.0043
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5$ V	0.006
$I_D$ (A)	50
Configuration	Single

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_g$  and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Compliant to RoHS Directive 2002/95/EC

**TO-252**

 G D S  
Top View

Drain Connected to Tab



N-Channel MOSFET

ORDERING INFORMATION	
Package	TO-252
Lead (Pb)-free and Halogen-free	SQD50N02-04-GE3

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	$V_{DS}$	20	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$		
Continuous Drain Current <sup>a</sup>	$I_D$	$T_C = 25$ °C	50	
		$T_C = 125$ °C	50	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	50	A	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	200		
Single Pulse Avalanche Energy	$I_{AS}$	36		
Single Pulse Avalanche Current	$E_{AS}$	L = 0.1 mH	65	mJ
Maximum Power Dissipation <sup>b</sup>			$P_D$	
		$T_C = 125$ °C		45
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	50	°C/W
Junction-to-Case (Drain)			

### Notes

- Package limited.
- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
<b>Static</b>							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		20	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		1.5	2.0	2.5	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 20 V	-	-	1.0	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 20 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 20 V, T <sub>J</sub> = 175 °C	-	-	250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≥ 5 V	50	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A	-	0.0033	0.0043	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	-	0.0063	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	-	0.0073	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 20 A	-	0.0045	0.006	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		-	80	-	S
<b>Dynamic<sup>b</sup></b>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 10 V, f = 1 MHz	-	5000	6250	pF
Output Capacitance	C <sub>oss</sub>			-	1437	1800	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	731	915	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 50 A	-	79	119	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	11.5	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	14.1	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.85	1.7	2.55	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 0.2 Ω I <sub>D</sub> ≅ 50 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		-	13	20	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			-	10	15	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	41	62	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	9	14	
<b>Source-Drain Diode Ratings and Characteristics<sup>b</sup></b>							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	200	A
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 50 A, V <sub>GS</sub> = 0 V		-	0.92	1.5	V

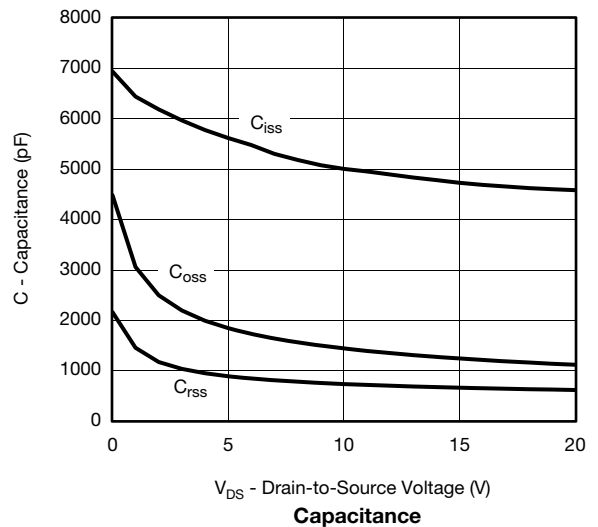
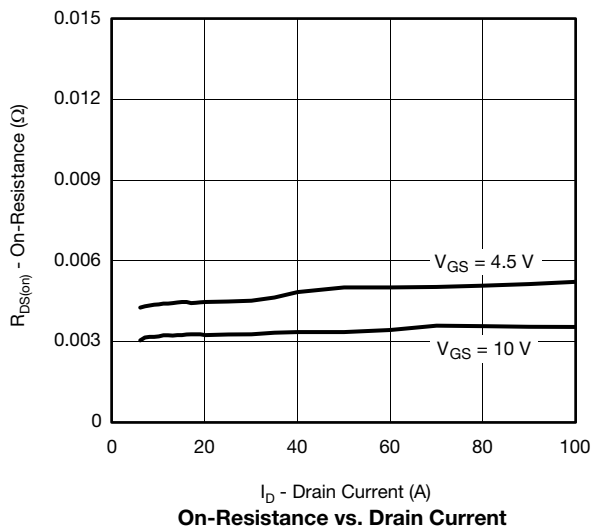
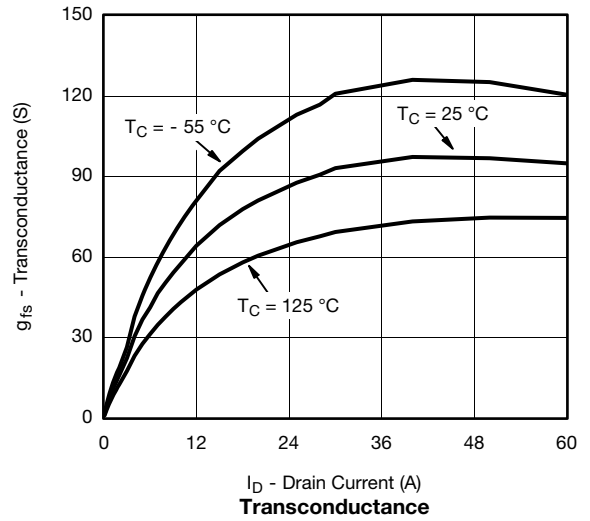
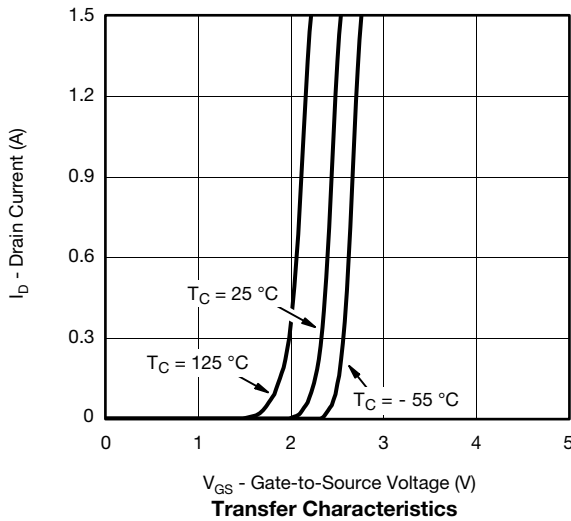
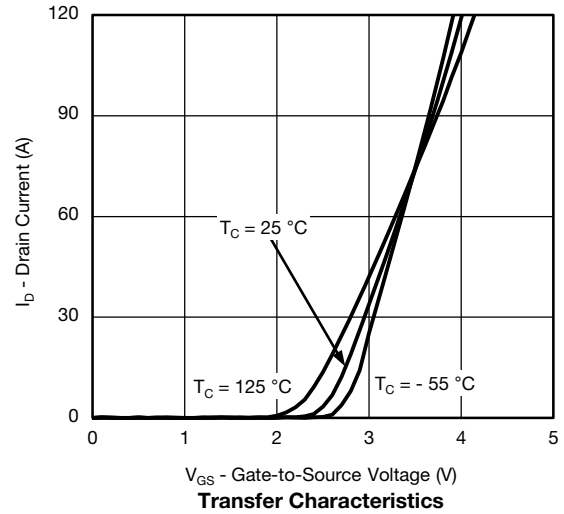
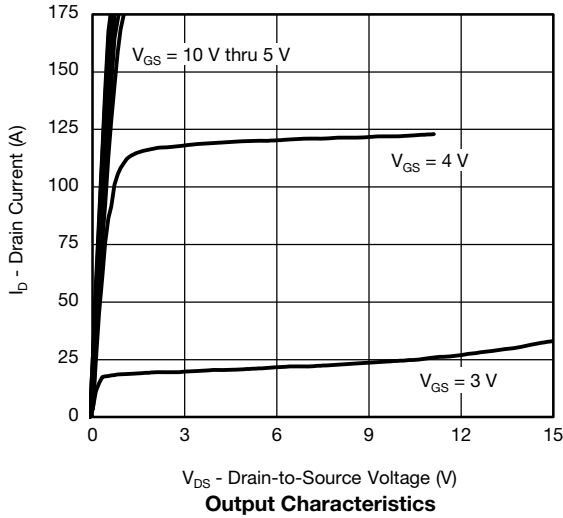
**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

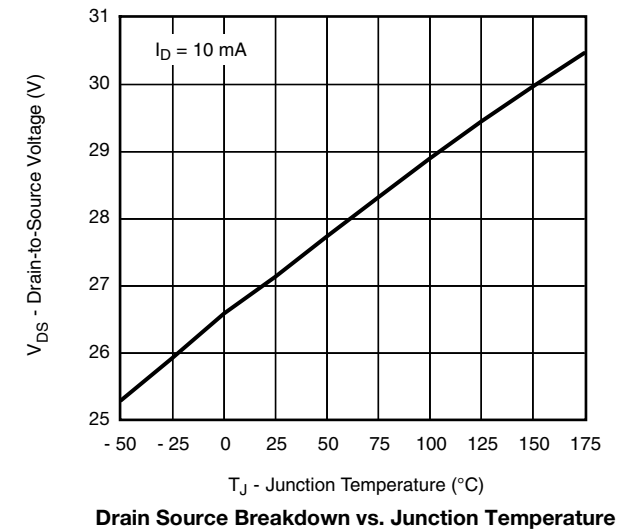
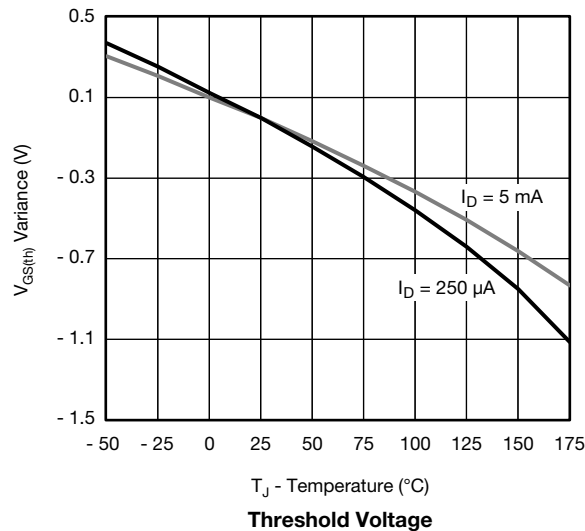
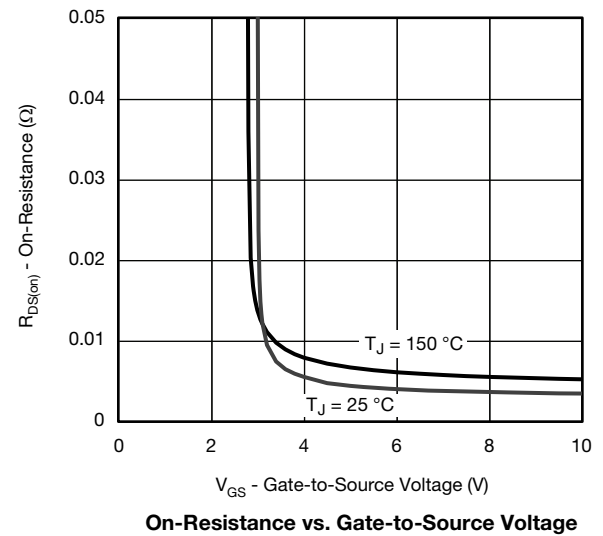
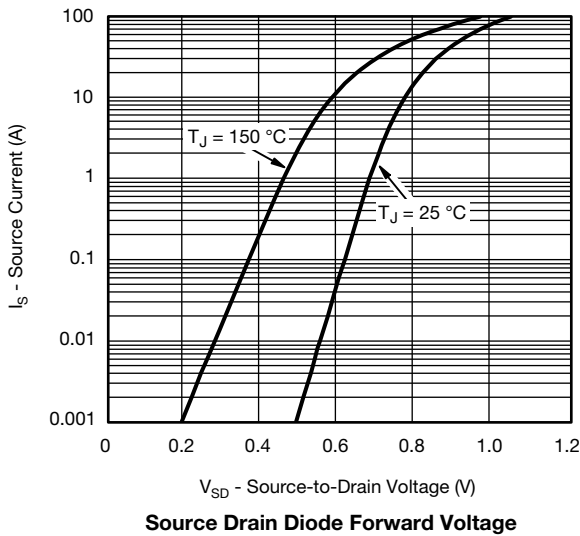
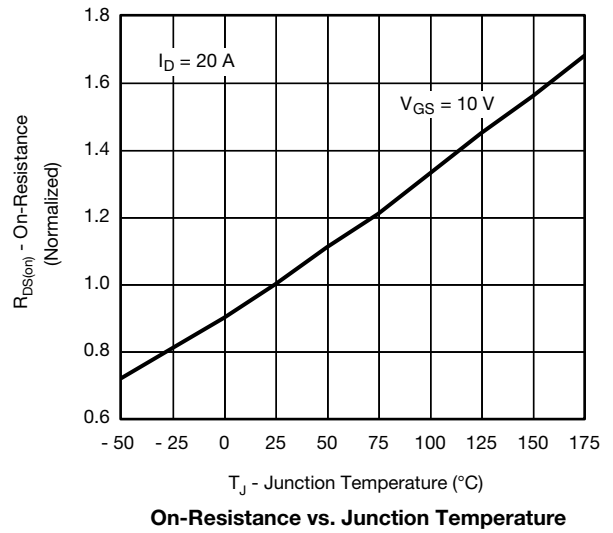
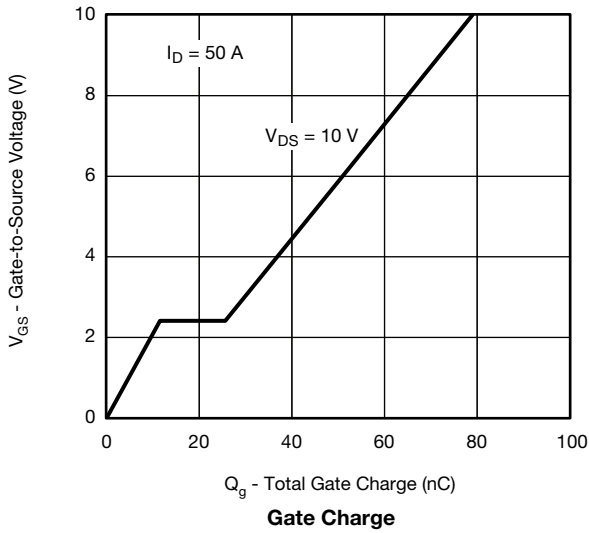


TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



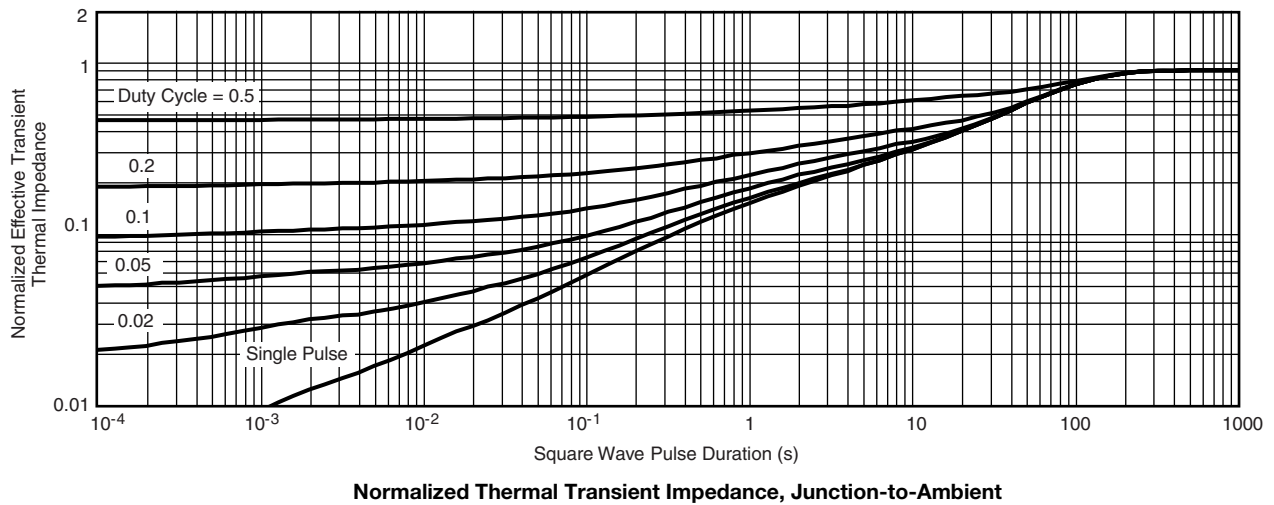
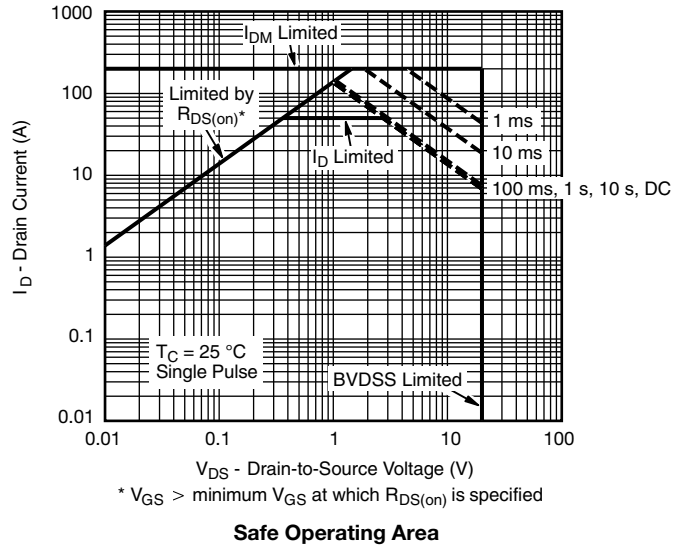


**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



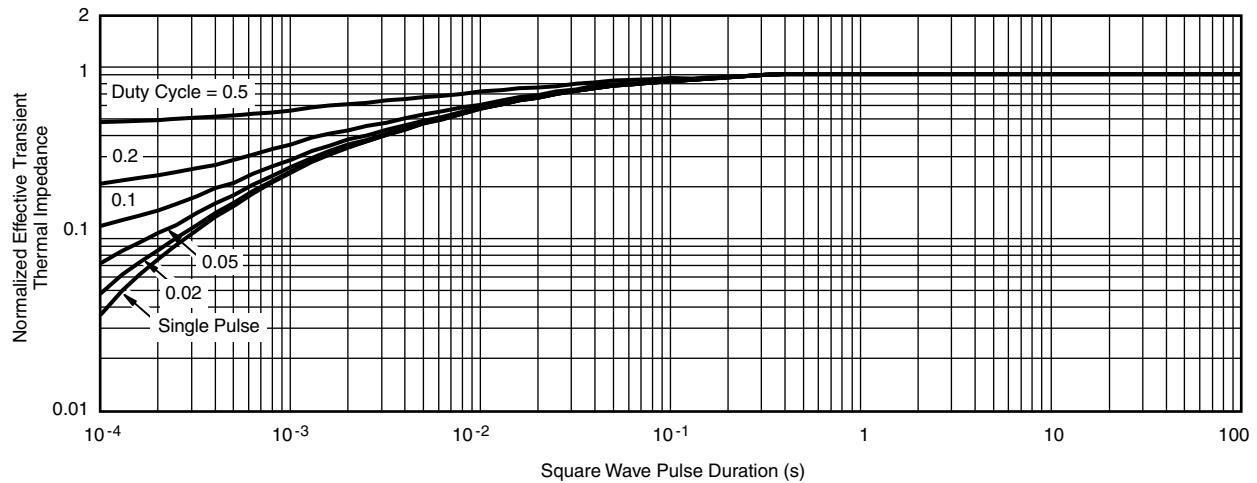


**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)





**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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