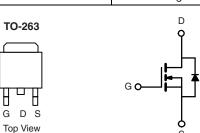


www.vishay.com

Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0035			
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5 \text{ V}$	0.0053			
I _D (A)	120			
Configuration	Single			



N-Channel MOSFET

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- AEC-Q101 Qualifiedd
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



FREE

ORDERING INFORMATION	
Package	TO-263
Lead (Pb)-free and Halogen-free	SQM110N04-03L-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unles		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	40		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C ^a		120		
	T _C = 125 °C	- I _D	105		
Continuous Source Current (Diode Conduction) ^a		I _S	120	Α	
Pulsed Drain Current ^b		I _{DM}	480		
Single Pulse Avalanche Current	0.1!!	I _{AS}	54		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	145	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	$T_C = 25 \degree C$ $T_C = 125 \degree C$ P_D	230	W	
	T _C = 125 °C		76	7	
Operating Junction and Storage Temperatu	re Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient P	CB Mount ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)		R _{thJC}	0.65	- C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static	1			l.	·	ı	ı	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		40	-	-	V	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu A$		2.0	2.5		
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 40 V	-	-	1.0		
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 40 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 175 °C	-	-	250		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	120	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 30 A	-	0.0027	0.0035	Ω	
		V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	-	0.0057		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	-	0.0070		
		V _{GS} = 4.5 V	I _D = 20 A	-	0.0041	0.0053		
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		-	106	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	4252	5315	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	854	1070		
Reverse Transfer Capacitance	C _{rss}]		-	472	590		
Total Gate Charge ^c	Qg		V _{DS} = 20 V, I _D = 110 A	-	93	140	nC	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	14.6	-		
Gate-Drain Charge ^c	Q _{gd}	1		-	19.2	-		
Gate Resistance	R _g	f = 1 MHz		0.5	1.1	1.7	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	15	23		
Rise Time ^c	t _r	V_{DD} = 20 V, R_L = 0.18 Ω $I_D \cong$ 110 A, V_{GEN} = 10 V, R_g = 1 Ω		-	13	20	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	52	78		
Fall Time ^c	t _f			-	16	24		
Source-Drain Diode Ratings and Char-	acteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	480	Α	
Forward Voltage	V _{SD}	$I_F = 100 \text{ A}, V_{GS} = 0$		-	0.9	1.5	V	

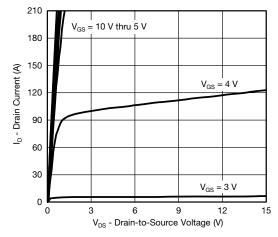
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

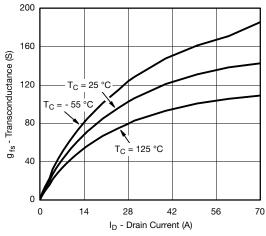
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



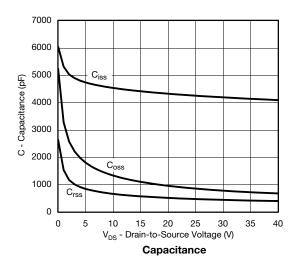
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

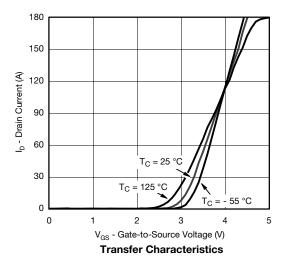


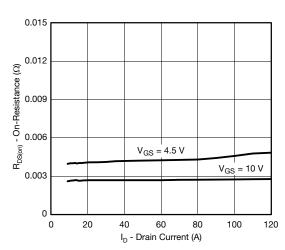
Output Characteristics



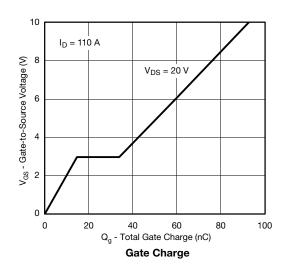
Transconductance





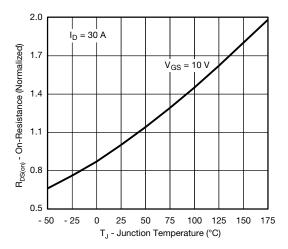


On-Resistance vs. Drain Current

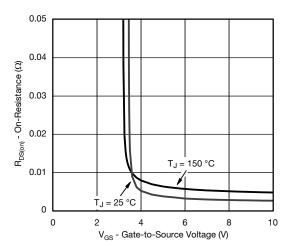




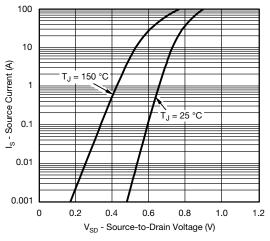
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



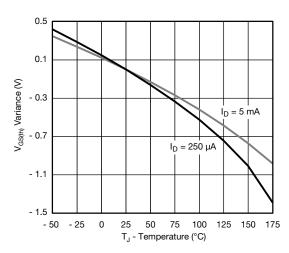
On-Resistance vs. Junction Temperature



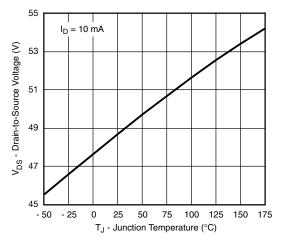
On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage



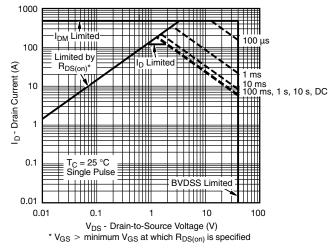
Threshold Voltage



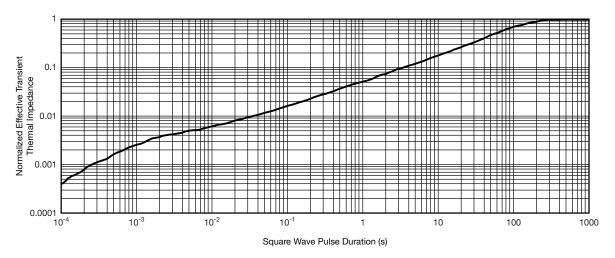
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



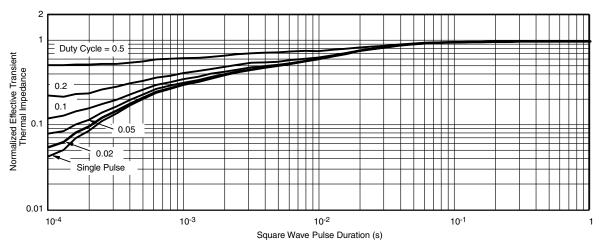
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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