## Si2367DS

RoHS

COMPLIANT

HALOGEN

FREE

www.vishay.com

Marking code: H7

V<sub>DS</sub> (V)

Q<sub>q</sub> typ. (nC)

Configuration

I<sub>D</sub> (A) d

**PRODUCT SUMMARY** 

 $R_{DS(on)}$  max. ( $\Omega$ ) at  $V_{GS}$  = -4.5 V

 $R_{DS(on)}$  max. ( $\Omega$ ) at  $V_{GS}$  = -2.5 V

 $R_{DS(on)}$  max. ( $\Omega$ ) at  $V_{GS}$  = -1.8 V

**Vishay Siliconix** 



#### SOT-23 (TO-236)

-20

0.066

0.086

0.130

9

-3.8

Single

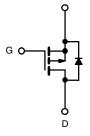
## **FEATURES**

P-Channel 20 V (D-S) MOSFET

- TrenchFET<sup>®</sup> power MOSFET
- 100 % R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · Load switch for portable devices
- DC/DC converter



P-Channel MOSFET

S

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	Si2367DS-T1-GE3

ABSOLUTE MAXIMUM RATINGS	(T <sub>A</sub> = 25 °C, unless	s otherwise not	ed)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	-20	V	
Gate-source voltage		V <sub>GS</sub>	± 8	v	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		-3.8		
	T <sub>C</sub> = 70 °C	1 . [	-3		
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-2.8 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		-2.2 <sup>a, b</sup>	А	
Pulsed drain current (10 µs width)		I <sub>DM</sub>	-15		
Continuous source-drain diode current	T <sub>C</sub> = 25 °C		-1.4		
	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-0.8 <sup>a, b</sup>		
Maximum power dissipation	T <sub>C</sub> = 25 °C		1.7		
	T <sub>C</sub> = 70 °C		1.1		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.96 <sup>a, b</sup>	— W	
	T <sub>A</sub> = 70 °C	1 1	0.62 <sup>a, b</sup>	7	
Operating junction and storage temperature rat	nge	TJ, T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient <sup>a, c</sup>	t ≤ 5 s	R <sub>thJA</sub>	100	130	°C/W
Maximum junction-to-foot (drain)	Steady state	R <sub>thJF</sub>	60	75	0/11

Notes

a. Surface mounted on 1" x 1" FR4 board

b. t = 5 s

c. Maximum under steady state conditions is 175 °C/W

d.  $T_C = 25 \ ^{\circ}C$ 

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Si2367DS

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SPECIFICATIONS (T <sub>J</sub> = 25 $^{\circ}$ C,	unless oth	erwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \ \mu A$	-20	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = -250 μΑ	-	-20	-	mV/°C
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	i <sub>D</sub> = -230 μA	-	-2.5	-	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	-0.4	-	-1	V
Gate-source leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 8 V	-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = -20 V, V_{GS} = 0 V$	-	-	-1	цА
	USS	$V_{DS}$ = -20 V, $V_{GS}$ = 0 V, $T_J$ = 55 $^\circ C$	-	-	-10	μA
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \leq$ -5 V, $V_{GS}$ = -4.5 V	-5	-	-	Α
		$V_{GS}$ = -4.5 V, I <sub>D</sub> = -2.5 A	-	0.055	0.066	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -2 \text{ A}$	-	0.071	0.086	Ω
		$V_{GS}$ = -1.8 V, I <sub>D</sub> = -1.5 A	-	0.100	0.130	
Forward transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -2.5 \text{ A}$	-	7.5	-	S
Dynamic <sup>b</sup>						
Input capacitance	C <sub>iss</sub>		-	561	-	pF
Output capacitance	C <sub>oss</sub>	$V_{DS}$ = -10 V, $V_{GS}$ = 0 V, f = 1 MHz	-	112	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	89	-	
Tatal acto obergo	0	$V_{DS}$ = -10 V, $V_{GS}$ = -8 V, $I_{D}$ = -2.5 A	-	15	23	nC
Total gate charge	Qg		-	9	13.5	
Gate-source charge	Q <sub>gs</sub>	$V_{DS}$ = -10 V, $V_{GS}$ = -4.5 V, $I_{D}$ = -2.5 A	-	1	-	
Gate-drain charge	Q <sub>gd</sub>		-	2.5	-	
Gate resistance	Rg	f = 1 MHz	2	10	20	Ω
Turn-on delay time	t <sub>d(on)</sub>		-	20	40	
Rise time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{L} = 5 \Omega,$	-	20	40	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong$ -2 A, $V_{GEN}$ = -4.5 V, $R_g$ = 1 $\Omega$	-	40	70	
Fall time	t <sub>f</sub>		-	10	20	
Turn-on delay time	t <sub>d(on)</sub>		-	8	16	ns
Rise time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{L} = 5 \Omega,$	-	9	18	-
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong$ -2 A, $V_{GEN}$ = -8 V, $R_g$ = 1 $\Omega$	-	35	65	
Fall time	t <sub>f</sub>		-	9	18	
Drain-Source Body Diode Characteris	tics					
Continuous source-drain diode current	Is	T <sub>C</sub> = 25 °C	-	-	-1.4	
Pulse diode forward current	I <sub>SM</sub>		-	-	-15	A
Body diode voltage	V <sub>SD</sub>	$I_{\rm S} = -2$ A, $V_{\rm GS} = 0$ V	-	-0.79	-1.2	V
Body diode reverse recovery time	t <sub>rr</sub>		-	21	35	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = -2 A, di/dt = 100 A/µs,	-	15	25	nC
Reverse recovery fall time	t <sub>a</sub>	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$	-	9	-	
Reverse recovery rise time	t <sub>b</sub>			12	_	ns

Notes

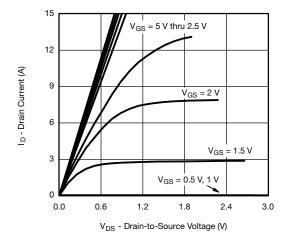
a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$ 

b. Guaranteed by design, not subject to production testing

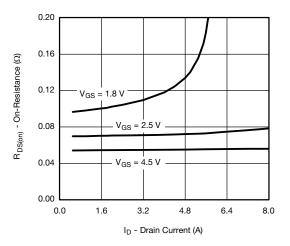
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



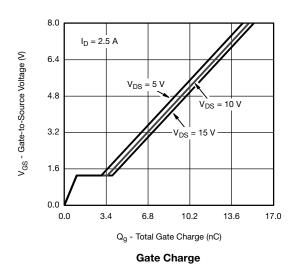
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

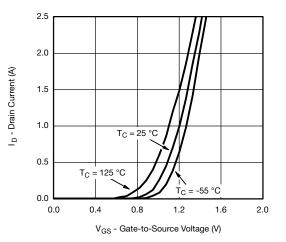


**Output Characteristics** 

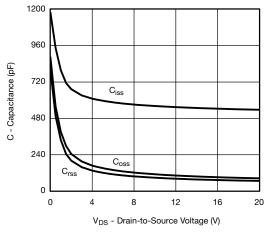


**On-Resistance vs. Drain Current and Gate Voltage** 

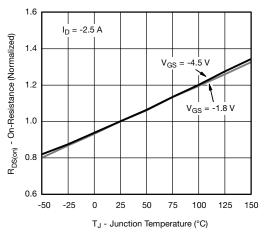




Transfer Characteristics







**On-Resistance vs. Junction Temperature** 

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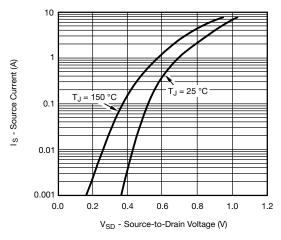
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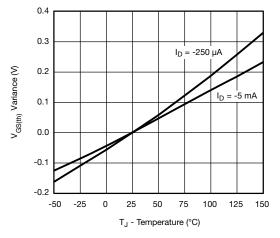
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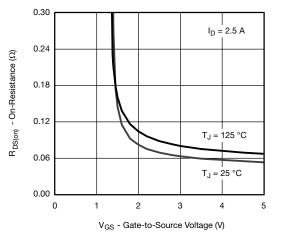
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



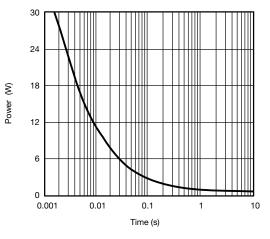
Source-Drain Diode Forward Voltage



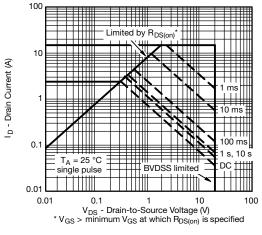




**On-Resistance vs. Gate-to-Source Voltage** 



Single Pulse Power, Junction-to-Ambient

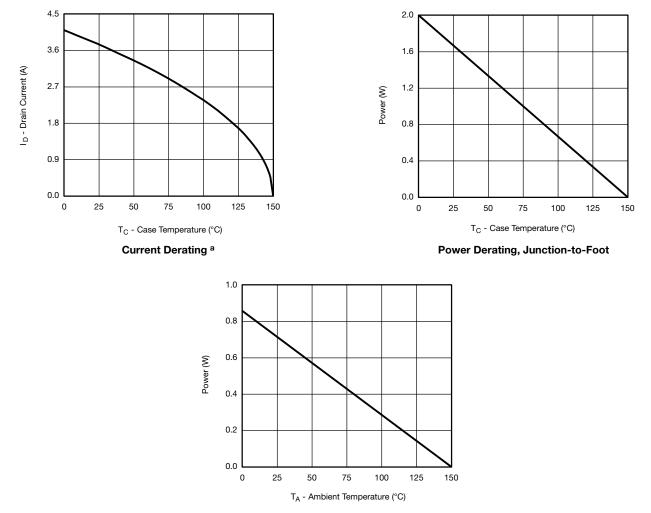


Safe Operating Area, Junction-to-Ambient

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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



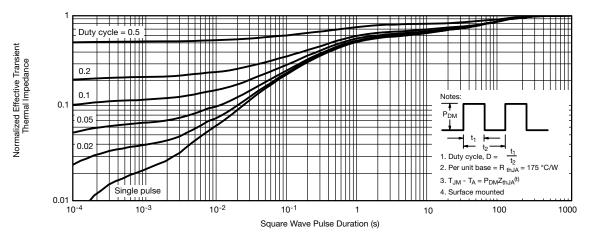
Power, Junction-to-Ambient

#### Note

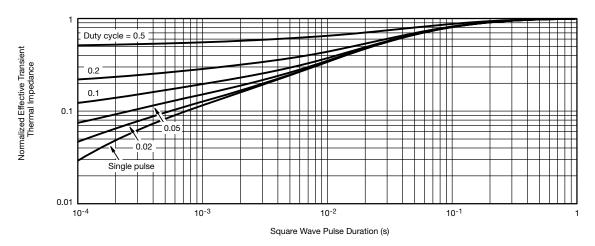
a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?65015</u>.



# Package Information

Vishay Siliconix

## SOT-23 (TO-236): 3-LEAD







Dim Min	MILLIN	METERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



# Application Note 826

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## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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