SQ3427AEEV

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Vishay Siliconix

Automotive P-Channel 60 V (D-S) 175 °C MOSFET

FEATURES

TrenchFET[®] power MOSFET

• Typical ESD protection 800 V

www.vishay.com/doc?99912

for definitions of compliance please see

(3) G O

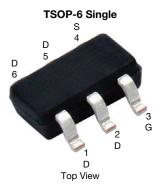
(1, 2, 5, 6) D

(4) S P-Channel MOSFET

• AEC-Q101 qualified ^c

• 100 % $R_{\rm q}$ and UIS tested

• Material categorization:



PRODUCT SUMMARY				
V _{DS} (V)	-60			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	0.095			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.135			
I _D (A)	-5.3			
Configuration	Single			
Package	TSOP-6			

Marking Code: 8Y

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3427AEEV (for detailed order number please see <u>www.vishay.com/doc?79771</u>)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	-60	V	
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current	T _C = 25 °C	1	-5.3	
	T _C = 125 °C	l _D	-3	
Continuous Source Current (Diode Conduction)	I _S	-6.3	А	
Pulsed Drain Current ^a		I _{DM}	-21	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	-21	
Single Pulse Avalanche Energy		E _{AS}	22	mJ
Maximum Power Dissipation ^a	T _C = 25 °C		5	w
	T _C = 125 °C	P _D	1.6	vv
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient PCB Mount ^b		R _{thJA}	110	°C/W	
Junction-to-Foot (Drain)		R _{thJF}	30	0/11	

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

b. When mounted on 1" square PCB (FR4 material)

c. Parametric verification ongoing

S21-1246-Rev. B, 10-Jan-2022

1

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Pb-free RoHS COMPLIANT HALOGEN FREE

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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT		
Static	•							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA		-60	-	-	v	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = -250 μA	-1.5	-2	-2.5	v	
Onto Course Logicono	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 10		
Gate-Source Leakage		V _{DS} =	0 V, $V_{GS} = \pm 10 V$	-	-	± 2	mA	
		$V_{GS} = 0 V$	V _{DS} = -60 V	-	-	-1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = -60 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	-50	μA	
		$V_{GS} = 0 V$	$V_{DS} = -60 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	-150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = -10 V	$V_{DS} \le -5 V$	-10	-	-	Α	
		$V_{GS} = -10 V$	I _D = -4.5 A	-	0.079	0.095	Ω	
Drain Source On State Registered a	Б	$V_{GS} = -10 V$	I _D = -4.5 A, T _J = 125 °C	-	-	0.148		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = -10 V	I _D = -4.5 A, T _J = 175 °C	-	-	0.178		
		$V_{GS} = -4.5 V$	I _D = -3.5 A	-	0.112	0.135		
Forward Transconductance ^a	g _{fs}	V _{DS}	= -15 V, I _D = -4 A	-	9	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{DS} = -30 V, f = 1 MHz	-	700	1000	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	90	120		
Reverse Transfer Capacitance	C _{rss}			-	50	75	-	
Total Gate Charge ^c	Qg			-	15.3	22		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = -10 V	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	-	2.5	-	nC	
Gate-Drain Charge ^c	Q _{gd}			-	5.4	-		
Gate Resistance	Rg	f = 1 MHz		2.7	5.4	8.1	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	8	12		
Rise Time ^c	tr	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{L}} = 6 \Omega$ $I_{\text{D}} \cong -5 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	24	35		
Turn-Off Delay Time ^c	t _{d(off)}			-	26	38	ns	
Fall Time ^c	t _f			-	33	50		
Source-Drain Diode Ratings and Chara	cteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	-21	Α	
Forward Voltage	V _{SD}	I _F = -1.6 A, V _{GS} = 0 V		-	-0.8	-1.2	V	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

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c. Independent of operating temperature.

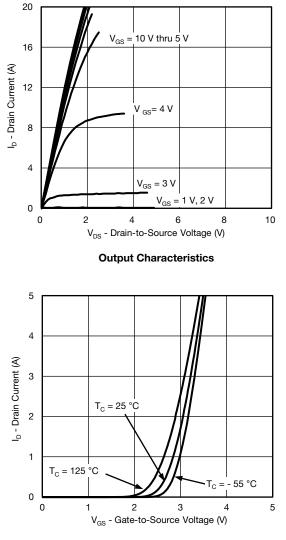
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



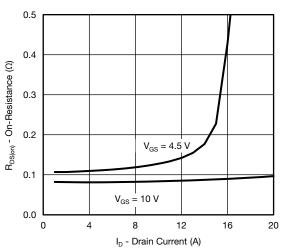
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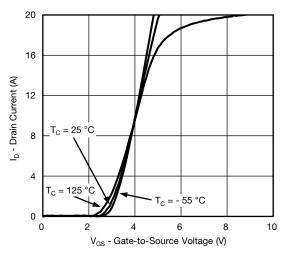
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



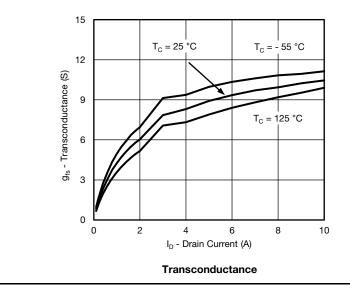


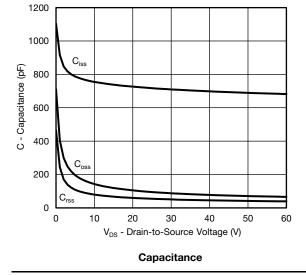


On-Resistance vs. Drain Current and Gate Voltage









S21-1246-Rev. B, 10-Jan-2022

3

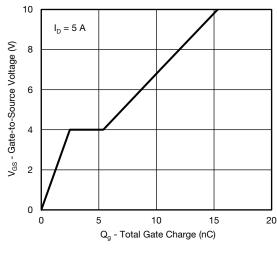
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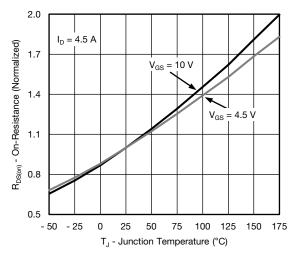
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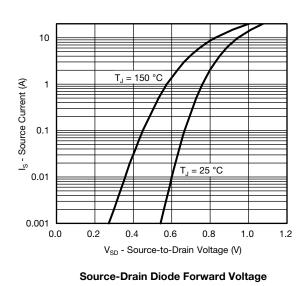
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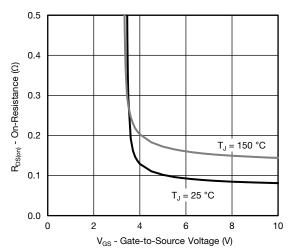




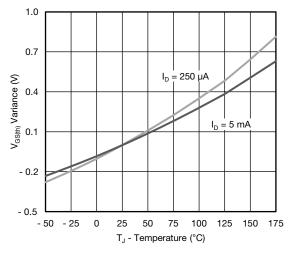


On-Resistance vs. Junction Temperature

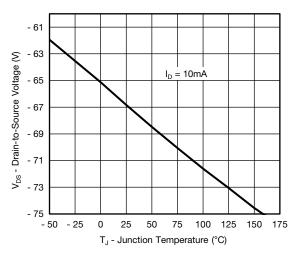




On-Resistance vs. Gate-to-Source Voltage







Drain-to-Source Voltage vs. Junction Temperature

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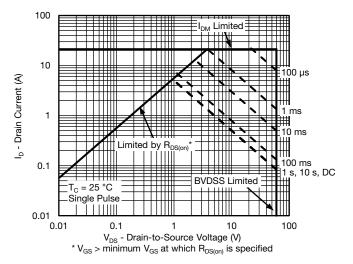
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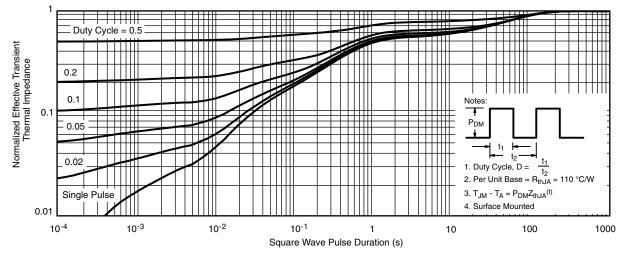


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

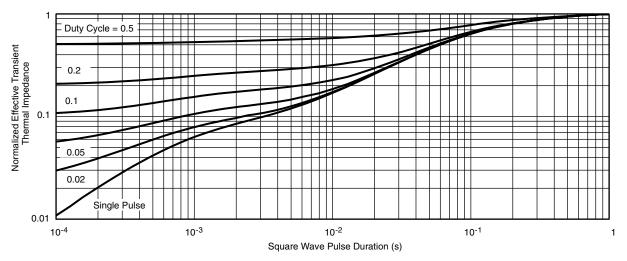


S21-1246-Rev. B, 10-Jan-2022

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

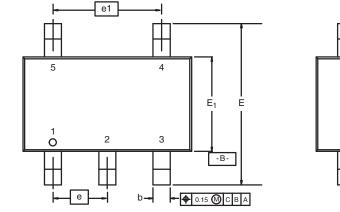
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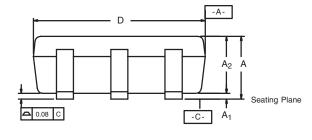
Package Information

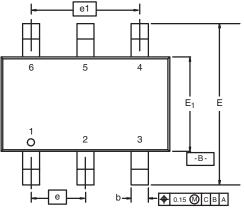
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TSOP: 5/6-LEAD JEDEC Part Number: MO-193C

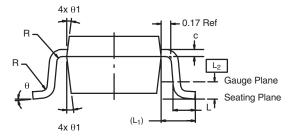








6-LEAD TSOP



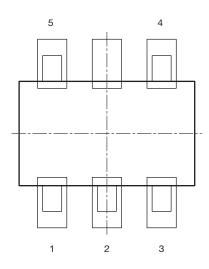
	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC			0.0374 BSC		
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref			0.024 Ref		
L ₂	0.25 BSC				0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom				7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

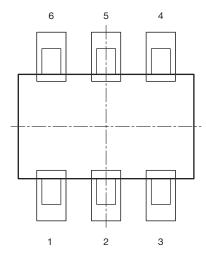
PAD Pattern



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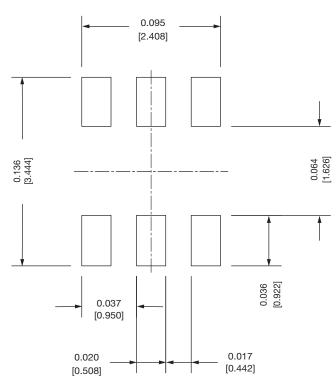
Recommended Land Pattern For TSOP-5L / TSOP-6L





TSOP 5L





Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	



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