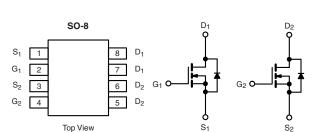


Vishay Siliconix

Automotive Dual N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.020			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.026			
I _D (A)	8			
Configuration	Dual			



N-Channel MOSFET N-Channel MOSFET

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_a and UIS Tested
- AEC-Q101 Qualified^d
- Compliant to RoHS Directive 2002/95/EC



FREE

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and Halogen-free	SQ4942EY-T1-GE3

ABSOLUTE MAXIMUM RATING	(16 = 5, 5, 11 1 5				
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	40	V	
Gate-Source Voltage		V_{GS}	± 20	 	
Continuous Drain Current	T _C = 25 °C ^a	1	8		
	T _C = 125 °C	- I _D	6.3		
Continuous Source Current (Diode Conduction)		I _S	4	А	
Pulsed Drain Current ^b		I _{DM}	30		
Single Pulse Avalanche Energy	1 0 1 1	I _{AS}	28		
Single Pulse Avalanche Current	L = 0.1 mH	E _{AS}	39	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C		4.4	· w	
	T _C = 125 °C		1.4		
Operating Junction and Storage Temperature	e Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient F	PCB Mount ^c	R _{thJA}	110	°C/W	
Junction-to-Foot (Drain)		R_{thJF}	34] C/W	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static	1			L	ı	ı	<u> </u>	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40	-	-	.,	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	· V _{GS} , I _D = 250 μA	1.5	2	2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 40 V	-	-	1.0		
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 40 V, T _J = 55 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	20	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 6 A	-	0.016	0.020	Ω	
	В	V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.031		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.036		
		V _{GS} = 4.5 V	I _D = 5 A	-	0.020	0.026		
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 6 A		-	23	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V V _{DS} = 25 V, f = 1 MHz	-	1409	1760	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	199	250		
Reverse Transfer Capacitance	C _{rss}	1		-	112	140		
Total Gate Charge ^c	Qg			-	28.4	43		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 20 \text{ V}, I_{D} = 5.7 \text{ A}$	-	4	-	nC	
Gate-Drain Charge ^c	Q _{gd}			-	6	-		
Gate Resistance	R _g	f = 1 MHz		0.5	-	2	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	8	12		
Rise Time ^c	t _r	$V_{DD} = 20 \text{ V, } R_L = 3.5 \Omega$ $I_D \cong 5.7 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	13	20	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	20	30		
Fall Time ^c	t _f			-	9	14		
Source-Drain Diode Ratings and Char	acteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	30	Α	
Forward Voltage	V _{SD}	I _F = 1.8 A, V _{GS} = 0 V		-	0.75	1.1	V	

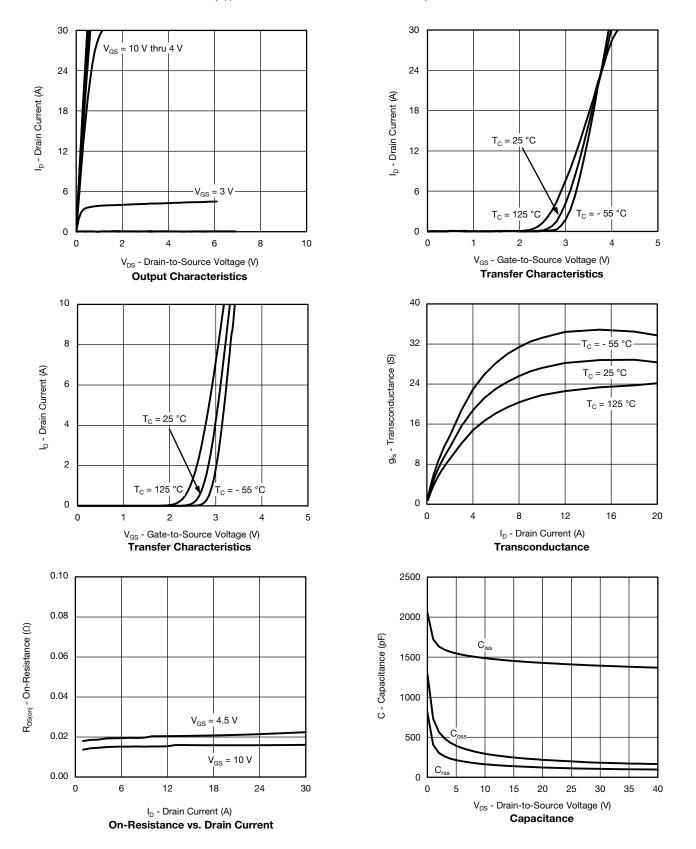
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

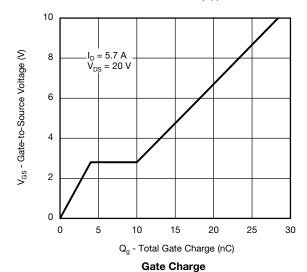


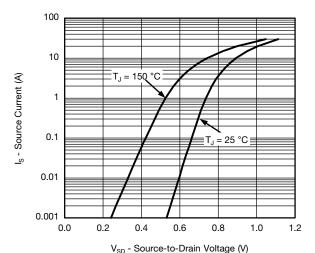
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



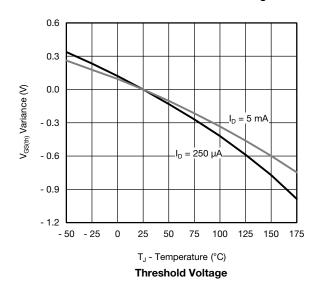


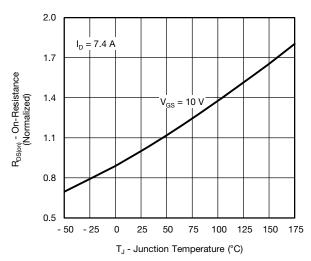
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



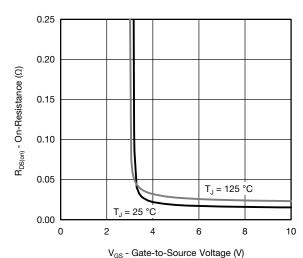


Source Drain Diode Forward Voltage

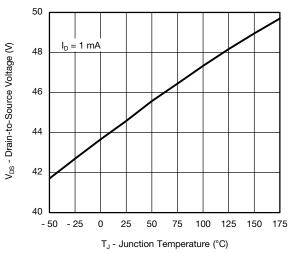




On-Resistance vs. Junction Temperature



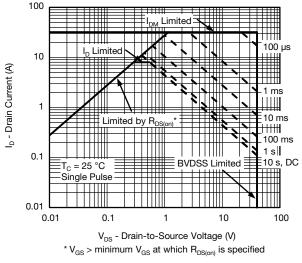
On-Resistance vs. Gate-to-Source Voltage



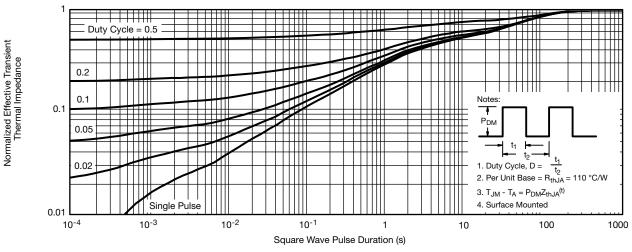
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



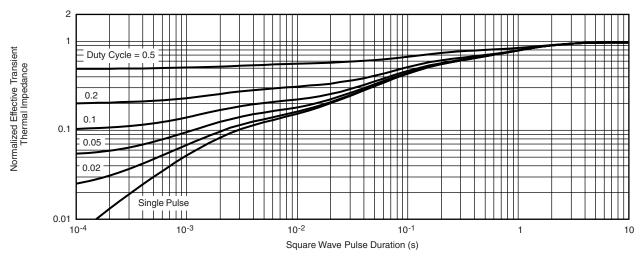
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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