Vishay Siliconix

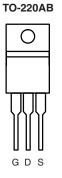
# N-Channel 100 V (D-S) MOSFET

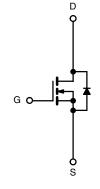
PRODUCT SUMMARY			
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)	
100	0.030 at V <sub>GS</sub> = 10 V	38.5	
	0.034 at V <sub>GS</sub> = 6 V	36	

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

Top View

Ordering Information: SUP40N10-30-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_{C} = 25 \ ^{\circ}C$ , unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	v	
Gate-Source Voltage		V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		38.5		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	17	А	
Pulsed Drain Current		I <sub>DM</sub>	75		
Avalanche Current		I <sub>AS</sub>	35		
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	61	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	Р	89 <sup>b</sup>	w	
	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub> -	3.1	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	40	°C/W
	Free Air		62.5	
Junction-to-Case (Drain)		R <sub>thJC</sub>	1.4	

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	100			v
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
	I <sub>DSS</sub>	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	
		$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$			150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	75			А
Drain-Source On-State Resistance <sup>a</sup>	(* )	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		0.024	0.030	- Ω
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 10 A		0.026	0.034	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C			0.054	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 150 °C			0.060	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	10			S
Dynamic <sup>b</sup>	•		•			
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		2400		pF
Output Capacitance	C <sub>oss</sub>			270		
Reverse Transfer Capacitance	C <sub>rss</sub>			90		
Total Gate Charge <sup>c</sup>	Qq			35	60	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A		11		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			9		
Gate Resistance	R <sub>q</sub>			1.7		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			11	20	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 50 V, R <sub>L</sub> = 1.25 $\Omega$ I <sub>D</sub> $\cong$ 40 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 2.5 $\Omega$		12	20	- ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			30	45	
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20	
Source-Drain Diode Ratings and Cha	aracteristics 7	<sub>C</sub> = 25 °C <sup>b</sup>				
Continuous Current	ا <sub>S</sub>				40	^
Pulsed Current	I <sub>SM</sub>				75	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			60	100	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, dl/dt = 100 A/μs		5	8	Α
Reverse Recovery Charge	Q <sub>rr</sub>			0.15	0.4	μC

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

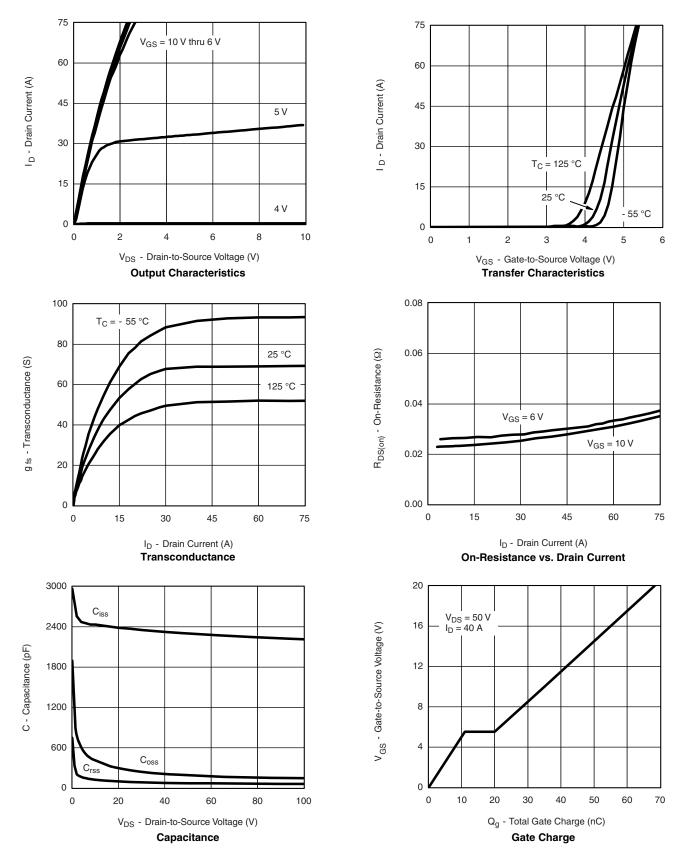
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



# SUP40N10-30-GE3

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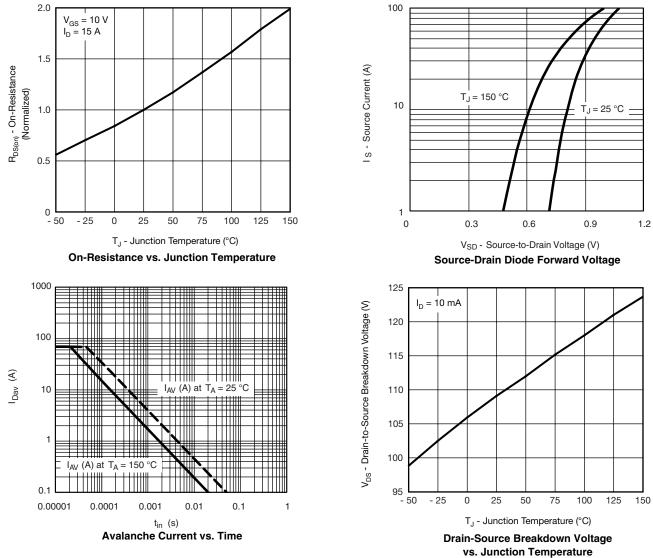
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



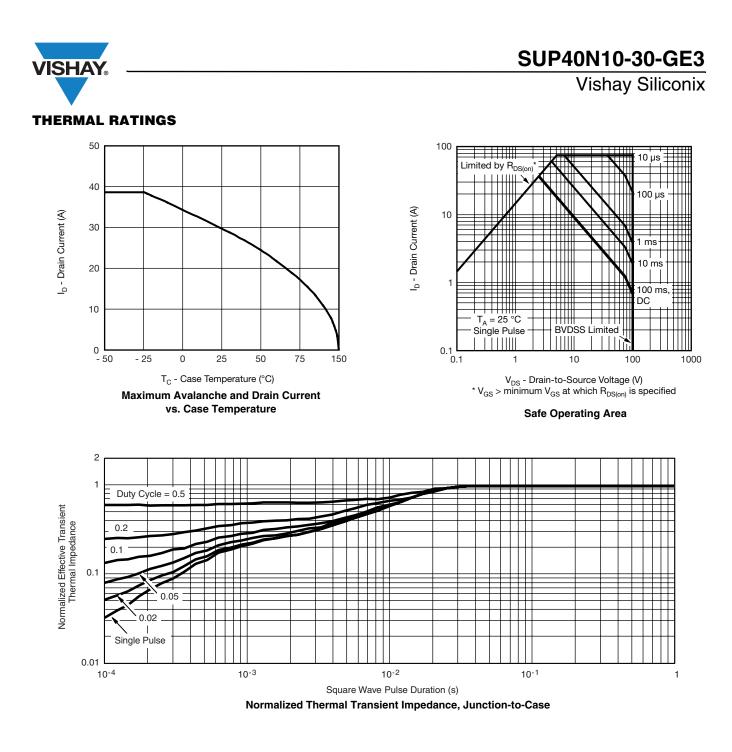
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