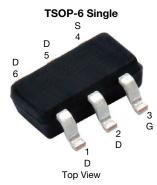
SQ3457EV

www.vishay.com

Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET



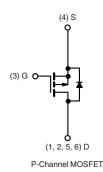
PRODUCT SUMMARY				
V _{DS} (V)	-30			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.065			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.100			
I _D (A)	- 6.8			
Configuration	Single			

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified^d
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3457EV (for detailed order number please see <u>www.vishay.com/doc?79771</u>)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V _{DS}	-30	N/		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current ^a	T _C = 25 °C	1	-6.8		
	T _C = 125 °C	I _D	-3.9		
Continuous Source Current (Diode Conduction) ^a	I _S	-6.3	А		
Pulsed Drain Current ^b		I _{DM}	-27		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	-14		
Single Pulse Avalanche Energy		E _{AS}	10	mJ	
Mauiau na Daura Diasia stiant	T _C = 25 °C	D	5	W	
Maximum Power Dissipation ^b	T _C = 125 °C	P _D	1.7	٧V	
Operating Junction and Storage Temperature Range	ge	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	110	°C/W	
Junction-to-Foot (Drain)		R _{thJF}	30	C/W	

Notes

a. Package limited

b. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

c. When mounted on 1" square PCB (FR-4 material)

d. Parametric verification ongoing

S21-1246-Rev. C, 10-Jan-2022

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Document Number: 66715

For technical questions, contact: automostechsupport@vishay.com

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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static	•	•					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = - 250 μA		- 30	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = - 250 μA	- 1.5	- 2.0	- 2.5	v
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, $V_{GS} = \pm 20$ V	-	-	± 100	nA
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V _{DS} = - 30 V	-	-	- 1	
	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = -30 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	- 50	μA
		$V_{GS} = 0 V$	$V_{DS} = -30 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	- 150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	$V_{DS} \ge 5 V$	- 10	-	-	Α
Drain Source On State Desistence	_	V _{GS} = - 10 V	I _D = - 6 A	-	0.035	0.065	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 V$	I _D = - 4.9 A	-	0.072	0.100	Ω
Forward Transconductance ^b	g _{fs}	V _{DS} = - 15 V, I _D = - 5 A		-	9	-	S
Dynamic ^b	•				•		
Input Capacitance	C _{iss}		_S = 0 V V _{DS} = - 15 V, f = 1 MHz	-	565	705	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	140	180	
Reverse Transfer Capacitance	C _{rss}			-	98	120	
Total Gate Charge ^c	Qg			-	14	21	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 10 V	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	-	2.1	3	nC
Gate-Drain Charge ^c	Q _{gd}			-	3	5	
Gate Resistance	Rg		f = 1 MHz		6.5	11	Ω
Turn-On Delay Time ^c	t _{d(on)}				6	9	
Rise Time ^c	t _r	$V_{\text{DD}} = -15 \text{ V}, \text{ R}_{\text{L}} = 3 \Omega$ $I_{\text{D}} \cong -5 \text{ A}, \text{ V}_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	9	14	- ns
Turn-Off Delay Time ^c	t _{d(off)}			-	18	27	
Fall Time ^c	t _f			-	6	9	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	- 27	А
Forward Voltage	V _{SD}	$I_{\rm F} = -1.6 {\rm A}, {\rm V_{GS}} = 0$		_	- 0.8	- 1.1	V

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

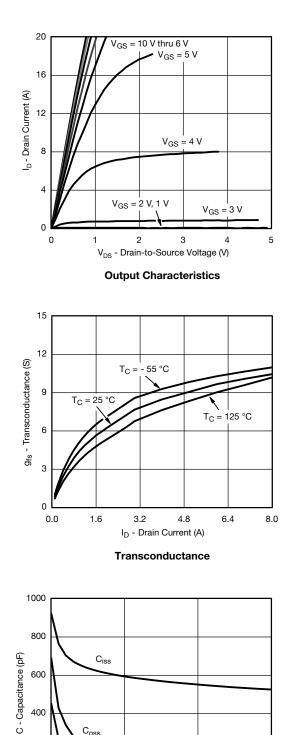
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

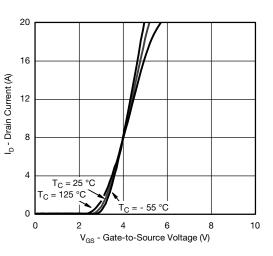
2



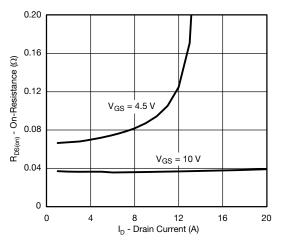
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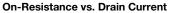
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

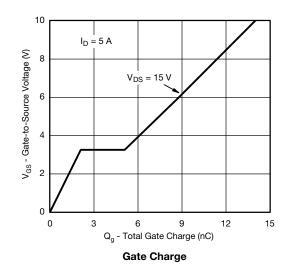


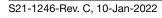


Transfer Characteristics









Coss

10

20

V_{DS} - Drain-to-Source Voltage (V)

Capacitance

30

 C_{rss}

400

200

0

0

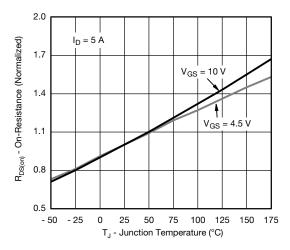
Document Number: 66715

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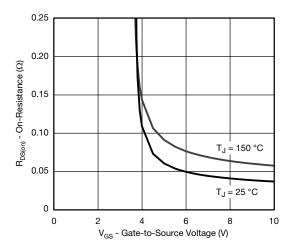


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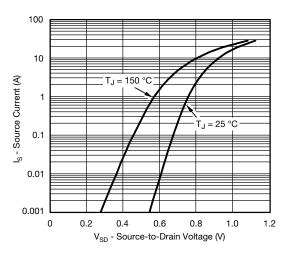
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



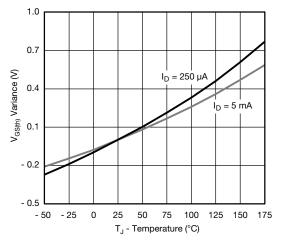
On-Resistance vs. Junction Temperature



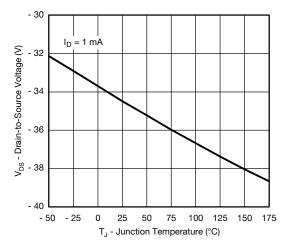
On-Resistance vs. Gate-to-Source Voltage



Source-Drain Diode Forward Voltage





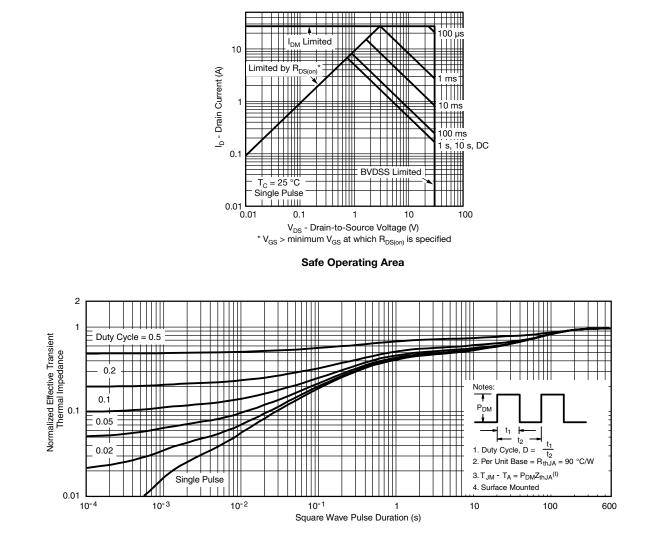


Drain Source Breakdown vs. Junction Temperature



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

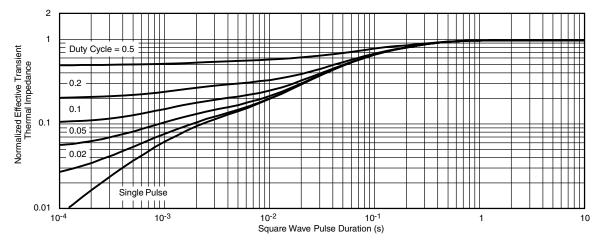


SQ3457EV

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

The characteristics shown in the two graphs

S21-1246-Rev. C, 10-Jan-2022

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?66715.



Package Information

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TSOP: 5/6-LEAD JEDEC Part Number: MO-193C









6-LEAD TSOP



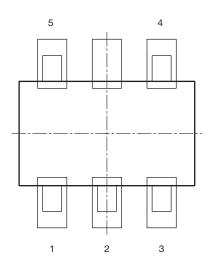
	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC			0.0374 BSC		
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref			0.024 Ref		
L ₂	0.25 BSC				0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom				7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

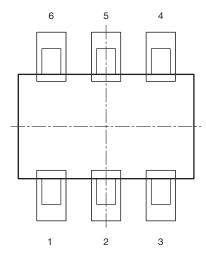
PAD Pattern



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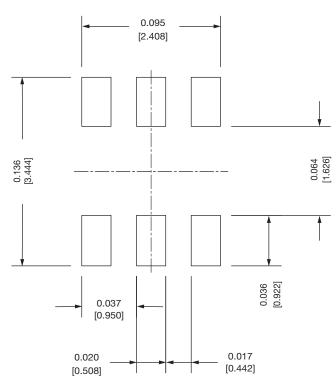
Recommended Land Pattern For TSOP-5L / TSOP-6L





TSOP 5L





Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	

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Revision: 01-Jan-2025

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