Automotive N-and P-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY		
	N-CHANNEL	P-CHANNEL
V_{DS} (V)	30	- 30
$R_{DS(on)}$ (Ω) at V_{GS} = ± 10 V	0.055	0.070
$R_{DS(on)}$ (Ω) at V_{GS} = ± 4.5 V	0.100	0.190
I_D (A)	5.6	- 5.3
Configuration	N- and P-Pair	

FEATURES

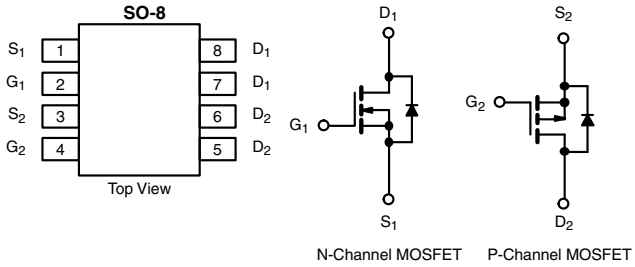
- **Halogen-free According to IEC 61249-2-21 Definition**
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified[c]
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

AUTOMOTIVE GRADE

Pb-free

RoHS COMPLIANT

HALOGEN FREE



ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and Halogen-free	SQ4532EY-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	N-CHANNEL	P-CHANNEL	UNIT
Drain-Source Voltage		V_{DS}	30	- 30	V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current	T_C = 25 °C	I_D	5.6	- 5.3	A
	T_C = 125 °C		3.2	- 3	
Continuous Source Current (Diode Conduction)		I_S	3	- 3	
Pulsed Drain Current[a]		I_{DM}	22	- 21	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	10	- 9	
Single Pulse Avalanche Energy		E_{AS}	5	4	mJ
Maximum Power Dissipation[a]	T_C = 25 °C	P_D	3.3	3.3	W
	T_C = 125 °C		1.1	1.1	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to + 175		°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	N-CHANNEL	P-CHANNEL	UNIT
Junction-to-Ambient	PCB Mount[b]	R_{thJA}	110	105	°C/W
Junction-to-Foot (Drain)		R_{thJF}	45	45	

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. When mounted on 1" square PCB (FR4 material).
c. Parametric verification ongoing.

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SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)								
PARAMETER	**SYMBOL**	**TEST CONDITIONS**			**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static								
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0, I_D = 250 µA		N-Ch	30	-	-	V
		V_{GS} = 0, I_D = - 250 µA		P-Ch	- 30	-	-	
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA		N-Ch	1.5	2	2.5	
		V_{DS} = V_{GS}, I_D = - 250 µA		P-Ch	- 1.5	- 2	- 2.5	
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		N-Ch	-	-	± 100	nA
				P-Ch	-	-	± 100	
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V	V_{DS} = 30 V	N-Ch	-	-	1	µA
		V_{GS} = 0 V	V_{DS} = - 30 V	P-Ch	-	-	- 1	
		V_{GS} = 0 V	V_{DS} = 30 V, T_J = 125 °C	N-Ch	-	-	50	
		V_{GS} = 0 V	V_{DS} = - 30 V, T_J = 125 °C	P-Ch	-	-	- 50	
		V_{GS} = 0 V	V_{DS} = 30 V, T_J = 175 °C	N-Ch	-	-	150	
		V_{GS} = 0 V	V_{DS} = - 30 V, T_J = 175 °C	P-Ch	-	-	- 150	
On-State Drain Current[a]	$I_{D(on)}$	V_{GS} = 10 V	V_{DS} = ≥ 5 V	N-Ch	15	-	-	A
		V_{GS} = - 10 V	V_{DS} = ≤ 5 V	P-Ch	- 15	-	-	
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V	I_D = 4.9 A	N-Ch	-	0.046	0.055	Ω
		V_{GS} = - 10 V	I_D = - 3.5 A	P-Ch	-	0.056	0.070	
		V_{GS} = 10 V	I_D = 4.9 A, T_J = 125 °C	N-Ch	-	-	0.087	
		V_{GS} = - 10 V	I_D = - 3.5 A, T_J = 125 °C	P-Ch	-	-	0.100	
		V_{GS} = 10 V	I_D = 4.9 A, T_J = 175 °C	N-Ch	-	-	0.105	
		V_{GS} = - 10 V	I_D = - 3.5 A, T_J = 175 °C	P-Ch	-	-	0.117	
		V_{GS} = 4.5 V	I_D = 4.1 A	N-Ch	-	0.083	0.100	
		V_{GS} = - 4.5 V	I_D = - 2.5 A	P-Ch	-	0.157	0.190	
Forward Transconductance[b]	g_{fs}	V_{DS} = 15 V, I_D = 4.9 A		N-Ch	-	9.8	-	S
		V_{DS} = - 15 V, I_D = - 3.5 A		P-Ch	-	5.5	-	
Dynamic[b]								
Input Capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch	-	444	555	pF
		V_{GS} = 0 V	V_{DS} = - 25 V, f = 1 MHz	P-Ch	-	384	480	
Output Capacitance	C_{oss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch	-	96	120	
		V_{GS} = 0 V	V_{DS} = - 25 V, f = 1 MHz	P-Ch	-	100	125	
Reverse Transfer Capacitance	C_{rss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch	-	36	45	
		V_{GS} = 0 V	V_{DS} = - 25 V, f = 1 MHz	P-Ch	-	56	70	
Total Gate Charge	Q_g	V_{GS} = 10 V	V_{DS} = 15 V, I_D = 3.9 A	N-Ch	-	8.7	13	nC
		V_{GS} = - 10 V	V_{DS} = - 15 V, I_D = - 2.5 A	P-Ch	-	9.7	15	
Gate-Source Charge	Q_{gs}	V_{GS} = 10 V	V_{DS} = 15 V, I_D = 3.9 A	N-Ch	-	1.9	-	
		V_{GS} = - 10 V	V_{DS} = - 15 V, I_D = - 2.5 A	P-Ch	-	1.8	-	
Gate-Drain Charge[c]	Q_{gd}	V_{GS} = 10 V	V_{DS} = 15 V, I_D = 3.9 A	N-Ch	-	1.6	-	
		V_{GS} = - 10 V	V_{DS} = - 15 V, I_D = - 2.5 A	P-Ch	-	2.3	-	
Gate Resistance	R_g	f = 1 MHz		N-Ch	1.4	-	4.2	Ω
				P-Ch	3.7	-	11	

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SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 15 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch	-	7	11	ns
		V_{DD} = - 15 V, R_L = 15 Ω $I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 1 Ω	P-Ch	-	7	11	
Rise Time	t_r	V_{DD} = 15 V, R_L = 15 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch	-	10	15	
		V_{DD} = - 15 V, R_L = 15 Ω $I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 1 Ω	P-Ch	-	9	14	
Turn-Off Delay Time	$t_{d(off)}$	V_{DD} = 15 V, R_L = 15 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch	-	14	21	
		V_{DD} = - 15 V, R_L = 15 Ω $I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 1 Ω	P-Ch	-	17	26	
Fall Time	t_f	V_{DD} = 15 V, R_L = 15 Ω $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch	-	7	11	
		V_{DD} = - 15 V, R_L = 15 Ω $I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 1 Ω	P-Ch	-	8	12	
Source-Drain Diode Ratings and Characteristics[b]							
Pulsed Current[a]	I_{SM}		N-Ch	-	-	22	A
			P-Ch	-	-	- 21	
Forward Voltage	V_{SD}	I_S = 2 A	N-Ch	-	0.8	1.2	V
		I_S = - 1.5 A	P-Ch	-	- 0.8	- 1.2	

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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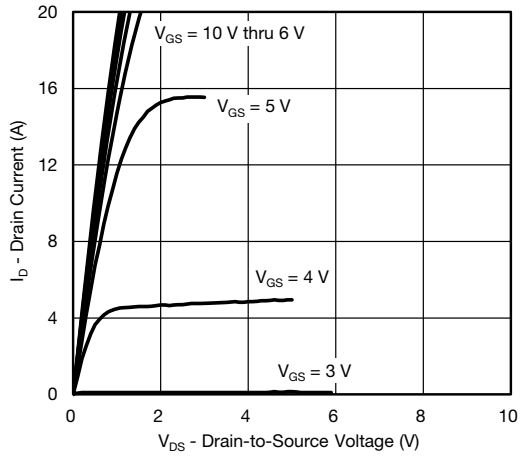
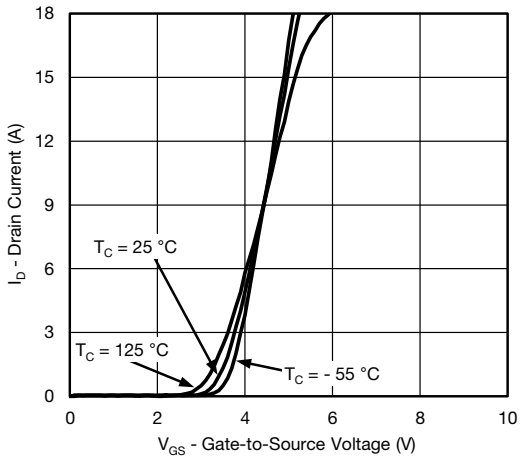
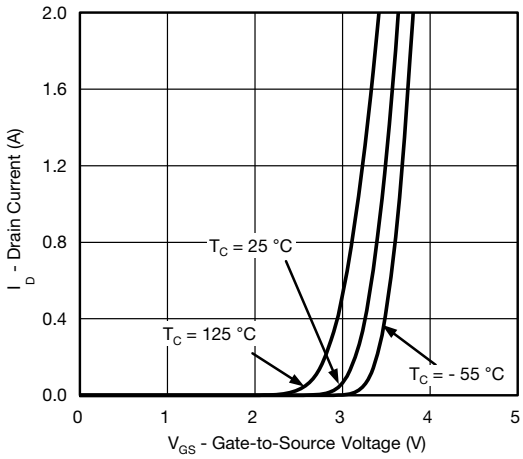
N-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



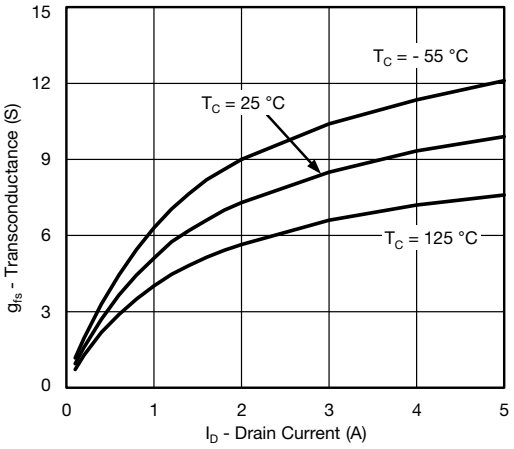
Output Characteristics



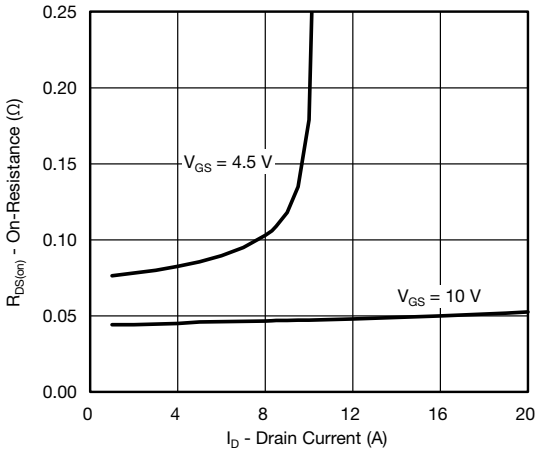
Transfer Characteristics



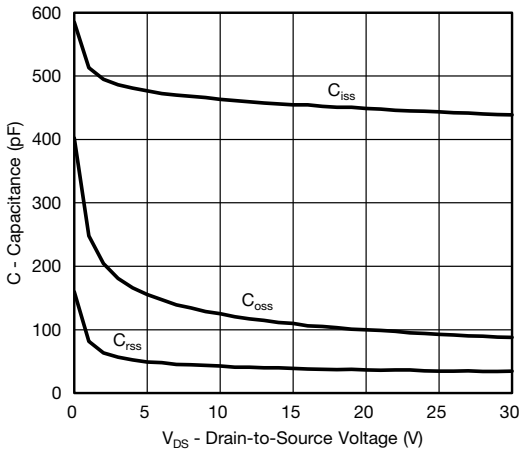
Transfer Characteristics



Transconductance



On-Resistance vs. Drain Current



Capacitance

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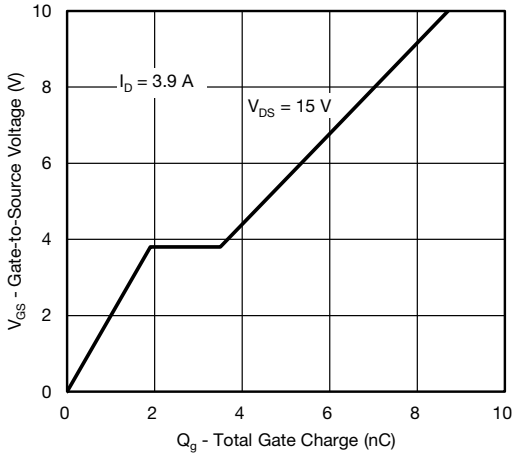
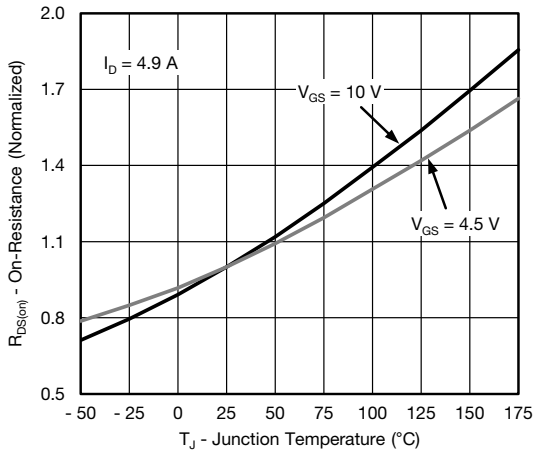
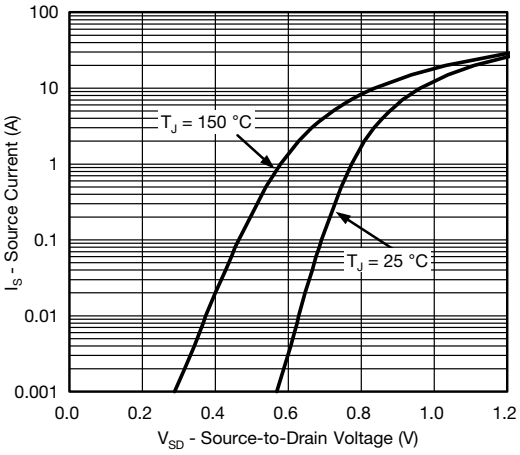


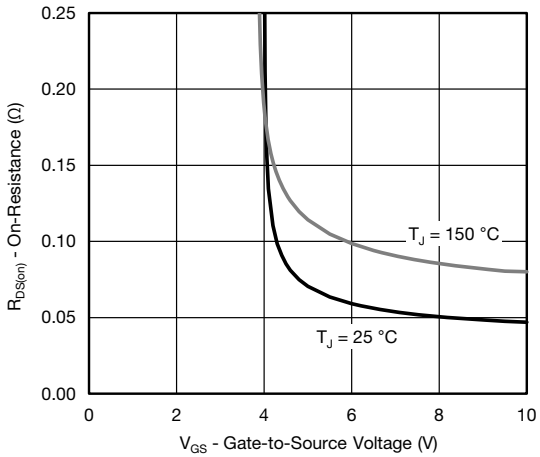
Gate Charge



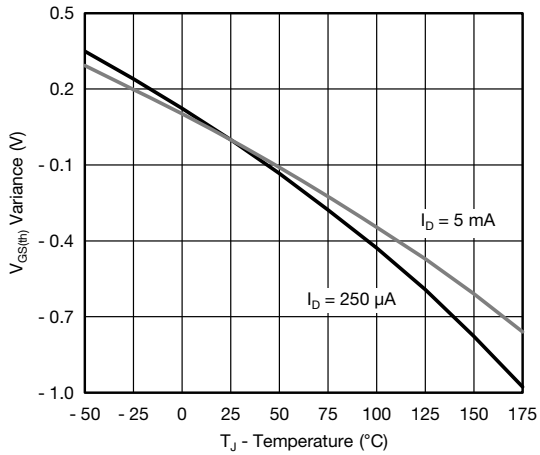
On-Resistance vs. Junction Temperature



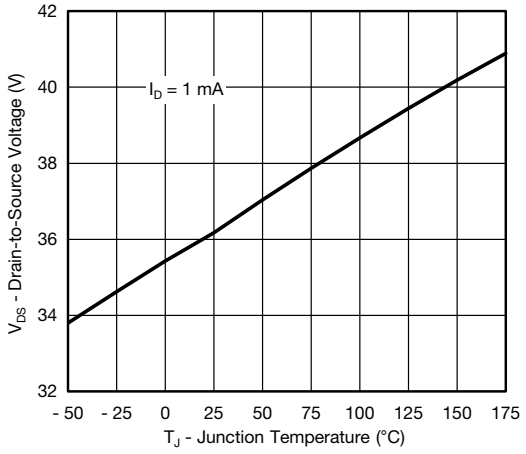
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

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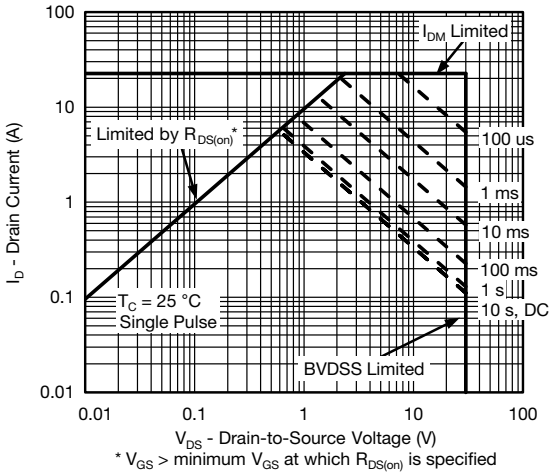
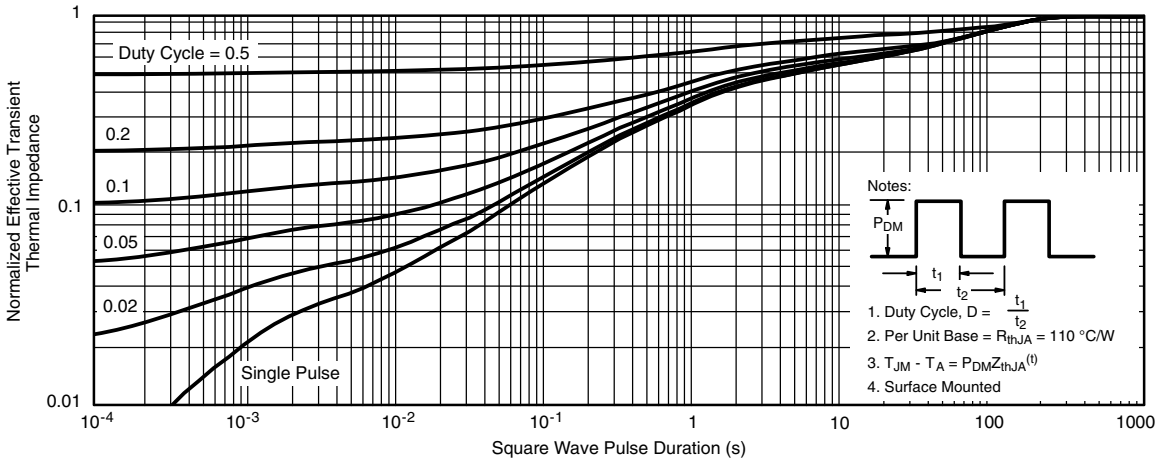
N-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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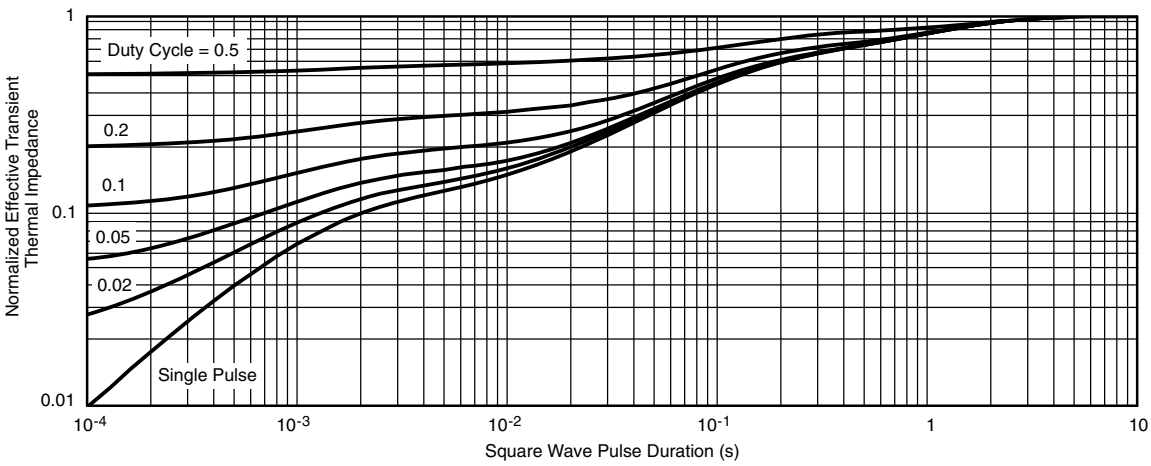

Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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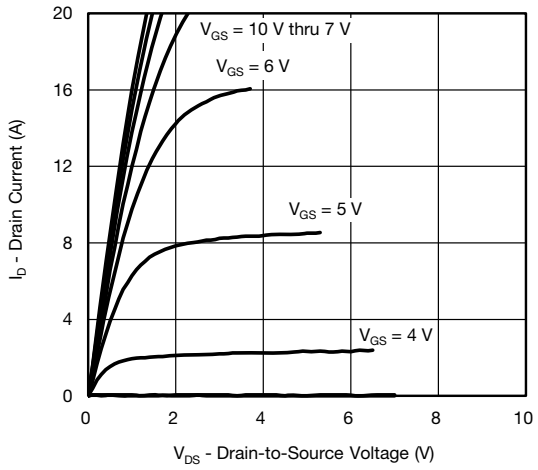
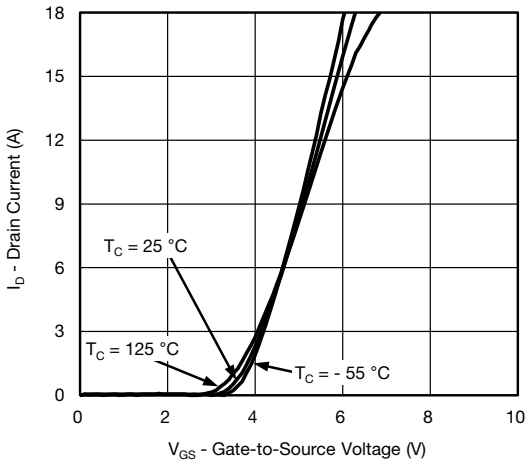
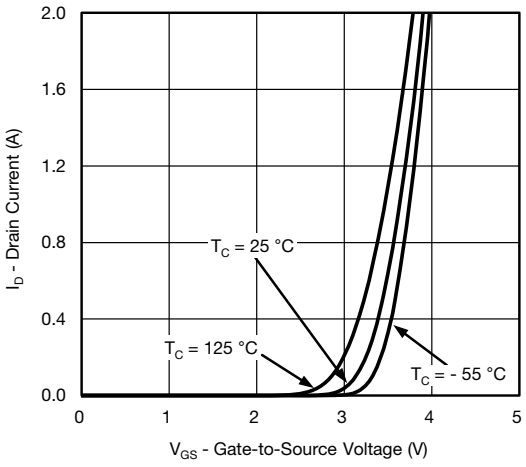
P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



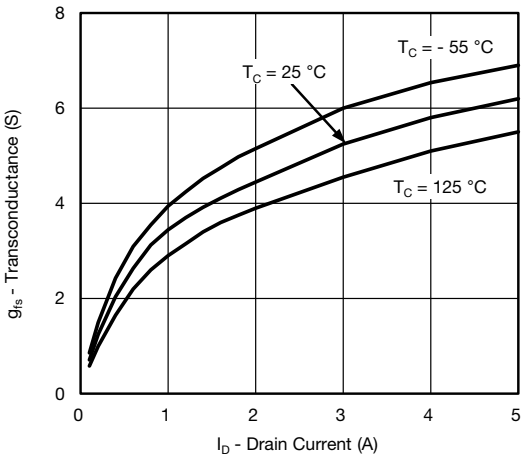
Output Characteristics



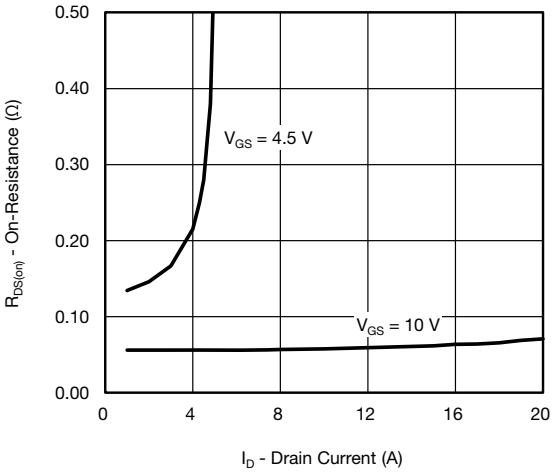
Transfer Characteristics



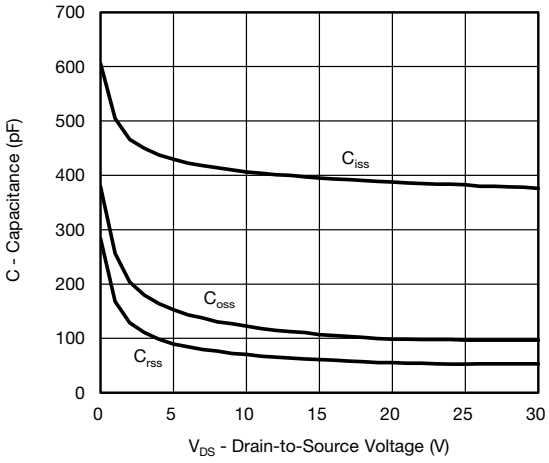
Transfer Characteristics



Transconductance



On-Resistance vs. Drain Current



Capacitance

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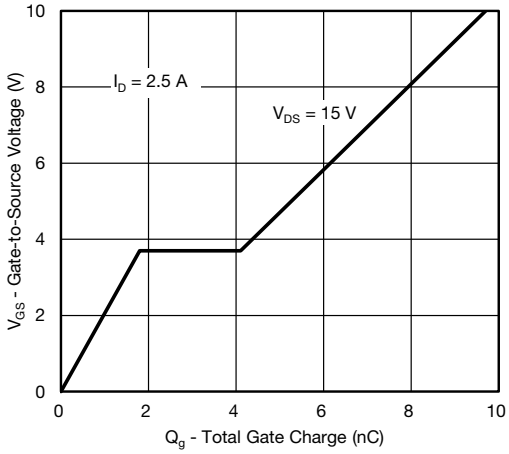
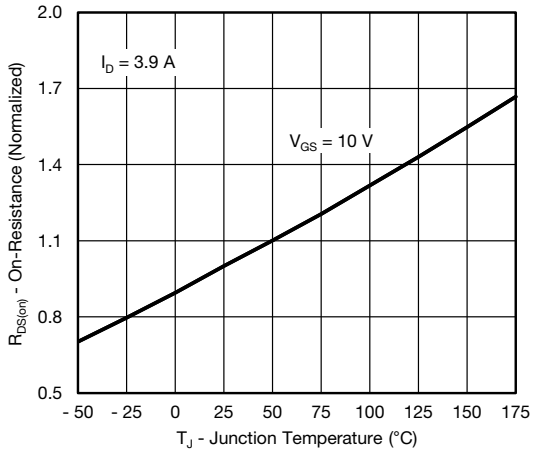
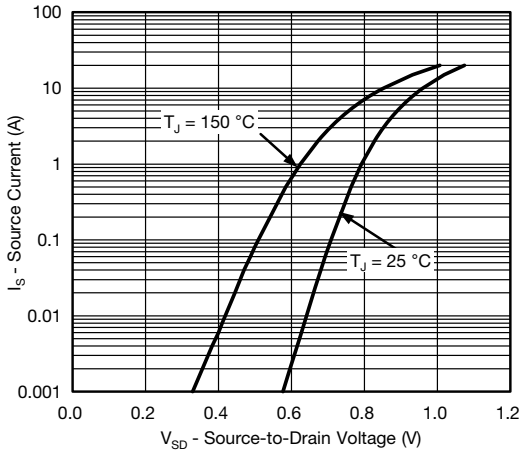


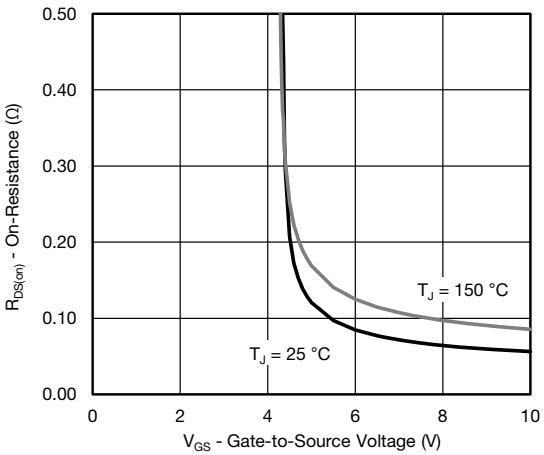
Gate Charge



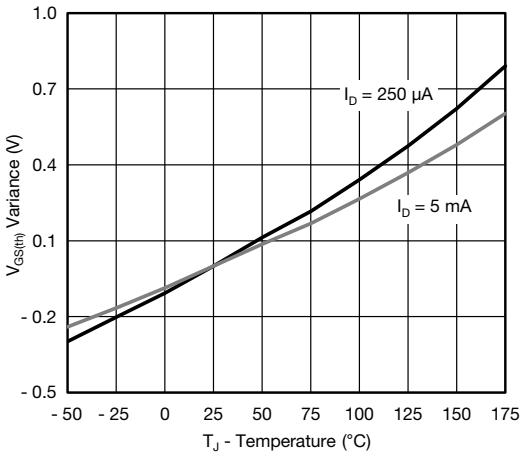
On-Resistance vs. Junction Temperature



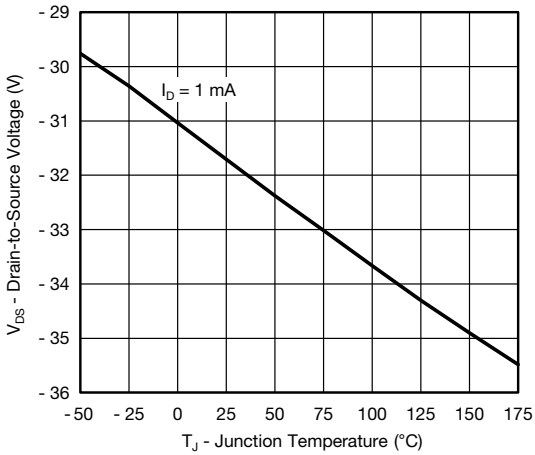
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

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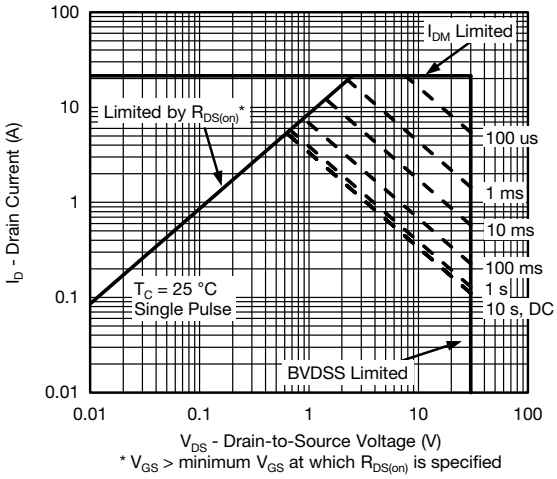
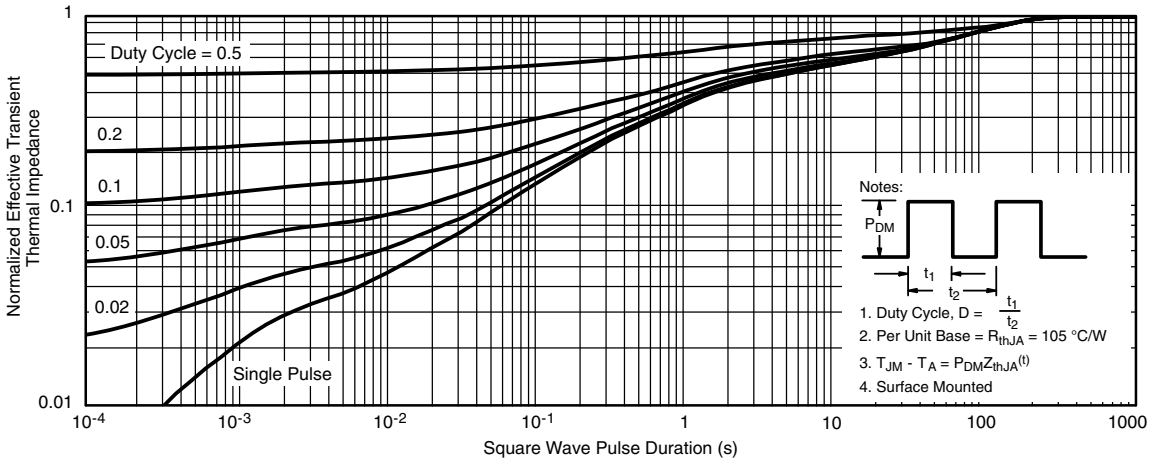


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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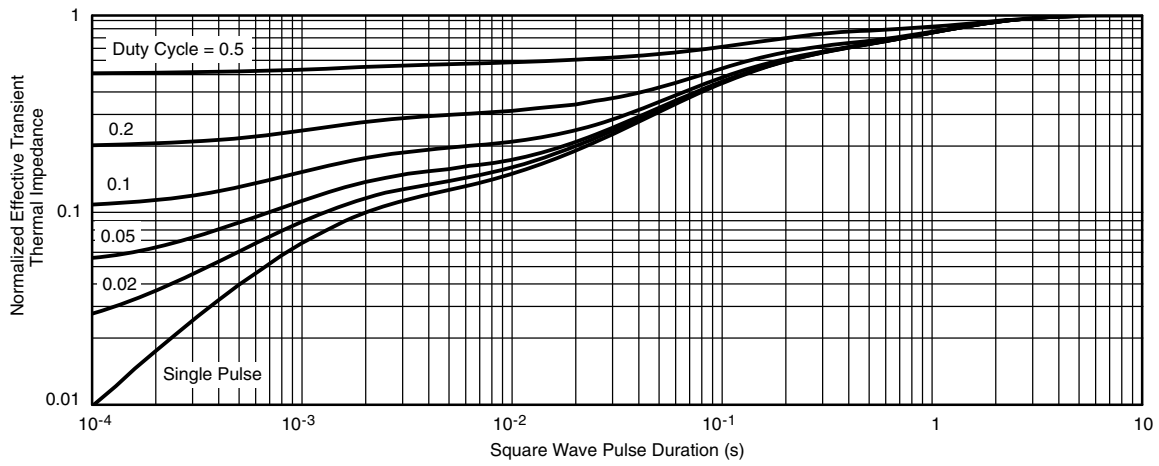
P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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