



# N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a, b</sup>	Q <sub>g</sub> (Typ.)		
30	0.026 at V <sub>GS</sub> = 10 V	8	4.2		
	0.032 at V <sub>GS</sub> = 4.5 V	8	4.2		

#### **FEATURES**

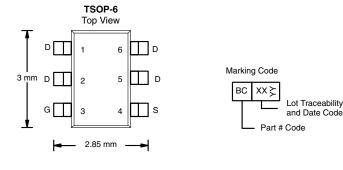
- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

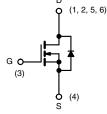


HALOGEN **FREE** 

#### **APPLICATIONS**

- · Load Switch for Portable Devices
- DC/DC Converters





Ordering Information: Si3424CDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	<b>S</b> (T <sub>A</sub> = 25 °C, un	less otherwi	se noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	]	
	T <sub>C</sub> = 25 °C		8 <sup>a</sup>		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	1 ,	7.7		
Continuous Diam Current (1) = 130 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	7.2 <sup>c, d</sup>	Α	
	T <sub>A</sub> = 70 °C		5.7 <sup>c, d</sup>		
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	20			
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	3	А	
Continuous Source-Diam Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.7 <sup>c, d</sup>	^	
	T <sub>C</sub> = 25 °C		3.6	W	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	2.3		
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	] ' D	2.0 <sup>c, d</sup>		
	T <sub>A</sub> = 70 °C		1.3 <sup>c, d</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>e</sup>	t ≤ 5 s	R <sub>thJA</sub>	50	62.5	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	28	35		

#### Notes:

- a. Package limited.
- b. Based on  $T_C = 25$  °C.
- c. Surface mounted on 1" x 1" FR4 board.
- d. t = 5 s.
- e. Maximum under steady state conditions is 110 °C/W.

# Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		28		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 3.7		mv/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtana Dvain Coverant	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	^	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Durin Course Co Clata Daristana a	D	$V_{GS} = 10 \text{ V}, I_D = 7.2 \text{ A}$		0.021	0.026	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$		0.026	0.032		
Forward Transconductance	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 7.2 \text{ A}$		17		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			405			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		92		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			42			
Tatal Cata Chayera	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{ A}$		8.3	12.5	nC	
Total Gate Charge				4.2	6.3		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 24 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 7.2 \text{ A}$		1.2			
Gate-Drain Charge	Q <sub>gd</sub>			1.6			
Gate Resistance	$R_{g}$	f = 1 MHz	0.6	3	6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			3	6		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 2.6 $\Omega$		12	20	-	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \approx 5.7 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		16	24		
Fall Time	t <sub>f</sub>	,		8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			10	20	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 2.6 $\Omega$		22	33	- - - -	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong 5.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		15	23		
Fall Time	t <sub>f</sub>	-		9	18		
<b>Drain-Source Body Diode Characteris</b>	tics						
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			3	Α	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				20	1 ^	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 5.7 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			13	20	nC	
Body Diode Reverse Recovery Charge Q <sub>r</sub>		L = 5.7 A dl/dt = 100 A/va		5	10		
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 5.7 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		8		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			5			

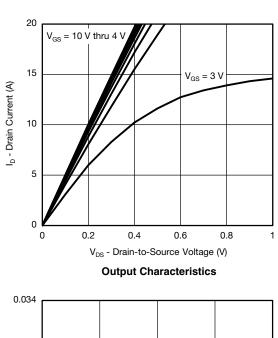
#### Notes:

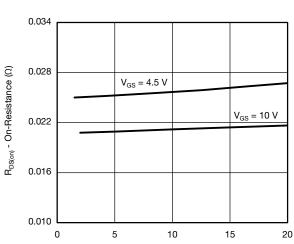
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

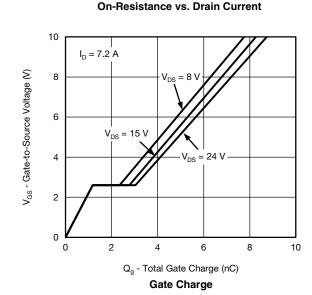


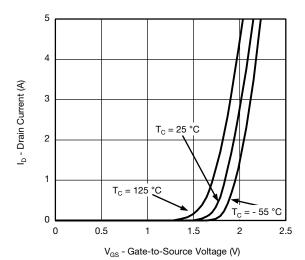
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



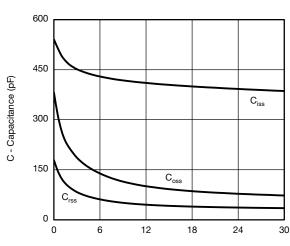


I<sub>D</sub> - Drain Current (A)

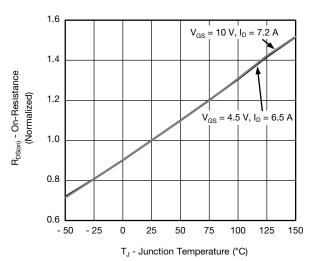




Transfer Characteristics Curves vs. Temp.



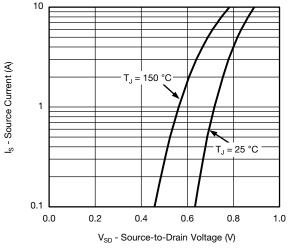
 $V_{DS}$  - Drain-to-Source Voltage (V) **Capacitance** 

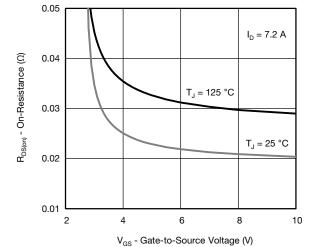


On-Resistance vs. Junction Temperature

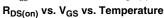
# Vishay Siliconix

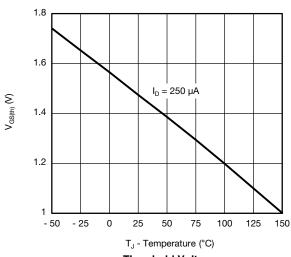
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

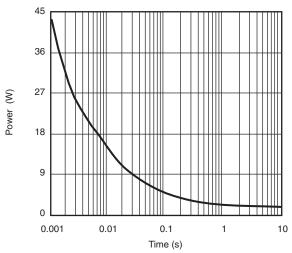




Source-Drain Diode Forward Voltage

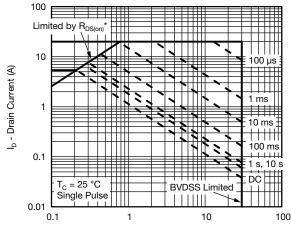






**Threshold Voltage** 

Single Pulse Power

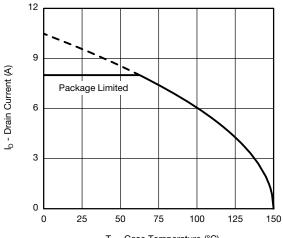


V<sub>DS</sub> - Drain-to-Source Voltage (V) \*  $V_{\text{GS}} > \text{minimum } V_{\text{GS}}$  at which  $R_{\text{DS(on)}}$  is specified

Safe Operating Area, Junction-to-Ambient

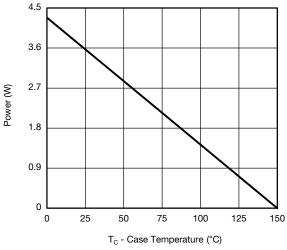


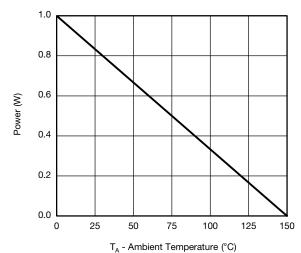
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



T<sub>C</sub> - Case Temperature (°C)

#### **Current Derating\***





Power Derating, Junction-to-Foot

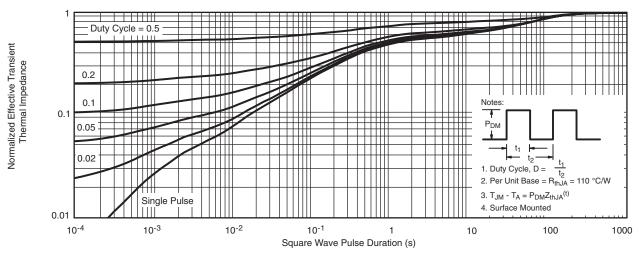
Power Derating, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

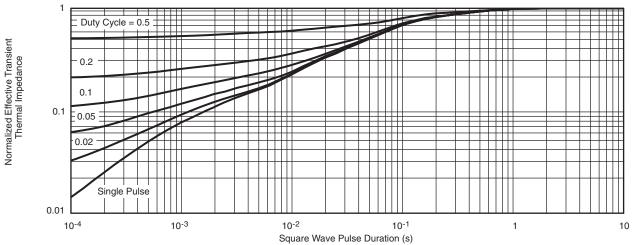
# Vishay Siliconix



## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

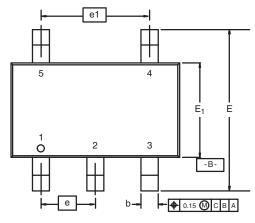
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?67443.

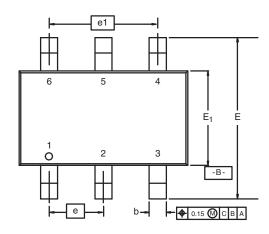




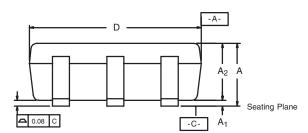
TSOP: 5/6-LEAD

**JEDEC Part Number: MO-193C** 

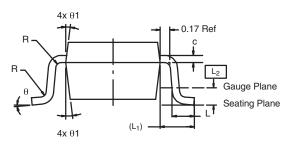




**5-LEAD TSOP** 





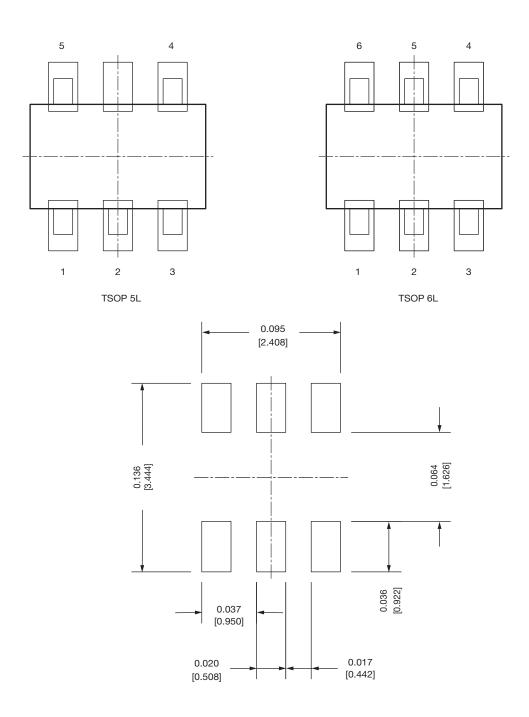


	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>	0.60 Ref			0.024 Ref			
L <sub>2</sub>	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 18-Dec-06



## Recommended Land Pattern For TSOP-5L / TSOP-6L



### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



## **Legal Disclaimer Notice**

Vishay

## **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.